The overarching goal of the NACHOS program is to demonstrate semiconductor lasers that have volume sizes no larger than the cubic of the wavelength in vacuum and can operate at room temperature under electrical injection in continuous-wave mode. Our team has successfully achieved this goal using the semiconductor-metal core-shell design. A secondary goal is to achieve an output power of 2 microwatts under the above operating conditions. We have so far achieved 20 microwatts at 260K and the power scaling indicates that this secondary goal is realizable in the next few months, well within the period of no-cost extension. In addition, many other novel device designs and...
Plasmonic Bowtie Antenna Nanolaser

ABSTRACT
The overarching goal of the NACHOS program is to demonstrate semiconductor lasers that have volume sizes no larger than the cubic of the wavelength in vacuum and can operate at room temperature under electrical injection in continuous-wave mode. Our team has successfully achieved this goal using the semiconductor-metal core-shell design. A secondary goal is to achieve an output power of 2 microwatts under the above operating conditions. We have so far achieved 20 microwatts at 260K and the power scaling indicates that this secondary goal is realizable in the next few months, well within the period of no-cost extension. In addition, many other novel device designs and fabrication approaches have been explored, showing great promises as alternatives to the core-shell design with other appealing features. The overall project consists of several individual tasks with progress detailed in the following pages.
Enter List of papers submitted or published that acknowledge ARO support from the start of the project to the date of this printing. List the papers, including journal references, in the following categories:

(a) Papers published in peer-reviewed journals (N/A for none)

<table>
<thead>
<tr>
<th>Received</th>
<th>Paper</th>
</tr>
</thead>
<tbody>
<tr>
<td>05/05/2014 26.00</td>
<td>Hanwei Gao, Anthony Fu, Sean C. Andrews, Peidong Yang. Cleaved-Coupled Nanowire Lasers, Proceedings National Academy of Sciences, (01 2013): 865. doi:</td>
</tr>
<tr>
<td>08/15/2012 21.00</td>
<td>C. Z. Ning, Debin Li. All-semiconductor active plasmonic system in mid-infrared wavelengths, Optics Express, (07 2011): 14594. doi: 10.1364/OE.19.014594</td>
</tr>
<tr>
<td>08/15/2012 20.00</td>
<td>D. B. Li, C. Z. Ning. Interplay of various loss mechanisms and ultimate size limit of a surface plasmon polariton semiconductor nanolaser, Optics Express, (07 2012): 16348. doi: 10.1364/OE.20.016348</td>
</tr>
</tbody>
</table>


TOTAL: 26
Number of Papers published in peer-reviewed journals:

(b) Papers published in non-peer-reviewed journals (N/A for none)

<table>
<thead>
<tr>
<th>Received</th>
<th>Paper</th>
</tr>
</thead>
</table>

TOTAL:

Number of Papers published in non peer-reviewed journals:

(c) Presentations

| Number of Presentations: 0.00 |

<table>
<thead>
<tr>
<th>Received</th>
<th>Paper</th>
</tr>
</thead>
</table>

TOTAL:

Number of Non Peer-Reviewed Conference Proceeding publications (other than abstracts):

<table>
<thead>
<tr>
<th>Received</th>
<th>Paper</th>
</tr>
</thead>
</table>

TOTAL:

Peer-Reviewed Conference Proceeding publications (other than abstracts):

<table>
<thead>
<tr>
<th>Received</th>
<th>Paper</th>
</tr>
</thead>
</table>

TOTAL:
Number of Peer-Reviewed Conference Proceeding publications (other than abstracts):

(d) Manuscripts

Received  Paper

TOTAL:

Number of Manuscripts:

Books

Received  Paper

TOTAL:

Patents Submitted

Patents Awarded

Awards

Prof. Peidong Yang
2013  Honorary Fellow, Chinese Chemical Society
2013  Honorary Professor, Griffith University
2013  Overseas Expert, Chinese Academy of Science.
2013  Honorary Professor, Jilin University
2013  Honorary Professor, Xiamen University
2013  ACS Inorganic Nanoscience Award
2012  Honorary Professor, Fudan University
2012  R&D 100 Award
2012  Elected as member of American Academy of Arts and Science
### Graduate Students

<table>
<thead>
<tr>
<th>NAME</th>
<th>PERCENT_SUPPORTED</th>
<th>Discipline</th>
</tr>
</thead>
<tbody>
<tr>
<td>Laklani, Amit</td>
<td>0.50</td>
<td></td>
</tr>
<tr>
<td>Lu, Chien Yao</td>
<td>0.50</td>
<td></td>
</tr>
<tr>
<td>Matsudaira, Akira</td>
<td>0.50</td>
<td></td>
</tr>
<tr>
<td>Ni, Chi Yu</td>
<td>0.25</td>
<td></td>
</tr>
<tr>
<td>Chen, Roger</td>
<td>0.30</td>
<td></td>
</tr>
<tr>
<td>Tran, Thai-Truong Du</td>
<td>0.50</td>
<td></td>
</tr>
<tr>
<td>Li, Kun</td>
<td>0.20</td>
<td></td>
</tr>
<tr>
<td>Shanker, Aamod</td>
<td>0.20</td>
<td></td>
</tr>
<tr>
<td><strong>FTE Equivalent:</strong></td>
<td><strong>2.95</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Total Number:</strong></td>
<td><strong>8</strong></td>
<td></td>
</tr>
</tbody>
</table>

### Names of Post Doctorates

<table>
<thead>
<tr>
<th>NAME</th>
<th>PERCENT_SUPPORTED</th>
</tr>
</thead>
<tbody>
<tr>
<td>Gao, Hanwei</td>
<td>1.00</td>
</tr>
<tr>
<td>Ding, Kang</td>
<td>1.00</td>
</tr>
<tr>
<td>Jin, Leijun</td>
<td>1.00</td>
</tr>
<tr>
<td>Prawiharjo, Jerry</td>
<td>0.30</td>
</tr>
<tr>
<td>Kim, Myungki</td>
<td>0.88</td>
</tr>
<tr>
<td><strong>FTE Equivalent:</strong></td>
<td><strong>4.18</strong></td>
</tr>
<tr>
<td><strong>Total Number:</strong></td>
<td><strong>5</strong></td>
</tr>
</tbody>
</table>

### Names of Faculty Supported

<table>
<thead>
<tr>
<th>NAME</th>
<th>PERCENT_SUPPORTED</th>
<th>National Academy Member</th>
</tr>
</thead>
<tbody>
<tr>
<td>Chuang, Shun Lien</td>
<td>0.15</td>
<td></td>
</tr>
<tr>
<td>Ning, Cun-Zheng</td>
<td>0.17</td>
<td></td>
</tr>
<tr>
<td>Wu, Ming C.</td>
<td>0.18</td>
<td></td>
</tr>
<tr>
<td><strong>FTE Equivalent:</strong></td>
<td><strong>0.50</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Total Number:</strong></td>
<td><strong>3</strong></td>
<td></td>
</tr>
</tbody>
</table>

### Names of Under Graduate students supported

<table>
<thead>
<tr>
<th>NAME</th>
<th>PERCENT_SUPPORTED</th>
<th>Discipline</th>
</tr>
</thead>
<tbody>
<tr>
<td>Wei, Eric</td>
<td>0.10</td>
<td></td>
</tr>
<tr>
<td><strong>FTE Equivalent:</strong></td>
<td><strong>0.10</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Total Number:</strong></td>
<td><strong>1</strong></td>
<td></td>
</tr>
</tbody>
</table>
**Student Metrics**
This section only applies to graduating undergraduates supported by this agreement in this reporting period

The number of undergraduates funded by this agreement who graduated during this period: ..... 0.00
The number of undergraduates funded by this agreement who graduated during this period with a degree in science, mathematics, engineering, or technology fields: ..... 0.00

The number of undergraduates funded by your agreement who graduated during this period and will continue to pursue a graduate or Ph.D. degree in science, mathematics, engineering, or technology fields: ..... 0.00

Number of graduating undergraduates who achieved a 3.5 GPA to 4.0 (4.0 max scale): ..... 0.00
Number of graduating undergraduates funded by a DoD funded Center of Excellence grant for Education, Research and Engineering: ..... 0.00

The number of undergraduates funded by your agreement who graduated during this period and intend to work for the Department of Defense: ..... 0.00
The number of undergraduates funded by your agreement who graduated during this period and will receive scholarships or fellowships for further studies in science, mathematics, engineering or technology fields: ..... 0.00

### Names of Personnel receiving masters degrees

<table>
<thead>
<tr>
<th>NAME</th>
<th>Total Number</th>
</tr>
</thead>
</table>

### Names of personnel receiving PHDs

<table>
<thead>
<tr>
<th>NAME</th>
<th>Total Number</th>
</tr>
</thead>
</table>

### Names of other research staff

<table>
<thead>
<tr>
<th>NAME</th>
<th>PERCENT SUPPORTED</th>
<th>FTE Equivalent</th>
<th>Total Number</th>
</tr>
</thead>
</table>

### Sub Contractors (DD882)

### Inventions (DD882)

### Scientific Progress

### Technology Transfer
See Attachment
**Final Report: Plasmonic Nanolasers**

**PI:** Peidong Yang (UC Berkeley)  
**Co-PIs:** Connie Chang-Hasnain (UC Berkeley)  
Shun-Lien Chuang (University of Illinois)  
Cun-Zheng Ning (Arizona State University)  
Ming Wu (UC Berkeley)

**Summary:** The overarching goal of the NACHOS program is to demonstrate semiconductor lasers that have volume sizes no larger than the cubic of the wavelength in vacuum and can operate at room temperature under electrical injection in continuous-wave mode. Our team has successfully achieved this goal using the semiconductor-metal core-shell design. A secondary goal is to achieve an output power of 2 microwatts under the above operating conditions. We have so far achieved 20 microwatts at 260K and the power scaling indicates that this secondary goal is realizable in the next few months, well within the period of no-cost extension. In addition, many other novel device designs and fabrication approaches have been explored, showing great promises as alternatives to the core-shell design with other appealing features. The overall project consists of several individual tasks with progress detailed in the following pages.
I. Widegap Semiconductor Nanowire Lasers (Peidong Yang)

I.1 Cleaved-Coupled Nanowire Lasers

As lasers are seeing a rapid increase in applications for digitized communication and signal processing, the monochromaticity of the lasing output becomes an important figure of merit. Laser emission at multiple frequencies can lead to both temporal pulse broadening and false signaling because of group velocity dispersion. These problems can be avoided by controlling the laser to oscillate at a single frequency. Single-mode lasing is obtained when the spectral spacing between the modes, the free spectral range, is larger than the bandwidth of the optical gain. Accordingly, because of their relatively broad gain profile (> 10 nm at room temperature), individual semiconductor nanowires lasing at a single optical frequency would need to be as short as a couple of microns. With low reflectivity of the end facets, such short nanowires are inefficient resonators, and may not have a reachable lasing threshold for the limited gain of the materials.

Here we demonstrate spectral manipulation of lasing modes by axially coupling two nanowire cavities through a nanoscale gap. Lasing operation at a single ultraviolet wavelength at room temperature was achieved in cleaved-coupled GaN nanowires with a total length of up to 9 μm. Besides the reduced number of lasing modes, the cleaved-coupled nanowires also operate with a lower threshold gain than that of the individual component nanowires. Good agreement was found between the measured lasing spectra and the predicted spectral modes obtained from simulated optical coupling properties. The correspondence between the theory and the experiment presents design principles for rational control over the lasing modes in cleaved-coupled nanowire lasers.

Cleaved-coupled nanowires were obtained by defining the optical cavities on single-crystalline GaN nanowires using focused ion beam milling (Figure I.1a). This fabrication technique provides precise control over two critical parameters for spectral manipulation of the lasing modes: cavity lengths and inter-cavity gap widths (as narrow as 30 nm). While multiple Fabry-Pérot modes are observed in the lasing spectra of the individual nanowires with lengths of 3.86 μm and 5.14 μm (Figure I.1b), single-frequency

Figure I.1. Single-frequency lasing in 9 μm (4:3) cleaved-coupled nanowires
lasing is generated from the 9 μm (4:3) cleaved-coupled nanowire cavity when the two short cavities are aligned axially with a 40 nm inter-cavity gap. (The notation x μm (y:z) describes a cleaved-coupled cavity with a total length of x μm and y:z is the ratio between the lengths of the two component nanowires.) In addition to the reduced number of lasing modes, the lasing transition occurs at a lower pump power in the cleaved-coupled nanowires (Figure 1.1c). These two advantages cannot be achieved simultaneously in single Fabry-Pérot nanowire cavities where a larger free spectral range produced in a shorter cavity is always accompanied by a higher threshold gain. The coupled nanowires exhibit stable single-mode operation over a large range of pump energy densities (Figure 1.1d).

The dimensions of the inter-cavity gaps are found critical to the optical coupling properties in axially coupled nanowires. Finite cross sections of the optical cavity induce severe diffraction losses at the inter-cavity gaps, especially for nanowires with diameters comparable to the effective wavelength. Calculated using three-dimensional full-wave finite-element methods, the transmittance of the nanowire-guided mode through the gap decreases monotonically with an increase in gap width. To maintain effective coupling, the gap width between coupled nanowires needs to be roughly within 30-100 nm where low optical loss and a moderate coupling constant can be obtained. Experimental measurements on cleaved-coupled nanowires with varying gap widths confirmed the predictions from simulations. While 9 μm (4:3) coupled nanowires with a 60-nm gap can produce single-frequency lasing, multimode operation is observed when the gap width was increased to 150 nm.

![Figure 1.2](image)

*Figure 1.2.* Lasing modes calculated based on transfer matrix methods.

The spectral modulation of modes observed in cleaved-coupled nanowire lasers can be predicted with good agreement using transfer matrix methods based on the calculated optical properties of the inter-cavity gaps. The calculated eigenvalues for 9 μm (4:3) coupled nanowires with a 60-nm gap show significant modulation of the threshold gain among different lasing modes (Figure 1.2, blue diamonds). Within the bandwidth of the GaN gain profile, only one mode is outstanding with the lowest threshold gain. In contrast, modes predicted for single nanowires have nearly equal threshold gains and multiple modes are expected in their lasing output. Moreover, for the modes of coupled cavities, both component nanowires provide the optical gain; therefore, the net gain required for coupled nanowires to reach lasing transitions is expected to be lower than that for each individual nanowire (Figure 1.2, red squares and green triangles), which agrees well with the experimental results (Figure 1.1c) and confirms the two benefits (i.e. lower threshold and fewer modes) of the coupled nanowire lasers.
The analytical model for mode calculations allows rational design of cleaved-coupled nanowires for the selection of desirable modes. Integer or fractional ratios between the lengths of the component nanowires make a significant difference in the free spectral range of the coupled nanowires. The lasing spectra confirm that 9 μm (1:1) and 9 μm (2:1) coupled nanowires show a modulation in the threshold gain that follows the resonances in the shorter cavities (Figure I.3). Alternatively, fractional ratios lead to modulation in the threshold gain at low-frequency spectral periodicities by a beating effect, which is therefore favorable for achieving single-frequency operation in cleaved-coupled nanowire lasers.

Our work offers a promising and practical route toward improved quality of lasing from semiconductor nanowires. Along with simple geometric designs and precise fabrication techniques, the excellent agreement between the theoretical predictions and the experimental observations allows reliable and rational control of the lasing modes in nanowires. While single-frequency lasing is demonstrated here in GaN nanowires, the principles for selecting modes using cleaved-coupled nanowire lasers should not be restricted to a specific material, operating wavelength, or pumping scheme. The idea of axial coupling can also be extended to coupled cavities containing more than two nanowires to achieve more complicated modulation of lasing modes. This compact, one-dimensional architecture is highly reproducible and can be suited for integration into chip-based photonic circuits. The highly monochromatic light from coupled nanowire lasers is anticipated to provide ultra-compact photon sources for laser-based remote sensing, optical data storage, and long-distance optical communication.

1.2 P-Type Transport in Li-doped ZnO Nanowires

Due to the wide band gap (3.4 eV) and large binding energy (~60 meV), ZnO is suitable for short wavelength photonic applications, such as ultraviolet lasers, light-emitting diodes, and solar cells. However, such high quality optoelectronic devices rely on reliable of both n- and p-type ZnO. Most ZnO is grown intrinsically n-type, thereby making p-type doping of extreme importance. However, low solubility of dopants, high defect ionization energies, and intrinsic donor defect
compensation, such as Zn interstitials (Z\textsubscript{i}) and O vacancies (V\textsubscript{o}), have provided difficulties towards this goal. In this study, Li was chosen as the dopant towards p-type behavior is ZnO. Unlike other larger cation dopants, the relatively small size of Li increases the allowable solubility. Additionally, the donor defect state of the substitution Li (Li\textsubscript{Zn}) lies \(-150\) meV from the valence band edge, which is lower than several other metallic dopants.\(^1\) Li-doped ZnO nanowires were synthesized via physical vapor deposition while in the presence of Li\textsubscript{3}N (Figure 1.2a). The amount of Li present in the ZnO growth zone was qualitatively controlled by adjusting the Li\textsubscript{3}N position relative to the growth zone: the closer the precursor to the growth zone, the higher the temperature (and vapor pressure), thereby introducing more Li for uptake into the ZnO. This takes advantage of the inherent temperature gradient of the tube furnace while not changing the growth condition of the ZnO nanowires (Figure 1.2b). Li incorporate into the ZnO nanowires was confirmed by a shift in the ZnO (100) diffraction peak (Figure 1.2c).

Compared with pure ZnO nanowires, lattice expansion is observed as a function of Li\textsubscript{3}N position, with the largest \(a\) parameter occurring at the 6 cm position. This expansion is attributed to the location of the Li dopants\(^2,3\), which predominately exist in the octahedral interstitial site (Li\textsubscript{i}) and as a Li\textsubscript{i}-Li\textsubscript{Zn} complex within the wurtzite lattice.\(^4\) The effect of the in situ CVT doping on electrical transport was investigated by single nanowire field-effect transistor and thermopower measurements (Figure 1.3a). The as-grown Li-doped ZnO nanowires exhibited high resistances but responded n-type, with the average mobility and electron concentration of 5.2 \(\pm\) 3.2 cm\(^2\)V\(^{-1}\)s\(^{-1}\) and between 10\(^{15}\) - 10\(^{16}\) cm\(^{-3}\), respectively. Seebeck coefficient (\(S_c\)) measurements confirmed that electrons were still the majority carrier. Because of the large amount of Li required to shift the lattice parameter (on the order of 2-6\%), a decrease of the mobility and increase in the \(S_c\) as compared to pure ZnO nanowires was expected. However, considering that Li\textsubscript{i} can act as donor, the reduced carrier concentration may be the consequence of the weak acceptor behavior of the Li\textsubscript{Zn}-Li\textsubscript{i} complex.\(^4\) To break the Li\textsubscript{Zn}-Li\textsubscript{i} bond and aid Li\textsubscript{Zn} formation, an on-device anneal (500 °C, O\textsubscript{2}, 1 hour) was performed on the Li-doped ZnO nanowire as a ‘dopant activation’ step. As seen in Figure 1.3b, the back-gated response of the device switches from n-type behavior to p-type behavior. The
now p-type ZnO nanowires exhibit both higher mobilities (10.4 ± 5.4 cm²V⁻¹s⁻¹) and carrier concentrations (~10¹⁶ - 10¹⁷ cm⁻³) then before the annealing. Additionally, the threshold voltage remains positive even after activation. Effectively, the "normally off" n-type channel was converted to a "normally on" p-type channel. The sign of the Seebeck also switches to positive voltages after the annealing step, providing further evidence that holes are the majority carrier (Figure I.3c). With further optimization of dopant incorporation and activation, increases in overall conductance can be achieved for optimal optoelectronic device performance.


II. Semiconductor-Metal Core-Shell Nanolasers and Their Size-Limit (Cun-Zheng Ning)

II.1 Room Temperature Operation with Record Performance

This task adopts the original core-shell approach [1], as was proposed at the beginning of the NACHOS program. Over the course of the program, the design and fabrication have been significantly improved and optimized. Various records have been achieved using this approach, including the first electrical injection lasers with a dimension below the diffraction limit, breaking the half-wavelength limit [2] for the first time. This design allowed the operation of the sub-wavelength laser to operate at a record high temperature at 260 K two years ago [3], and at room temperature recently [4]. One of the key issues with the room temperature operation is the relatively broad linewidth.
By improving the design and fabrication, we have [5,6] finally demonstrated in a more convincing manner a nanolaser under electrical injection with a sub-wavelength cavity volume of 0.67λ^3 operating in CW mode at room temperature, thus achieving one of the most important programmatic goals of NACHOS project. A more sophisticated power measurement has been carried out using the integration sphere, indicating the possibility of reaching 2 micron watts total output power. In addition, we studied nanolasers with circular cross section. Azimuthally polarized laser beam from small nanolaser supporting TE\textsubscript{01} mode is verified experimentally. Room temperature operation is achieved on some larger devices as well. Following are more detailed report describing the specific milestones achieved:

**Major Results:**

- **Record performance of CW room temperature operation under electrical injection:**
  
  Fig. II.1. shows the lasing characteristics of a nanolaser with physical cavity volume of 1.15 (W) × 1.39 (L) × 1.7 (H) μm^3 = 0.67 \lambda^3 (\lambda=1591 \text{ nm}).

  ![Figure II.1 A: L-I and FWHM-I curve at 294K under DC current injection of the laser. Black squares ■: integrated lasing mode intensity; Red solid circles ●: integrated spontaneous emission intensity. B: Spectra (offset for clarity) of this device at different currents.](image-url)

This device shows a clear threshold current at 1.1 mA. The linewidth drops continuously to 0.5 nm which is 1/6 of our previous room temperature result. Polarization resolved measurement shows the lasing emission is linearly polarized along device length direction, consistent with the FDTD simulation. So far this result is comparable or better than other room temperature pulse sub-wavelength lasers and approaching conventional RT CW semiconductor lasers.
**Power measurement at high temperature:** We performed power measurements on our sub-wavelength nanolasers at high temperature (>200 K) where the nanolasers show an CW output power on the order of tens of microwatts (see Fig. II.2). At room temperature, the output power is so low that beyond our measurement ability. Even at 260 K, the power of this device can be up to 20 µW. A current efficiency is defined as the slope of power-current curve. The drop of this efficiency represents the degradation of device performance as temperature increases.

![Figure II.2 The output power and current efficiency from a device with cavity volume of 0.88 µm (W) X 1.69 µm (L) X 1.70 µm (H) at DC voltage of 4V and injection current ~2mA. Measurements were conducted from 200 K to 260 K.](image)

**II.2 Circular Core-Shell Structure with Azimuthal Polarization**

Metallic cavity nanolasers with circular geometry also exhibited interesting polarization properties and can be explored for some interesting applications. Radially or azimuthally polarized laser beams have many interesting applications. But so far, such beams have been generated under optical pumping, using external optics, or fabricating gratings on top of a VCSEL. A compact nanoscale electrically driven laser source with azimuthally polarized beam has not been reported so far. Through our simulation, we found that in small circular nanolasers, single mode operation with specific operation can be achieved. Fig. II.3 shows our FDTD simulation result for small circular nanolasers showing the resonance wavelength dependence on

![Figure II.3 Resonance wavelengths of the three lowest order modes in a small circular laser with radius from 80 to 260 nm.](image)
device diameter. The mode spacing of the three lowest modes HE_{11}, TM_{01} and TE_{01} is so large that only one mode will fall within material gain bandwidth, thus enabling controllable selection of a single mode.

CW lasing under electrical injection is achieved on the device with semiconductor core diameter of 350 nm, corresponding to a total cavity volume of 0.146 λ^3. Its lasing characteristics are shown in Fig. II.4. This device supports TE_{01} mode. The output beam image after passing through a linear polarizer is shown in Fig. II.4(c). The azimuthal polarization predicted in our simulation is verified by the dark line along the polarizer direction in such images.

Circular devices also allow better performance with smaller overall volume. Some circular devices with somewhat larger diameters can achieve room temperature operation. Fig. II.5 shows the room temperature lasing characteristics of a circular nanolaser with radius 700 nm under pulse current injection.
II.3 Theoretical Modeling and Simulation: Ultimate Size Limit of a Plasmonic Nanolaser

During the course of this project, simulation and modeling was an integral part of the project execution and played an important role in predicting and analyzing the ultimate potential of device performance. One of the important results is the possibility of doing plasmonics without metals which are lossy and difficult to produce with high quality. Our analysis shows that InAs heterostructures is the best material choice and can potentially realize plasmon resonance in the near-infrared [10]. Furthermore, such all-semiconductor approach allows ready integration of active and passive components using pure epitaxial growth with minimal loss. Another prediction of our modeling is the ultimate size limit of a plasmonic nanolaser [9]. We find that there is an interesting interplay between the facet loss and the SPP propagation loss and that such interplay leads to the existence of a minimum-threshold mode in each mode group. The red-shift of the minimum-threshold mode with the decrease of device thickness leads to a further reduction of threshold gain, making the threshold for the SPP nanolaser achievable for many semiconductors, even at room temperature. In addition, we find that the threshold can be further reduced by using thinner metal cladding without much exacerbated mode leakage. Finally, a specific design example (see Fig. II.6) is optimized using Al$_{0.3}$Ga$_{0.7}$As/GaAs/Al$_{0.3}$Ga$_{0.7}$As single quantum well sandwiched between silver layers, which has a physical volume of $1.5 \times 10^{-4} \lambda^3$, potentially the smallest semiconductor nanolasers designed or demonstrated so far.

![Figure II.6](image)
References for Section II


III. Metal-Cavity Surface Emitting Nanolaser (Shun-Lien Chuang)

III.1 Development of 980 nm Metal-Cavity Surface-Emitting Micro/NanoLasers

To have a wider application range for metal-cavity lasers, we gradually move the spectra from 850 nm to 1550 nm. During the phase-II project, we have successfully applied our process techniques for 850 nm metal-cavity surface-emitting lasers to longer wavelength lasers (980 nm and 1550 nm). The experiments were initiated with conventional VCSEL samples with 37 DBR pairs and a hybrid mirror (composed of 23 DBR pairs plus silver) as shown in Fig. 1 [1]. Continuous-wave operation at room-temperature was demonstrated for a volume of 12 \( \lambda_0^3 \) at 980 nm with the power increasing to more than 8 \( \mu \)W, compared to our previous work.

We have designed a few 980 nm layered structures with reduced or only one-sided DBR as feedback mirrors. Several combinations are theoretically investigated with reduced volumes. For example, in Fig. III. 2, a design with a total number of 12 DBR pairs is analyzed. The volume is bounded to below 1 \( \lambda_0^3 \) to meet the Phase-III final goal. The analysis shows a promising result for achieving room temperature continuous-wave operation.

III.2 NanoLasers and NanoLEDs without DBRs

We have fabricated metal encapsulated nano-cavity light emitting diodes operating with electrical injection at room temperature. The active region of the device is bulk InGaAs with InP as the carrier injector. An etching stop layer of InGaAsP is inserted in between InP and substrate for substrate removal purposes. Figure III.3(a) and 3(b) show the schematics of the device which is flip-chip bonded to silicon substrate wafer. The metal-defined nano disk has a thickness of 200 nm and a radius of about 1 \( \mu \)m. The sidewall of disk is passivated with approximately 100 nm silicon nitride before metallization. This layer serves to passivate the sidewall, provides electrical isolation between the anode and cathode, and is an optical cladding layer between the active disk and metal. Silver is chosen to define the metal cavity for its smaller optical absorption compared
to other metals. The cavity volume encapsulated by silver is $\sim 0.76 \, \mu\text{m}^3$, which corresponds to only $0.19 \lambda_0^3$ where $\lambda_0$ is the free space emitted wavelength.

![Figure III. 3. (a) Schematic of metal encapsulated nano-cavity light emitter. (b) Detailed structure of active cavity region. SEM images of processed device after (c) thin SiNx passivation on sidewall and floor, (d) thick silver deposition on device top and sidewall, and (e) finished sample viewing from the top. [2]](image)

Scanning electron micrographs (SEM) of critical steps in processing the device are shown in Fig. III. 3(c), 3(d), and 3(e). After the p-i-n junction disk is defined by etching processes, silicon nitride is uniformly covered for passivation and electrical insulation. Then, only the top side of the silicon nitride is selectively removed for electrical injection (Fig. III. 3c). After a thick silver layer is deposited on the top and side (Fig. III. 3d), the device is flip-bonded on a silicon substrate with a gold electrode pad. Figure III.3(e) shows finished sample from the top view after substrate removal and deposition of a thin silver contact layer (40 nm in this work), which serves as the anode contact. By varying the top sliver thickness, it is possible to control the optical confinement factor $Q$.

Figure III. 4(a) shows cross sectional electrical field simulated by the finite-difference time-domain (FDTD) method. The simulation result shows the fundamental HE_{111} mode with a quality factor $Q$ of 196 at the resonant wavelength. Fig.III. 4(b) shows the voltage vs. current (V-I) and light output vs. current (L-I) curves at two different temperatures. The V-I curve indicates good isolation between the anode and cathode by the thin 100 nm silicon nitride layer, showing a typical diode threshold voltage at 1.06 V. The two L-I curves show a decreasing slope efficiency with increasing temperature. At 15°C and 28 °C, the slope efficiency is 0.31 and 0.27 mW/A respectively. We have successfully overcome the processing challenges of forming an electrical injection device for a metallic nano cavity with a heterojunction thickness of 200 nm. Electroluminescence (EL) spectrums of a processed device and the photoluminescence (PL) spectrum of the epiwafer measured at room temperature (28 °C) are shown in Fig. 4(c). Despite no lasing activity, the EL spectrum from the device shows no degradation during the process compared to pre-processing PL data.
Figure III.4. (a) A cross-sectional FDTD simulated field profile of the device showing the fundamental HE_{111} mode. (b) The light output and voltage vs. current curves showing clear diode turn-on behaviour. (c) Electroluminescence and photoluminescence spectrum at room temperature.

We have also designed a larger cavity volume metal encapsulated device for higher quality factor in an attempt to fabricate a lasing device. Figure III. 5(a) shows the device schematic and FDTD simulation for the fundamental HE_{116} mode for this design. The quality factor of this design is around 700, significantly larger than the previous thin design. Figure III. 5(b) is an electrical injection, CW, optical spectrum at room temperature. The cavity dimensions are; radius = 1 μm, height = 1.599 μm, cavity volume = 1.23 λ₀³. Even though the cavity volume is larger than the previous device, room temperature spectrum narrowing was observed where the output power increased to slightly over 1 μW.

Figure III. 5. (a) Schematic and FDTD simulation for metal-cavity, electrical injection nano emitter with a total p-i-n junction thickness of 1599 nm. (b) Electrical injection optical spectrum compared with epiwafer PL data measured at room temperature showing spectrum narrowing. [2]

III.3 Theory on the design rules and optimization of a nano-coin laser [3]
We have formulated and established the fundamental frame work for the theory of metal-cavity nanolasers [3]. During the last year, we applied our theory to new structure designs such as nano-coin lasers and nanoLEDs. Several design optimizations are proposed and verified by full-structure numerical analyses. To further simplify and generalize the design rule, a Fabry-Perot analogy of nanolaser is summarized and used to design our DBR-free metal-cavity nanolasers. The simplified FP model of the HE11 mode includes all the necessary parameters for the design and predicts the performance with a very good agreement with the full-structure FDTD and FEM methods. Figure 6 shows the calculated result from the FP model of a nano-coin structure with p-InP (40 nm)/i-InGaAs (160 nm)/ n-InP(40 nm). The design agrees very well with the prediction of FDTD. We are making progress toward fabricating the nano-coin lasers.
III.4 Cavity Volume Scaling Law of Quantum-Dot Metal-Cavity Surface-Emitting Microlasers

Quantum-dot (QD) metal-cavity surface-emitting microlasers were designed, fabricated, and characterized recently [4] for various sizes of cavity volume for both lateral and vertical confinements. Microlasers using submonolayer quantum dots in the active region are fabricated according to our design model optimized for a resonant metal cavity. The cavity-volume scaling law is studied by our theoretical modeling and experimental demonstration as shown in Fig. III.7. The smallest laser has a diameter of 1-μm with silver metal cladding operating at room temperature with electrical injection in pulsed mode. Our experimental results show significant self-heating effect in the smaller devices with a diameter of a few micrometers due to high series resistance and high threshold gain. With the use of hybrid metal-DBR mirrors, the number of DBR pairs in the top hybrid mirror can be reduced from 19.5 to 5.5 without sacrificing threshold current density.

References for Section III:
IV. Monolithic Nanolasers on Silicon (Connie Chang-Hasnain)

IV.1 Introduction
Augmenting electronics with photonics is expected to enable new capabilities in myriad fields including computing, communications, sensing, and medical diagnostics. Active photonic devices typically employ III-V materials because of their superior optical properties. Meanwhile, the foundation of electronics is silicon (Si), an optically-deficient semiconductor. Physically merging these two materials therefore provides a natural pathway towards creating optoelectronic functionality. However, prominent dissimilarities in basic material properties like lattice constant have severely inhibited such integration. Furthermore, the nanoscale nature of modern transistors demands complementary nanophotonics that approach or even break subwavelength limits of light. Here, we leverage a unique CMOS-compatible growth process that populates silicon wafers with single-crystal GaAs-based nanopillars. With these nanostructures, we have realized monolithic nanolasers on silicon under optical pumping. Under electrical injection, nanopillars exhibit amplified spontaneous emission, representing a major step towards the first silicon-based diode nanolasers.

IV.2. Material growth and device fabrication
GaAs-based nanopillars were grown without catalysts on (111) silicon using metal-organic chemical vapor deposition (MOCVD) at a low temperature of 400 °C, thereby achieving compatibility with strict CMOS foundry processes. This provides a critical advantage for scalability of nano-optoelectronics. By controlling doping and composition during core-shell growth, we created radial p-i-n heterostructures as schematized in Fig. IV.1(a). Figure IV.1(b) shows a scanning electron microscope (SEM) image of the as-grown nanomaterials. Typically, the core of the nanopillar is nominally n-type In$_{0.12}$Ga$_{0.88}$As followed by an intrinsic In$_{0.3}$Ga$_{0.7}$As active layer. The cladding consists of thin layers of i-GaAs and p-GaAs, which provide surface passivation and the device p-contact layer, respectively. Additional details on growth can be found in ref. [1–3]

To fabricate nanopillar devices, a series of lithography, etching, and deposition steps were

![Fig. IV.1.](image)

(a) Schematic of a III-V nanopillar on silicon with radial p-i-n heterojunctions. Core-shell growth and control of doping and composition enable these structures. (b) SEM images of as-grown nanopillars prior to processing. Various etching, lithography, and deposition techniques were used to fabricate nanopillar devices. Schematics and SEM images are respectively shown in (c) and (d) for nanopillar laser structures and in (e) and (f) for APDs. SiO$_2$ acts as electrical insulation between the p- and n-type layers. Two top-side test pads were used for high-speed APD testing.
used. Both electron beam and standard microfabrication techniques were employed. When fully encapsulated in carefully designed layers of SiO₂ and Au, nanopillars become metal-optic resonators as shown in Figs. IV.1(c) and 1(d). If an optical window is opened as shown in Figs.IV.1(e), nanopillar devices can function as both light-emitting diodes and avalanche photodiodes (APDs), depending on the polarity of electrical bias. Thus, nanopillars offer a great deal of flexibility for applications; simple modifications to the fabrication process can result in numerous nanophotonic device permutations.

**IV.3 Lasers and LEDs on silicon**

Because nanopillars are extremely well-faceted, they form natural on-chip optical cavities immediately after growth without any additional processing. Under optical pumping, nanopillars on silicon reach full laser oscillation up to room temperature and with subwavelength dimensions [4]. Relatively broadband spontaneous emission is seen from nanopillars below threshold. As the pump fluence increases, band filling effects are seen until a cavity peak emerges and full laser oscillation is achieved as shown in Fig. IV.2(a). Strong coherence of nanopillar emission can be seen from a high suppression ratio of 17 dB in the lasing spectra as well as prominent speckles in near field images.

Achieving sufficient optical feedback is not intuitive given that the back facet, i.e. the nanopillar and Si interface, sees hardly any index contrast. Conventional thinking suggests that there would be insufficient reflectivity as well as severe absorption loss due to the smaller Si band gap. However, we find that optical fields can be tightly confined in nanopillars by helically propagating modes that are cut off and reflected at the interface with silicon [4]. Light output as a function of pump fluence (L-L curve) is shown in a Fig. 2(b), showing a distinct peak at 22 µJ/cm². Fitting the L-L curve reveals β=0.01 and experimental Q~200. Theoretical studies have shown that these modes can have Q factors reaching several thousand even for subwavelength structures [4].

![Figure IV.2](image-url)

Fig. IV. 2. (a) Nanopillar emission spectra are shown at various pumping levels ($0.1P_\text{th} - 1.8P_\text{th}$). Evolution of the lasing peak can be clearly seen. In the insets, optical images show the appearance of speckle patterns due to optical coherence when the laser is biased above threshold. (b) The pump-dependence of nanopillar light output reveals a clear lasing threshold of 22 µJ/cm². From analysis of this curve and associated spectra, we calculated the quality and spontaneous emission coupling factors as $Q$=200 and $\beta$=0.01.

True nano-optoelectronic functionality requires electrical control of nanophotonic devices. When electrically contacting nanopillars, the presence of metal severely perturbs cavity modes of such small structures. Thus, a great deal of care is needed in designing an electrical injection
structure that simultaneously supports optical resonances. Here, metal-optics provides an attractive feedback mechanism for small lasers because it offers small mode volumes [5–7]. In this case, metal contacts can then conveniently double as optical reflectors. By embedding nanopillars in a metal-optic cavity (see Figs. IV.1(c) and 1(d)), we have now achieved amplified spontaneous emission driven by electrical bias. To test the devices, samples were wire bonded onto chip carriers mounted inside a liquid helium cryostat with optical access. Electrical feedthroughs connected devices to a pulse generator in the external environment. 20 ns pump pulses with a 10 µs period were delivered in these experiments. An objective collected electroluminescence (EL), which was then relayed to a spectrometer and CCD for detection.

Cavity peaks can be seen superimposed on a broad spontaneous emission background under high injection levels (up to 700 µA) as shown in Fig. IV. 3(a). In order to ascertain the origin of the EL peaks, temperature dependence studies were carried out. The shift versus temperature of the purported cavity peaks and the overall EL spectrum are plotted in Fig. IV.3(b). Notably, peaks A, B, C, and D all shift at the same rate, while the overall EL spectrum (X) shifts more strongly. This can be explained by the stronger temperature dependence of band gap energy compared to refractive index. When plotting the wavelength shifts as percentages as in Fig. IV. 3(c), we can compare our results to well-known and existing models. We find that the ASE peaks shift at a rate corresponding to a typical GaAs thermo-optic coefficient of \( \frac{dn}{dT}=2.67\times10^{-4} \), thereby substantiating their origin from cavity modes since optical resonances are linearly dependent on refractive index. Meanwhile, peak X follows the Varshni model known for describing the temperature dependence of semiconductor band gaps. We note that there is a deviation from the Varshni model at higher temperatures, which is a topic of future investigation. From other analyses, we found that we currently achieve a material gain of 3,400 cm\(^{-1}\). This agrees with the exceptional electrical properties we have observed. Device dark current is only 1.2 pA and 0.45 nA at 0 V and -1 V, respectively. Nanopillar devices have textbook IV characteristics with nearly-ideal
ideality factors of 2. A material gain of 3,400 cm\(^{-1}\) is sufficient for reaching typical lasing thresholds; thus, our structure is presently limited by insufficient optical feedback or \(Q\). Nonetheless, the observation of ASE from nanopillars grown on silicon is a major step towards realizing the ultimate monolithic silicon-based light source.

**IV.4 Outlook**

As computing power continues to centralize in the form of data centers, chip-level data transport capacity becomes increasingly critical. Using silicon nanophotonics to implement low-power optical interconnects is a promising approach to solving impending bandwidth limitations. To offer advantages over electrical interconnects, optical devices must be extremely efficient, and their integration onto Si has to be highly scalable. Nanolasers such as the ones presented above therefore offer an extremely attractive solution. With subwavelength physical volumes, nanolasers minimize power consumption while still achieving strong monochromatic emission, though sufficient power and beam control remain areas requiring further development. On a higher level, the variety of nanolasers achieved by our team has created a class of photonic devices opening new functionality beyond the scope of traditional lasers. Controllably interfacing photons with nanoscale matter such as atoms, molecules, nanoparticles, and fullerenes is no longer a fantasy, but instead a reality on the horizon.

**References for Section IV**


Addendum-2013-Part I

NACHOS Report: Plasmonic Nanolasers  (CunZheng Ning)

1. Executive Summary

Metalllic and plasmonic based nanostructures promise to allow miniaturization of photonic devices down to unprecedented levels. The efforts reported here covers the electrical injection plasmonic-semiconductor nanolasers and plasmonic emitters. Major achievements involve the first realization of a nanolaser with size smaller than wavelength in vacuum in all three dimensions, operating at room temperature under electrical injection. The team remains the only group in the world capable of doing this almost a year after they achieved these critical milestones. Several other interesting devices were also demonstrated for the first time during the period of performances including the first electrical injection, direct generation of an azimuthally polarized laser light, and the first demonstration of electrical injection light emission from an integrated semiconductor-metallic bowtie structure. These demonstrations illustrate the great potential of plasmonic-semiconductor nanostructures in their versatility for many other applications.

The novelty of our research and significance of progress made were also evidenced by great attention our research received from technology and popular press, including interviews, news reports, and press highlights in many journals, magazine, and websites. Following pages contain more detailed description of progress made and the major results.

2. Background

Nanophotonics deals with generation, control, and manipulation of photons at nanometer scale with eventual goals of achieving photonic integrated systems on a chip and the final grand integration with electronics on a chip. To achieve such a system on a chip, it is critical to achieve various light sources and other active devices at nanometer scale on a chip. Surface-plasmon polariton (SPP) formation is one of the most important mechanisms to confine light at nanometer scale for detection, sensing, and communication, and thus has become one of the most critical elements for on-chip photonic integrated system. During the performance period, research was carried out to develop an electrical injection SPP hotwire sources and plasmonic nanolasers.

**Surface Plasmon Polariton and its generation**

With the rapid progress in nanoscale fabrication capability and in our understanding of the interaction between light field and nano-metallic structure, it has become possible to design and fabricate truly nanophotonic devices to confine optical field in an extremely small space dimensions, on the order of a small fraction of the wave-length of the corresponding dielectric medium. One of the very prominent examples of such capability is the first demonstration of a semiconductor nanolaser with a thickness below the diffraction limit, carried out by the PI’s group and his collaborators. However, the demonstrated nanoscale lasers could only operate at very low temperature. It is extremely important for many practical applications to be able to operate such nanolasers at room temperature. In addition, among the metallic structures proposed to confine light field or generate plasmonic near field sources, metallic bowtie pairs are shown to
be able to confine light in the smallest space and with the highest field enhancement between the bowtie tips. Though bowtie structures have been studied quite extensively, lack of a device concept and a method of fabrication to integrate bowtie structure with active materials in a reproducible manner and in a controllable way prevent these advantages from being incorporated into device applications. Moreover, the current prevailing method of generating such SPP modes is using optical pumping. The requirement of phase matching and a large external pump laser required makes optical generation intrinsically incompatible with the intended future on-chip applications. Thus the focus of our research in the area of plasmonic nanoemitters is to achieve room temperature operation of a nanolaser under electrical injection and on electrical injection operation of bowtie emitters as a potential plasmonic source.

3. Description of Major Research Results

3.1 First Room Temperature Electrical Injection Nanolaser finally Demonstrated: Achieving electrical injection nanolasers with a size smaller than wavelength in 3D capable of operating at room temperature has been a goal actively pursued worldwide over the laser few years. Due to the small size of such nanolasers, the fabrication precision required is extremely challenging to achieve high quality devices, capable of room temperature operating. We are the first group to finally achieve this long sought goal recently, as shown in Fig.1. So far nearly 9 months after the publication of our results, we are still the only group being able to do this. The results have been widely reported world-wide by tech press and news media in general. This achievement represents a major milestone in the development of metallic cavity nanolasers and in terms of possible real world application of such nanolasers. Currently similar research is actively pursued worldwide.

![Figure 1 Room temperature operating characteristics of a semiconductor metallic cavity nanolaser of subwavelength in size. Left: L-I curve (left axis) and linewidth vs. current (right axis) showing the standard threshold behavior of linewidth reduction with pumping current with the narrowest linewidth around 0.5 nm. This is the smallest linewidth demonstrated for a metallic cavity nanolaser. The L-I curve is plotted on log-log scale in the middle panel, showing the typical S-curve. The right panel shows the spectra at increasing pumping levels. All three panels show convincingly that the lasing has occurred in such a laser. The size of this laser: 1.15 (W) × 1.39 (L) × 1.5 (H) µm³ = 0.67 \( \lambda^3 \) (\( \lambda = 1591 \) nm).](image)

During the period of the performance, we have been leading the community over the last few years with either the smallest electrical injection lasers, or operating at the highest temperature. Various other interesting results were obtained also including the first purely electrical injection azimuthally polarized laser source. Our results have been published in several research papers and in various invited review articles as listed in the following:


### 3.2 Electrical injection metallic-semiconductor bowtie plasmonic emitters demonstrated:

Bowtie structure has been a prototype of a plasmonic emitter due to the strong enhanced field

![Figure 2: Left: schematic of the electrical injection metallic bowtie plasmonic emitter; Middle: schematic of the cross section (top) and simulated field distribution (bottom); Right panel: SEM image of semiconductor bowtie pillar (top) and L-I curve of experiment measurements of the light output from the side after cutting by a focused ion beam from the side.](image-url)
near the bowtie tips. But the effectiveness of such structure requires the active gain medium be positioned in between the bowtie tips. Such an arrangement represents a fabrication challenge. So far people have not been able to do this reliably by other bottom-up approaches. While carrying out our plasmonic nanolaser research, we realized that a one-step fabrication based on top-down lithography could allow the fabrication of the metallic bowtie structure and the integration with gain medium, potentially allowing such structures be fabricated using standard device fabrication tools and processes. Fig. 2 (right) shows a scanning electron microscope (SEM) image of a semiconductor pillar etched from an InP/InGaAsP wafer. The bowtie-shaped groves etched into semiconductor pillar will serve as a template for later deposition of metals (such as Au and Ag), so that metallic bowtie pairs will be formed in the middle of the pillar, where wafer growth defines semiconductor gain region between and around bowtie tips, creating a closely and precisely positioned and strongly coupled metal-semiconductor bowtie system, ideal for generating intense SPP near field. Since the InP wafer has built-in doped and contact layers, electrical injection can be readily accomplished, leading to a natural on-chip SPP generation without the need of bulk optical pumping. We have recently used the similar fabrication process to demonstrate the first ever sub-diffraction limit laser. The light emission has been measured from such structure as shown in Fig. 2 (low right). For more info, see, K. Ding, H. Wang, M. T. Hill, and C. Z. Ning, Design and fabrication of an electrical injection metallic bowtie plasmonic structure integrated with semiconductor gain medium, App. Phys. Lett., 102, 041110 (2013); doi: 10.1063/1.4775803

3.3 First demonstration of a nanolaser with azimuthal polarization: Light beams with various special polarizations have important applications such as polarized along radial or azimuthal directions. Typically such beams have to be generated by special gratings or by external polarization selection after devices been fabricated. For many applications, a single integrated device would be preferred. Our nanolasers with cylindrical symmetries (see Fig.3) are ideal for such polarized beam generation. We demonstrated for the first time an azimuthally polarized laser source from an electrically driven metallic cavity nanolaser with a physical cavity volume of 0.146 $\lambda^3$ ($\lambda = 1416$ nm). Single TE01 mode lasing at 78 K was achieved by taking the advantages of the large free spectral range in such nanoscale lasers and the azimuthal polarization of lasing emission was verified experimentally (see Fig.4). Mode shift controlled by device cavity radius was observed over a large wavelength range from 1.37 $\mu$m to 1.53 $\mu$m. Such metallic cavity nanolaser provides a compact electrically driven laser source for azimuthally polarized beam. Fig.3 shows the schematic laser structure and the SEM image of the fabricated pillar. Fig. 4 shows the L-I curve (a) and spectrum (inset of (a)), the lasing mode shift with pillar diameter, and polarization

Figure 3: (a) Structure of a metallic cavity nanolaser with circular cross section. (b) Scanning electron microscope image of an InP/InGaAs/InP nano-pillar coated with SiN layer on its sidewall. We etched away the SiN on top of the pillar and deposited Ti/Pt/Au thus forming the top n contact. Scale bar is 100 nm.
resolved imaging. The dark line in the images along the direction of the polarizer is the evidence of the azimuthal polarization.

4. Awards and Prizes

C.Z. Ning, Elected to Fellow of The Optical Society, 2012, due to significant contribution to nanophotonics

C.Z. Ning, Elected to be Fellow of IEEE, 2012, due to his significant contribution to nanophotonics

C.Z. Ning, OSA Fellow Lecturer Award, 2013

5. Invited Talks on the Research Results

1) C.Z. Ning, Kang Ding, Recent Progress in Metallic Cavity Nanolasers, Invited Talk at IEEE Photonics Society Summer Topical Meetings, Hawaii, July 2013

2) C.Z. Ning, Recent Progress in Metallic Cavity Nanolasers, invited talk at IEEE Photonics Society Annual Meeting (IPC), Seattle, WA, Sept 7-12, 2013


6) C.Z. Ning, Plasmonic Nanolasers: Towards the Ultimate Miniaturization, Invited Talk, Semiconductor and Integrated OptoElectronics (SIOE) Conference April 9-11th 2013, Cardiff,UK

7) C.Z. Ning, Loss Compensating and Lasing in Sub-wavelength Semiconductor-Metal Structures, Department of Physics, Imperial College London, UK, April 12, 2013

8) C.Z. Ning, Plasmonic and Metallic Cavity Semiconductor Nanolasers, HP Lab, April 4, 2013

9) C.Z. Ning, Semiconductor Nanowires for Photonic Applications from Ultraviolet to Mid-infrared, ASU Nano Science Seminar Series, April 1, 2013

of Synergetics, (on the occasion of Hermann Haken’s 85th birthday), Delmenhorst, Germany, Nov 13-16, 2012

11) C.Z. Ning, Nanophotonics with Nanowires and Plasmonics, Keynote talk, at The Second International Conference on Manipulation, Manufacturing and Measurement on the Nanoscale, 29 August – 1 September 2012, Xi’an, China

12) C.Z. Ning, Nanophotonics with Nanowires and Plasmonic Shells, Hong Kong Polytechnic University, May 22, 2012

13) C.Z. Ning, Photon-Plasmon Interactions and Subwavelength Nanolasers, Physics Colloquium, Shanghai Jiaotong University, May 30, 2012

14) C.Z. Ning, Two-Photon Lasers in Semiconductors, talk at Physics Department, Shanghai Jiaotong University, May 30, 2012

15) C.Z. Ning, Semiconductor Alloy Nanowires for Optoelectronic Applications from UV to IR, Seminar at School of Nanoscience and Technology, Suzhou University, Suzhou, China, June 4, 2012

16) C.Z. Ning, Semiconductor Alloy Nanowires and Plasmonic Nanostructures for Nanophotonics, Colloquium of Department of Optoelectronics, Zhejiang University, Hangzhou, China, June 7, 2012

17) C.Z. Ning, Photon-Plasmon Interactions and Subwavelength Nanolasers, Condensed Matter Seminar Series, Physics Department, Peking University, June 20, 2012

18) C.Z. Ning, Composition Graded II-VI and IV-VI Nanowires for Full-Spectrum Optoelectronic Applications from UV to Mid-IR, International Workshop on 6.1A II-VI and III-V Materials and their Integration, Tempe, AZ, Nov 2011

19) C.Z. Ning, Semiconductor Alloy Nanowires for Optoelectronics Applications from UV to Mid-Infrared, Material Science Seminar, University of Wisconsin Madison, Sept 29, 2011


23) Cun-Zheng Ning, Derek Caselli, Ding Kang, Debin Li, Zhicheng Liu, Patricia L. Nichols, Minghua Sun, Leijun Yin, Nanophotonics with Plasmonics and Nanowires: Applications to Subwavelength Lasers and Novel Solar Cells, Villa Conference on Interactions Among Nanostructures, April 21-25, 2011 Las Vegas, Nevada


25) C.Z. Ning, Alloy semiconductor nanowires for optoelectronic applications from UV to IR, March 16, 2011, Lecture, EECS Department, UC Berkeley

26) C.Z. Ning, Semiconductor alloy nanowires and applications to high efficiency solar cells, AZ Nanotech Council, Feb 24, 2011
28) C.Z. Ning, Approaching Size Limit of Nanolasers with Wires and Plasmonic Shells, ECE Colloquium, University of Illinois at Urbana Champaign, Oct 14, 2010

6. Papers published under the DARPA support:

Addendum-2013-Part II

Spectral Manipulation in Nanowire Lasers (Peidong Yang)

Nanowires have been identified as potential building blocks that mimic conventional photonic components such as interconnects, waveguides, and optical cavities at the nanoscale\textsuperscript{1-3}. Semiconductor nanowires with high optical gain offer promising solutions for lasers with small footprints and low power consumption\textsuperscript{4-5}. Although much effort has been directed toward controlling their size\textsuperscript{6}, shape\textsuperscript{7}, and composition\textsuperscript{8-9}, most nanowire lasers currently suffer from being low-quality cavities, a problem that is universal in nanocavities because miniature features induce significant optical loss. Because of this difficulty, it remains an outstanding challenge to rationally control the spectra of room-temperature nano-lasers. Our work focuses on the development of experimental and theoretical strategies to manipulate the lasing modes of a semiconductor nanowire while also improving the performance of the laser.

Previously, lasing operation at a controlled single UV wavelength at room temperature was achieved using a simple coupling scheme, the cleaved-coupled nanowire\textsuperscript{10}. The measured lasing spectra and laser threshold are in good agreement with the calculated spectral modes and threshold gain. This agreement between theory and experiment provides design principles for new methods to control nanowire cavities. The cleaved-coupled nanowire concept can be extended further by controllably etching an array of cuts at a specific period. This periodic structure can form a Distributed Bragg Reflector (DBR). A traditional laser cavity often uses mirrored surfaces at the ends of the cavity. DBRs provide an alternative approach to designing controlled reflection by layering areas of alternating high and low index of refraction. They can be fabricated by layering different materials, or grooves can be etched into a material at regular periods. By using focused-ion beam milling, controlled indentations can be etched into the nanowire to create stop bands with high reflectivity of the fundamental waveguide mode (Fig. 1).
Figure 1. Simulated transmission (T) and reflection (R) of DBR in nanowires. A DBR nanowire (green) lies on top of a silica (dark blue) substrate, backed by perfectly matched layers (red). A narrow photonic band near the wavelength of the GaN bandgap (370 nm) can be observed in both transmission and reflection.

Finite element methods (COMSOL 4.3 Multiphysics) were employed to simulate the optical properties of various DBR structures within a nanowire. In this simulation, the nanowire is 200 nm in radius with triangular cross sections and placed on a SiO$_2$ substrate, mimicking the actual experimental configuration. The fundamental mode (TM00) was injected from one end, partially reflected through the DBR, and partially transmitted to the other end of the nanowire. The loss (L) can be visualized as the field scattered into free space. The complex reflection and transmission coefficients were extracted by monitoring the electromagnetic wave propagating in the nanowire. The nanowire was surrounded by a cylindrical region (half glass and half air) and enclosed by a perfectly matched layer that absorbs all scattered electromagnetic waves propagating outward. Because each indentation does not cut through the nanowire, a large reflection coefficient (>80%) is observed since the scattering loss is suppressed. It should be noted that the reflection coefficient for the fundamental mode in a conventional single nanowire is ~20%. These results imply that the lasing threshold for a DBR nanowire can be an order of magnitude lower than the lasing threshold for a single nanowire without the DBR. These simulations also suggest that the photonic band gap can be tuned simply by changing the periodicity of the indentation. With these results in mind, DBR structures were fabricated in nanowires using focused-ion beam milling to control the periodicity and the etching depth.

Figure 2. Fabricated nanowire DBR structure and its corresponding spectral response. (a) SEM image of a gallium nitride nanowire with etched grooves at a 90-nm periodicity. (b) DBR stop bands across the visible spectrum.

To help protect the gallium nitride material, a ~50-nm tungsten metal protection layer is used to coat the nanowire before focused-ion beam milling, and hydrogen peroxide is used to remove the tungsten following the fabrication. The SEM image in Fig. 2 shows a DBR structure with 90-nm periodicity fabricated into a single gallium nitride nanowire. To measure the optical properties of
the DBR structures, a method to perform selected area spectroscopy was developed. One end of the nanowire is excited by a HeCd laser at 325 nm. The emission that passes through the DBR to the other end of the nanowire is collected by a microscope objective (Olympus 60x, N.A. 0.7, in an Olympus inverted optical microscope) and imaged onto a UV-visible spectroscopy spectrometer (Princeton Instruments/Acton) equipped with a 300-groove/mm grating blazed at 500 nm and a liquid N2-cooled charge-coupled device. The recorded spectra are from the light that passes through the DBR. As shown in Fig. 2, photonic stop bands across the entire visible spectrum were observed by adjusting the periodicity of the DBR from 250 nm to 80 nm, the periodicity used to match the band gap emission wavelength. The measured FWHM of the DBR can be less than 10 nm, which illustrates the uniformity and the control of this fabrication procedure. By examining the periodicity with the position of the stop band, the effective index of refraction across the entire visible spectrum for the nanowire can be extracted from the measurement. This method can be potentially applied to other material systems. This compact, one-dimensional architecture is highly reproducible and is anticipated to improve the lasing performance of conventional nanowire lasers.

References:
Addendum-2013-Part III

Nanopatch Laser (Ming C. Wu)

1. Introduction
To reduce the physical size as well as the mode volume of the nanolaser, we have developed a semiconductor nanopatch laser, in which the semiconductor gain media is sandwiched between a metallic nanopatch and a metal ground plane [1][2]. We have developed a versatile platform of realizing many different types of metallodielectric lasers. The fabrication methods developed can be extended for potential integration with CMOS. Both circular and rectangular nanopatch lasers were demonstrated (Fig. V.1).

![Diagram](image1.png)

Figure V.1. A schematic and SEM of (a) circular and (b) rectangular nanopatch cavities. Yellow regions represent gold and blue areas signify InGaAsP semiconductor gain media.

Because these lasers operate in near-infrared frequencies, metal behaves more ideally than at visible wavelengths so that plasmonic effects can be neglected and metals can be used in traditional roles (with lower conductivity). Using these metal-optic effects, lasing is achieved in the two modes of a cylindrical resonator, and lasing is achieved in rectangular nanopatch cavities. Physical laser volumes as low as $V_{\text{phys}}=0.75\left(\lambda_o/n\right)^3$ are achieved. We have also made plasmonic crystal laser where the plasmonic action played a key role in the lasing action [3].

2 Experimental Performance of Fabricated Nanopatch Laser
The nanolasers were fabricated using a process that can be compatible with integration onto silicon substrates for applications in optical interconnect technology. In summary, oxide and metal are evaporated onto the epitaxial layer, the substrate is then flipped upside down and bonded to another carrier, mechanical grinding and wet etching techniques are used to remove the backside, the sample is patterned using electron-beam lithography and liftoff, and finally
etched using reactive ion etching. Scanning electron micrographs (SEM) of nanopatch cavities can also be seen in Fig. V.1. More details of the fabrication process can be found in [1].

Lasing from multiple modes of nanopatch cavities is observed. For 220nm thick cavity, lasing is observed from TM_{111} and TE_{011} modes for cylindrical geometries and TM_{011} and TM_{101} modes for rectangular geometries. In cylindrical geometries, the TM_{111} mode is of great interest, since it represents a laser mode with sub-diffraction limited mode and physical volumes (V_n=0.067, V_{phys}=0.75). This so called “electric dipole” mode is also the most fundamental moderate quality factor mode in a cylindrical nanopatch laser. The quality factor is measured to be Q_{exp}=132. The TM_{111} mode has surface normal light emission, much like a VCSEL, and is linearly polarized due to its odd parity. The TE_{011} mode, also called the “magnetic dipole” mode since the electric field lines only have an azimuthal component, also lases. The magnetic mode has larger mode volumes and physical sizes (V_n=0.375, V_{phys}=1.25). The quality factor is measured to be Q_{exp}=168. Temperature dependent quality factors suggest that metal loss is reduced at low temperatures. Data taken from cylindrical nanopatch lasers is summarized in Fig. V.2.

![Figure V.2](image)

Figure V.2. Pump dependent spectra are shown in linear- and log-scale (inset) for the (a) TM_{111} and (b) TE_{011} cylindrical cavity modes. The linewidths measured are resolution limited above threshold. Linear scale spectra from the two modes just after threshold are seen in (c). Pump-dependent power output of nanopatch lasers are seen in (d) and (e) where rate equation modeled L-L curves are overlaid. Polarization-dependent near field images of each mode confirm each modes identity, with the TM_{111} mode being linearly polarized, and the TE_{011} mode being azimuthally polarized.

Rectangular nanopatches also show lasing action. Two non-degenerate modes with ‘electric-dipole-like’ mode profiles are seen if the rectangular is anisotropic so that the length is different from the width of the cavity. These devices also have vertical radiation emission. These modes eventually become degenerate as the anisotropy between the length and the width is reduced (Fig. V.3). Between the two modes, the higher energy mode always lases since higher gain can be achieved at higher energies due to a larger density of states. A plot of the mode spacing versus
anisotropy ratio is seen in Fig. V.3(f). There is an overall red shift in cavity resonances only because the cavities probed had larger dimensions at lower anisotropy.

Figure V.3. Nearly lasing spectra for rectangular patches with different anisotropy ratios are shown. The anisotropy ratio is reduced in order from (a) to (e). The yellow rectangles in the corner if each graph signify the top view of each rectangular nanpatch cavity, where the anisotropy has been exaggerated for clarity. The mode separation and wavelength versus anisotropy ratio is seen in (f).

V.3. Coupling to Waveguide and Nanofocusing

The nanolasers can be effectively coupled to integrated waveguide, such as those in Si photonics waveguide with 220nm thickness and about 500nm width. We have shown that with a metallic cavity, the coupling efficiency can be as high as 78% [4], [5]. In addition, the light output can be directly focused onto a nanoscale spot with 2x5nm² area using a metal-insulator-metal plasmonic three-dimensional taper [6], which is directly compatible with the nanpatch laser technology.

V.4. Conclusion

Nanopatch lasers of varying geometries are analyzed, simulated, fabricated, and characterized. These lasers operated in at near-infrared wavelengths, and are smaller than the diffraction limit in both modal and physical volumes. Using metaldielectric cavities at optical frequencies, radiation can be suppressed compared to purely dielectric cavities. Although loss is incurred with the presence of metal, using silver should help mitigate these problems. These lasers can also serve as attractive devices to investigate phenomena related to strong light-matter interactions. More importantly, however, they are physically small and easily adaptable to electrical injection, making them attractive candidates for future integration chip-scale onto silicon electronics.

References:


