



**A Comparison of Light-Emitting Diode
Power Supply Circuits**

by Arthur Harrison

ARL-TN-284

July 2007

NOTICES

Disclaimers

The findings in this report are not to be construed as an official Department of the Army position unless so designated by other authorized documents.

Citation of manufacturer's or trade names does not constitute an official endorsement or approval of the use thereof.

Destroy this report when it is no longer needed. Do not return it to the originator.

Army Research Laboratory

Adelphi, MD 20783-1197

ARL-TN-284

July 2007

A Comparison of Light-Emitting Diode Power Supply Circuits

Arthur Harrison
Sensors and Electron Devices Directorate, ARL

Approved for public release; distribution unlimited.

REPORT DOCUMENTATION PAGE

*Form Approved
OMB No. 0704-0188*

Public reporting burden for this collection of information is estimated to average 1 hour per response, including the time for reviewing instructions, searching existing data sources, gathering and maintaining the data needed, and completing and reviewing the collection information. Send comments regarding this burden estimate or any other aspect of this collection of information, including suggestions for reducing the burden, to Department of Defense, Washington Headquarters Services, Directorate for Information Operations and Reports (0704-0188), 1215 Jefferson Davis Highway, Suite 1204, Arlington, VA 22202-4302. Respondents should be aware that notwithstanding any other provision of law, no person shall be subject to any penalty for failing to comply with a collection of information if it does not display a currently valid OMB control number.

PLEASE DO NOT RETURN YOUR FORM TO THE ABOVE ADDRESS.

1. REPORT DATE (DD-MM-YYYY) July 2007		2. REPORT TYPE Final		3. DATES COVERED (From - To) 25 April to 21 May 2007	
4. TITLE AND SUBTITLE A Comparison of Light-Emitting Diode Power Supply Circuits				5a. CONTRACT NUMBER	
				5b. GRANT NUMBER	
				5c. PROGRAM ELEMENT NUMBER	
6. AUTHOR(S) Arthur Harrison				5d. PROJECT NUMBER	
				5e. TASK NUMBER	
				5f. WORK UNIT NUMBER	
7. PERFORMING ORGANIZATION NAME(S) AND ADDRESS(ES) U.S. Army Research Laboratory ATTN: AMSRD-ARL-SE-RM 2800 Powder Mill Road Adelphi, MD 20783-1197				8. PERFORMING ORGANIZATION REPORT NUMBER ARL-TN-284	
9. SPONSORING/MONITORING AGENCY NAME(S) AND ADDRESS(ES) U.S. Army Research Laboratory 2800 Powder Mill Road Adelphi, MD 20783-1197				10. SPONSOR/MONITOR'S ACRONYM(S)	
				11. SPONSOR/MONITOR'S REPORT NUMBER(S)	
12. DISTRIBUTION/AVAILABILITY STATEMENT Approved for public release; distribution unlimited					
13. SUPPLEMENTARY NOTES					
14. ABSTRACT In applications where LEDs use a large proportion of power and cell life is an important consideration, a power-conserving technique utilizing a switched circuit offers a considerable improvement in cell life. Two circuits, one utilizing a conventional method of resistive current limiting, and the other utilizing a switching method, were constructed and evaluated. The switching method yielded a 39% improvement in the duration of LED illumination.					
15. SUBJECT TERMS LED					
16. Security Classification of:			17. LIMITATION OF ABSTRACT UL	18. NUMBER OF PAGES 14	19a. NAME OF RESPONSIBLE PERSON Arthur Harrison
a. REPORT Unclassified	b. ABSTRACT Unclassified	c. THIS PAGE Unclassified			19b. TELEPHONE NUMBER (Include area code) (301) 394-4799

Contents

List of Figures	iv
Overview	1
Synopsis	1
Conclusion	3
References	5
Distribution List	7

List of Figures

Figure 1. Simple LED circuit.....	1
Figure 2. Switched LED circuit.	2
Figure 3. Schematic of circuit used for testing.	3

Overview

The Prognostics and Diagnostics program at the Army Research Laboratory uses various circuits that have light-emitting diodes (LEDs) as visible status indicators, powered by a 3-volt lithium cell. In these applications, where cell life is an important consideration, LEDs use a large proportion of power. This technical note provides the results of an experiment conducted to examine the merits of a power-conserving technique used in a LED power supply circuit.

Synopsis

A visible LED may be illuminated with a direct current provided by a 3v lithium cell, where the current delivered to the LED is determined by a series resistor, as shown in figure 1.

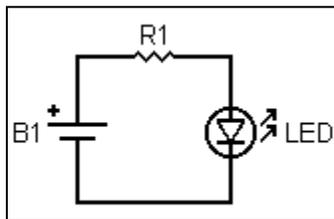


Figure 1. Simple LED circuit.

The LED current is defined by the equation:

$$I_{LED} = (V_{B1} - V_{f_{LED}}) / R1 = 990 \text{ microamperes (uA)}$$

where:

$$B1 = 3 \text{ Volts (V) lithium cell}$$

$$V_{B1} = 3.0V$$

$$V_{f_{LED}} = \text{Voltage across LED} = 2.1V$$

$$R1 = \text{Resistance} = 909\text{ohms}$$

The voltage across the resistor, V_{R1} , 0.9V, results in a power loss defined by the equation:

$$P_{loss} = (V_{R1})^2 / R1 = 0.891 \text{ milliwatts (mW)}$$

Another method for illuminating the same LED is to dispense with the resistor, and replace it with a semiconductor switch, metal oxide semiconductor field effect transistor (MOSFET), Q1, controlled with a rectangular waveform, as shown in figure 2.

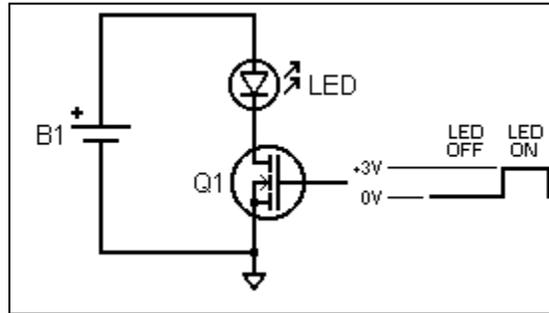


Figure 2. Switched LED circuit.

When 3V is applied to the gate of Q1, its channel resistance becomes less than 1ohm, providing nearly the full available cell current to the LED, limited only by the internal resistance of the cell, the on resistance of the MOSFET, and the resistance of the circuit's conductors. Since the MOSFET exhibits a very low on resistance of less than 1ohm, the associated power loss in the MOSFET is nearly zero.

If the MOSFET were to remain on, the resulting steady-state current may exceed the LED's maximum power rating and damage it. However, by regulating the on time and frequency of the MOSFET gate waveform, the average brightness of the LED may be set to equal the same steady-state value obtained in figure 1, while still operating the LED within its allowable pulsed-mode ratings.

Figure 3 shows the practical circuit employed for implementing the switching scheme. The type CD40106BE complementary metal oxide semiconductor (CMOS) six-section inverting Schmitt trigger integrated circuit (IC) was used for its low voltage operation capability. A relaxation oscillator is comprised of inverting Schmitt trigger U1A in conjunction with frequency-determining components resistor, R1, and capacitor, C1. The oscillator provides a frequency of approximately 800Hz. The oscillator's output is followed by a differentiator, comprised of capacitor, C2, and resistor, R2, with inverting Schmitt trigger, U1B, providing waveform restoration. The values of C2 and R2 are selected to provide an appropriate on-off ratio (duty cycle) for the LED. The four remaining inverting Schmitt triggers serve as a gate driver for the MOSFET. Capacitors C3 and C4 provide bypassing for the cell, lowering the power supply impedance to prevent the LED current impulses from affecting the oscillator. The figure 3 circuit, exclusive of the LED, uses approximately 10uA of battery current, which amounts to a power loss of 30 microwatts (uW), which is approximately one-thirtieth the power loss apparent in the circuit of figure 1.

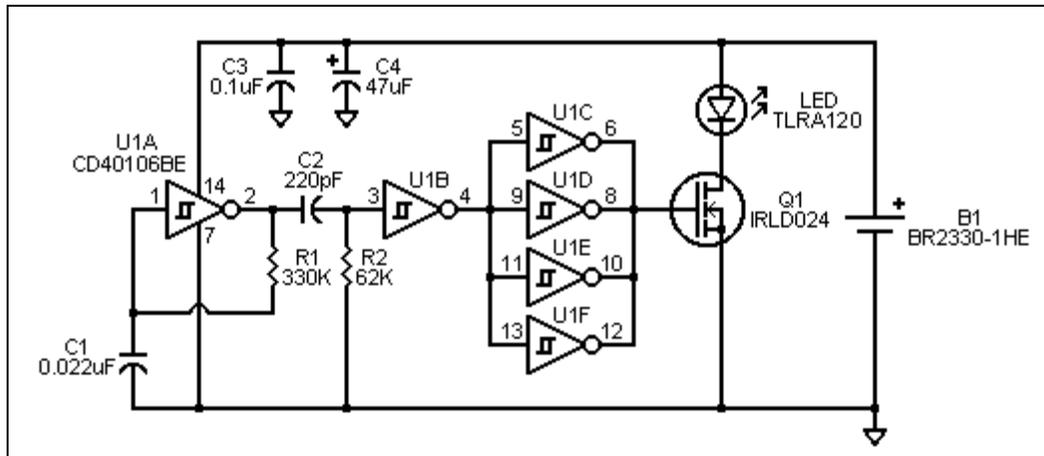


Figure 3. Schematic of circuit used for testing.

For the circuit shown in figure 3, the initial conditions were as follows:

- MOSFET on time = 22uS
- MOSFET off time = 1200uS
- Oscillator frequency = 818Hertz (Hz)
- LED duty cycle = 1.80%
- Cell voltage = 2.25V with the LED on

After 500 hours, the conditions were:

- MOSFET on time = 6uS
- MOSFET off time = 2700uS
- Oscillator frequency = 370Hz
- LED duty cycle = 0.22%
- Cell voltage = 2.40V with the LED on

The large variation in conditions results from the cell's voltage decrease during the test. More consistency of the timing parameters would be achieved in a system-based circuit employing a crystal-controlled timing generator, however, the autonomous circuit of figure 3 satisfied the requirement to evaluate the power-saving merits for a single LED, using a circuit with a minimum number of components.

Conclusion

The circuits of figures 1 and 3 were constructed with LEDs and power cells from the same lot, and were run concurrently. The brightness of the LEDs was evaluated on a subjective basis by several individuals, with the initial brightnesses being equal. After approximately 360 hours of

continuous operation, the LED in figure 1 exhibited approximately half of its initial brightness, while the LED in figure 2 exhibited half of its initial brightness after approximately 500 hours, yielding a 39% improvement in duration. At 500 hours, the cell voltage in the figure 1 circuit was 1.50V, and the LED was almost completely extinguished.

References

Data Sheet, Toshiba TLRA120 LED.

Data Sheet, International Rectifier IRLD024 MOSFET.

Data Sheet, Panasonic BR2320-1HE Lithium Cell.

INTENTIONALLY LEFT BLANK

Distribution List

ADMNSTR
DEFNS TECHL INFO CTR
ATTN DTIC-OCP (ELECTRONIC COPY)
8725 JOHN J KINGMAN RD STE 0944
FT BELVOIR VA 22060-6218

DARPA
ATTN IXO S WELBY
3701 N FAIRFAX DR
ARLINGTON VA 22203-1714

OFC OF THE SECY OF DEFNS
ATTN ODDRE (R&AT)
THE PENTAGON
WASHINGTON DC 20301-3080

US ARMY TRADOC
BATTLE LAB INTEGRATION & TECHL
DIRCTRT
ATTN ATCD-B
10 WHISTLER LANE
FT MONROE VA 23651-5850

SMC/GPA
2420 VELA WAY STE 1866
EL SEGUNDO CA 90245-4659

US ARMY INFO SYS ENGRG CMND
ATTN AMSEL-IE-TD F JENIA
FT HUACHUCA AZ 85613-5300

COMMANDER
US ARMY RDECOM
ATTN AMSRD-AMR W C MCCORKLE
5400 FOWLER RD
REDSTONE ARSENAL AL 35898-5000

US ARMY RDECOM-ARDEC
DIAG & PROGNOSTICS BR AUTO TEST
SYS DIV
ATTN P JONSSON
BLDG 91N ATS
PICATINNY ARSENAL NJ 07806-5000

US ARMY RSRCH LAB
ATTN AMSRD-ARL-CI-OK-TP TECHL
LIB T LANDFRIED (2 COPIES)
BLDG 4600
ABERDEEN PROVING GROUND MD
21005-5066

US GOVERNMENT PRINT OFF
DEPOSITORY RECEIVING SECTION
ATTN MAIL STOP IDAD J TATE
732 NORTH CAPITOL ST., NW
WASHINGTON DC 20402

DIRECTOR
US ARMY RSRCH LAB
ATTN AMSRD-ARL-RO-EV W D BACH
PO BOX 12211
RESEARCH TRIANGLE PARK NC 27709

US ARMY RSRCH LAB
ATTN AMSRD-ARL-CI-OK-T TECHL
PUB (2 COPIES)
ATTN AMSRD-ARL-CI-OK-TL TECHL
LIB (2 COPIES)
ATTN AMSRD-ARL-D J M MILLER
ATTN AMSRD-ARL-SE-RE R DEL
ROSARIO
ATTN AMSRD-ARL-SE-RM D W
VANCE

US ARMY RSRCH LAB (Cont'd)
ATTN AMSRD-ARL-SE-RM E ADLER
ATTN AMSRD-ARL-SE-RM
G MITCHELL
ATTN AMSRD-ARL-SE-RM T PIZZILLO
ATTN AMSRL-SE-RM A HARRISON
ATTN IMNE-ALC-IMS MAIL &
RECORDS MGMT
ADELPHI MD 20783-1197