

GENERAL ATOMICS ENERGY PRODUCTS
Engineering Bulletin

PULSE POWER CAPACITORS

**Fred MacDougall, Richard Jow, Joel Ennis,
S.P.S. Yen, X. H. Chip Yang, Janet Ho**
General Atomics Electronic Systems, Inc., San Diego, CA, United States
US Army Research Laboratory, Adelphi, MD, United States
NASA Jet Propulsion Laboratory, Pasadena, CA, United States

Presented at:

**2008 IEEE International Power Modulator Conference
May 27-31, 2008, Las Vegas, Nevada, USA**



4949 Greencraig Lane, San Diego, CA. 92123-1675
858.522.8400

www.ga-esi.com

Report Documentation Page

Form Approved
OMB No. 0704-0188

Public reporting burden for the collection of information is estimated to average 1 hour per response, including the time for reviewing instructions, searching existing data sources, gathering and maintaining the data needed, and completing and reviewing the collection of information. Send comments regarding this burden estimate or any other aspect of this collection of information, including suggestions for reducing this burden, to Washington Headquarters Services, Directorate for Information Operations and Reports, 1215 Jefferson Davis Highway, Suite 1204, Arlington VA 22202-4302. Respondents should be aware that notwithstanding any other provision of law, no person shall be subject to a penalty for failing to comply with a collection of information if it does not display a currently valid OMB control number.

1. REPORT DATE MAR 2008		2. REPORT TYPE		3. DATES COVERED 00-00-2008 to 00-00-2008	
4. TITLE AND SUBTITLE Pulse Power Capacitors				5a. CONTRACT NUMBER	
				5b. GRANT NUMBER	
				5c. PROGRAM ELEMENT NUMBER	
6. AUTHOR(S)				5d. PROJECT NUMBER	
				5e. TASK NUMBER	
				5f. WORK UNIT NUMBER	
7. PERFORMING ORGANIZATION NAME(S) AND ADDRESS(ES) General Atomics Electronic Systems Inc,4949 Greencraig Lane,San Diego,CA,92123				8. PERFORMING ORGANIZATION REPORT NUMBER	
9. SPONSORING/MONITORING AGENCY NAME(S) AND ADDRESS(ES)				10. SPONSOR/MONITOR'S ACRONYM(S)	
				11. SPONSOR/MONITOR'S REPORT NUMBER(S)	
12. DISTRIBUTION/AVAILABILITY STATEMENT Approved for public release; distribution unlimited					
13. SUPPLEMENTARY NOTES					
14. ABSTRACT see report					
15. SUBJECT TERMS					
16. SECURITY CLASSIFICATION OF:			17. LIMITATION OF ABSTRACT	18. NUMBER OF PAGES	19a. NAME OF RESPONSIBLE PERSON
a. REPORT unclassified	b. ABSTRACT unclassified	c. THIS PAGE unclassified			

Pulse Power Capacitors

Fred MacDougall¹, Richard Jow², Joel Ennis¹, S.P.S. Yen³, X. H. Chip Yang¹, Janet Ho²

¹General Atomics Electronic Systems, Inc., San Diego, CA, United States

²US Army Research Laboratory, Adelphi, MD, United States

³NASA Jet Propulsion Laboratory, Pasadena, CA, United States

I. ABSTRACT

The US Army Research Laboratory has sponsored a capacitor development program for film dielectric capacitors. The program has evaluated dielectric materials for high energy density capacitors from industrial and academic research programs. High performance capacitors have been developed that meet the needs of today's military applications. The performance of recently developed capacitors will be discussed.

II. PROGRAM OBJECTIVES

The performance objectives for capacitors needed by the US Military is ever changing to meet new applications. The ongoing effort in the development of pulse power capacitors has resulted in remarkable improvements in capacitor performance. These successes have caused a shift in focus to other areas where research is needed.

III. MILLISECOND DISCHARGE CAPACITORS

The performance of the millisecond class capacitors is normally measure in terms of charge discharge cycles. Here the driving requirements have been for Railguns and electro thermo ignition (ETI) guns. The capacitors have been used in these applications for decades. In the case of the Army's requirements to have a large pulse forming network (PFN) on a relatively small vehicle, there is a significant gap between what has been accomplished and what is needed. In terms of the ETI guns, the requirements have been met or exceeded. While a capacitor driven PFN is significantly larger than the designers would like for larger systems, the size of the equipment is steadily shrinking and may meet the requirements of a deployed system in a few years.

The energy density of the capacitors used for railgun PFNs a few years ago it was at 1 Joule/cc. Now, some five years later, there are 2 J/cc capacitors in production for the same application that out perform the 1 J/cc predecessors. The latest version of this capacitor coming out of the laboratory is operating at 2.4 J/cc. This class of capacitor is designed with a high efficiency dielectric. A capacitor starting at room temperature could operate for 200 charge discharge cycles with no cooling and without reaching the capacitors maximum operating temperature and with 200 shots taking place in less than half an hour.

Cooling the switches and other parts of the PFN as well as the gun barrel is going to be a far more difficult task.

The weight density of the capacitors will be somewhat determined by the mountings used. For militarized applications, the requirements for the capacitors include operate at an 80 g shock loading. Oil filled capacitors are inherently good at withstanding shocks. The internal parts are soft but surrounded by a dielectric fluid that transmits the force from through the capacitor without damaging the dielectric material. The major concerns are the permanent deformation of the capacitor case and the generation of leaks at the bushings. The bushing stress can be relieved with a light weight flexible connector. The capacitor case is a different matter.

Both the capacitors in Figure 1 are designed to handle 80 g shock loading and the design has been tested for that parameter. The capacitor on the right represents standard construction techniques for capacitors. In the case of the taller capacitor on the left, additional reinforcing was needed in the long dimension to meet the shock loading requirement. Small angles were added to the four long corners. The addition of the steel angles represents a 4% increase in weight for the capacitor.

The comparison in Figure 1 is a practical guide for how large a pulse forming capacitor can be before it will need special reinforcing in order to meet the military's 80g Shock loading requirement.

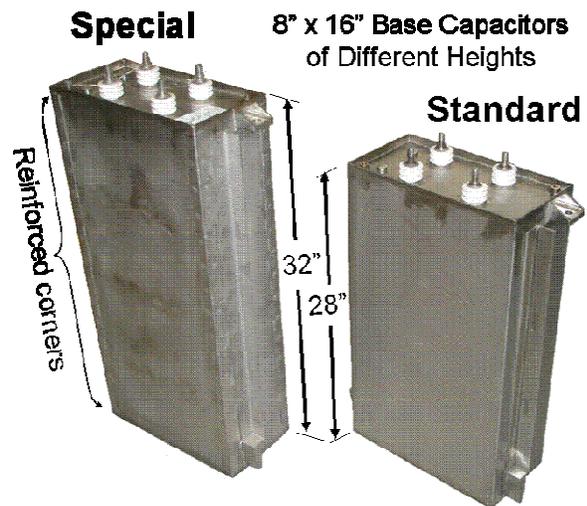


Figure 1 - 80 g Shock Capacitors

Larger capacitors are available for millisecond discharge applications. One capacitor is likely to store over a quarter megajoule. A capacitor operating at 2.4 J/cc and delivering a quarter megajoule will be 50% larger than the capacitor on the left in Figure 1 and will need the special reinforcing. With the extra reinforcing, the capacitors weight density will be about 2.1 J/g

IV. MICROSECOND DISCHARGE CAPACITORS

Work with microsecond capacitors has also yielded significant results. This class of capacitor is used for pulse shaping in millisecond class PFNs when fast turn on or turnoff is required. The higher frequency response millisecond class capacitors are used to form the upper corners of the flat top square wave. For this type of duty, the requirements will determine the actual energy density. Typically the energy density achieved in the faster capacitors is about 80% of slower millisecond discharge capacitors.

In other applications, a long DC life requirement drives the capacitor design. The capacitor must sit fully charged for weeks on end. The life expectancy of this type of capacitor has been improved by a factor of 1000 over the past four years. The shapes of the capacitors vary greatly but generally they have a rectangular cross section.

Microsecond discharge capacitors require a small series inductance budget to perform properly. Larger capacitor requirements often require that multiple capacitors be used to order to meet the inductance budget of the system. Another way of accomplishing this is to use multiple current paths requiring multiple bushings on the capacitors. This has been done microsecond discharge capacitor shown in Figure 2. The same technique is used to minimize the inductance on the capacitors shown in Figure 1.

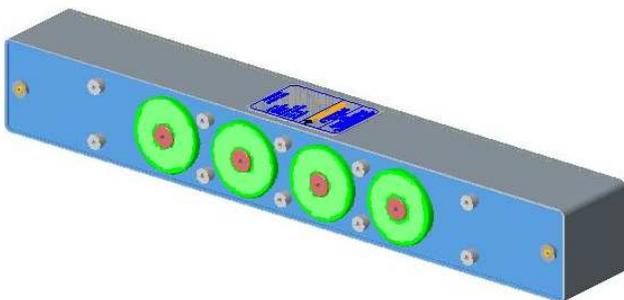


Figure 2 - Typical Microsecond Discharge Capacitor

At present, a 2000 hour DC Life, fast discharge capacitors are available at an energy density of 1.3 J/cc. There are a number of shapes and ratings that have been manufactured and supplied to various military customers.

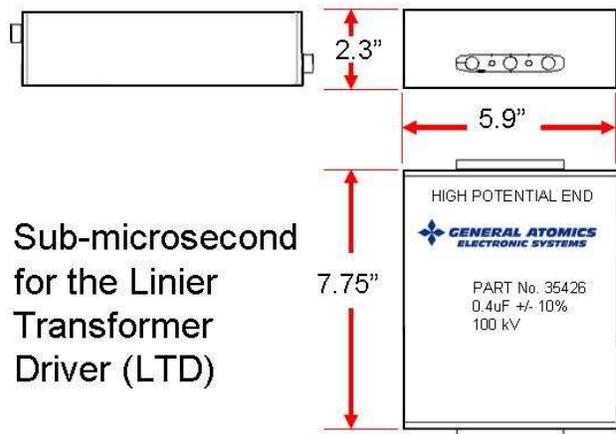
V. SUB-MICROSECOND CAPACITORS

With the millisecond and microsecond discharge capacitor development successes, the focus of the development effort has shifted. It is now focusing on sub-microsecond capacitors.

Traditionally, capacitors built in this time regime the life expectancy of the capacitors is in the millions or billions of charge discharge cycles. The capacitors are used in accelerators where they operate continuously at repetition rates in the kilohertz range. Because of the duty cycle and long life expectancy, these capacitors operate at very low energy densities. An example of this is the capacitor shown in Figure 3. This capacitor designed to be used in high volumes and is rated for:

- 100kVDC Operating Voltage
- 0.04uF +/-10%
- 0.1J/cc Energy Density
- 50,000 Charge Discharge Cycles
- 90% Survival

The military's requirements for sub-microsecond capacitors are quite different from that of LTD. The capacitors of greatest interest must function for one cycle, or for one burst of cycles, or in some cases for a few hundred cycles. They are designed to fit into specific equipment very restrictive space requirements. Most of the shapes required are round or rectangular cylinders.



Sub-microsecond for the Linier Transformer Driver (LTD)

Figure 3 - Conventional Sub-microsecond Capacitor

VII. CONCLUSIONS

The present work being done on capacitors for military applications has resulted in significant improvements in capacitor performance in the past few years. The level of effort in capacitor research is expected to continue. This holds promise of better capacitors for use in the energy discharge applications and other areas of interest to the military.

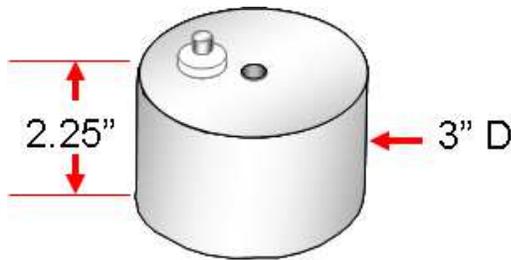


Figure 4 - Sub-microsecond Discharge Capacitor

The techniques that have been used to improve the performance of the millisecond capacitors and microsecond capacitors are now being applied to the sub-microsecond polymer capacitors. The energy density of the capacitors has increased with capacitors shipped by GA-ESI, shown in Figure 4, achieving 2J/cc in a single shot application.

While the energy density of the capacitor in Figure 4 is impressive, it is far from a direct comparison with the LTD capacitor. The capacitor of Figure 4 is designed to operate at a much lower voltage making it simpler to assemble. While this capacitor uses some of the same construction techniques as the millisecond and microsecond energy discharge capacitors, the failure mode is significantly different. End of life for the millisecond and microsecond capacitors is defined as the loss of an arbitrary percentage of its initial capacitance. For the capacitor of Figure 3 at the end of life, 90% of the capacitors in the bank will still be functioning properly. For the capacitor of Figure 4 the capacitor will be an open circuit at the end of life.

VI. PROJECTIONS FOR THE FUTURE

The capacitors described for the most part, represent the state of the art for oil filled pulse power capacitors. Tracking the performance record for oil filled capacitors over the past few years indicates that the pulse power capacitors investigated under the Army program are decreasing in volume by 50% about every 4 years. The continued success of the program has attracted a number of military users who are doing the basic research on capacitors under the ARL program. While there is no guarantee that the rate of improvement will be sustained, the most recent results from testing laboratory capacitors indicate that this trend will carry on in manufactured capacitors for the next few years.

VIII. ACKNOWLEDGEMENTS

Portions of the research reported in this document/presentation was performed in connection with contract W911QX-04-D-0003 with the U.S. Army Research Laboratory. The views and conclusions contained in this document/presentation are those of the authors and should not be interpreted as presenting the official policies or position, either expressed or implied, of the U.S. Army Research Laboratory or the U.S. Government unless so designated by other authorized documents. Citation of manufacturers' or trade names does not constitute an official endorsement or approval of the use thereof. The U.S. Government is authorized to reproduce and distribute reprints for Government purposes notwithstanding any copyright notation hereon.

IX. REFERENCES

- [1] Testing of High Energy Density Capacitors, T. Crowley, W. Shaheen, S Bayne, R. Jow, IEEE Pulse Power Conference Albuquerque, NM, June 2007
- [2] Recent Advances in High Voltage, High Energy Capacitor Technology, J. Ennis F. W. MacDougall, X. H. Yang, R. A. Cooper, K. Seal, C. Naruo, B. Spinks, P. Kroessler, J. Bates, IEEE Pulse Power Conference Albuquerque, NM, June 2007.
- [3] Large High Energy Density pulse Discharge Capacitor Characteristics, Fred MacDougall, Joel Ennis, Xiao Hui Yang, Ken Seal, Sanjay Phatak, Brian Spinks, Nathan Keller, Chip Naruo, T. Richard Jow, 15th IEEE International Pulsed Power Conference 2005 Monterey CA
- [4] Design Optimization of Linier Transformer Driver (LTD) Stage Cell Capacitors, Andrew H. Bushnell, Byeong-Mun Song, Joel Ennis, Richard Miller, Dave Johnson, John Maenchen, Power Modulator Conference, May 2004, San Francisco CA