TOP vs. BOTTOM CHARGING OF THE DIELECTRIC IN RF MEMS CAPACITIVE SWITCHES (Preprint)

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Using a movable top electrode, for the first time, top vs. bottom charging of the dielectric in metal/insulator/metal capacitors is delineated. For the Al/SiO2/Cr structure used in RF MEMS capacitive switches, charge injection from Al into the top of SiO2 was found to have a higher threshold voltage, faster charging time, and slower discharging time than charge injection from Cr into the bottom of SiO2. The higher threshold is attributed to non-ideal contact geometry and chemistry. The faster charging time is attributed to the exponential voltage dependence. The slower discharging time is attributed to diffusion across SiO2. Since top charging is more critical to switch performance and reliability than bottom charging, understanding the trade off of top versus bottom charging can help minimize their undesirable effects.

15. SUBJECT TERMS
RF MEMS, Dielectric Charging, low loss
Top vs. Bottom Charging of the Dielectric in RF MEMS Capacitive Switches

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\textbf{Abstract} — Using a movable top electrode, for the first time, top vs. bottom charging of the dielectric in metal/insulator/metal capacitors is delineated. For the Al/SiO\textsubscript{2}/Cr structure used in RF MEMS capacitive switches, charge injection from Al into the top of SiO\textsubscript{2} was found to have a higher threshold voltage, faster charging time, and slower discharging time than charge injection from Cr into the bottom of SiO\textsubscript{2}. The higher threshold is attributed to non-ideal contact geometry and chemistry. The faster charging time is attributed to the exponential voltage dependence. The slower discharging time is attributed to diffusion across SiO\textsubscript{2}. Since top charging is more critical to switch performance and reliability than bottom charging, understanding the trade off of top vs. bottom charging can help minimize their undesirable effects.

\textbf{Index Terms} — Charging, dielectric, RF MEMS, switch, trap.

\section{I. INTRODUCTION}

MEMS switches can potentially replace GaAs transistors and diodes in switching RF signals [1], [2]. However, in many cases, the lifetime of RF MEMS capacitive switches is limited by dielectric charging [3]-[15]. During switch operation, the control voltage across the dielectric causes charge to be injected into the dielectric and become trapped. With repeated operation, charge gradually builds up in the dielectric, which modifies the electrostatic force on the movable electrode and results in actuation-voltage shift and stiffness. For RF MEMS capacitive switches based on the Al/SiO\textsubscript{2}/Cr structure, we have characterized [10] charge injection from the bottom Cr electrode into the SiO\textsubscript{2} under moderate control voltages. We now report operation of similar switches under higher control voltages, which cause charge injection from the top Al electrode into the SiO\textsubscript{2}, in addition to from the bottom Cr electrode into the SiO\textsubscript{2}.

\section{II. EXPERIMENTAL}

Fig. 1 illustrates the cross section of an RF MEMS capacitive switch fabricated on a glass substrate. The dielectric is sputtered SiO\textsubscript{2} with a thickness of 0.25 µm and a dielectric constant of 4.0. The top electrode is a 0.3-µm-thick Al membrane that is grounded. The bottom Cr/Au electrode serves as the center conductor of a 50 Ω coplanar waveguide for the RF signal. Without any electrostatic force, the membrane is normally suspended in air 2.5 µm above the dielectric. A control voltage is applied to the bottom electrode, which pulls the membrane in contact with the SiO\textsubscript{2} thus forming a 120 µm x 80 µm capacitor to shunt the RF signal to ground. To characterize charging effects, the switch was stressed at different control voltages and polarities under room temperature and less than 1% relative humidity. During each stress period, shift in actuation voltage was periodically sampled by applying a triangular voltage pulse while monitoring the RF signal output [7].

\section{III. CHARGING UNDER POSITIVE CONTROL VOLTAGES}

Fig. 2 shows measured actuation-voltage shifts during and after stresses at 30, 40 and 50 V for 300 s. The actuation voltage for a pristine switch is 26 V. When the switch is stressed at 30 V, the actuation voltage decreases gradually by approximately 5 mV/s. This indicates that positive charge is injected from the bottom electrode into the dielectric, which enhances the electrostatic force on the top electrode when a positive control voltage is applied to the bottom electrode. After the 30 V stress is removed, the bottom-injected charge gradually discharges and the actuation voltage recovers to its pristine value of 26 V within approximately 1000 s. When the switch is stressed at 40 V, the actuation voltage shifts and recovers in a similar manner to that at 30 V. However, the

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{fig1.png}
\caption{Schematic cross section of an RF MEMS capacitive switch. Charging of the top and bottom of the dielectric is indicated for the case of a positive control voltage.}
\end{figure}
Actuation-voltage shift is larger than that at 30 V, which is consistent with the previously reported exponential dependence of charging on voltage [10]. Despite being larger, the actuation-voltage shift at 40 V also recovers within approximately 1000 s, which is consistent with the previously reported independence of charging and discharging times on voltage [10].

By contrast, when the switch is stressed at 50 V, the actuation voltage quickly increases before gradually decreases. Thus, after a total stress period of 300 s, the net actuation-voltage shift is almost nil. However, after the 50 V stress is removed, the actuation voltage gradually increases by approximately 3 V in ~1000 s. This suggests that, under the 50 V stress, in addition to positive charge being injected from the bottom electrode into the dielectric, negative charge is also injected from the top electrode into the dielectric. Furthermore, top charging is much faster than bottom charging, while discharging of top-injected charge is much slower than discharging of bottom-injected charge. Thus, when 50 V is applied to the bottom electrode, negative charge is quickly injected from the top electrode into the dielectric. Within a few seconds, top charging saturates or otherwise slows down to allow positive charge from the bottom to catch up and to gradually compensate for the effect that is caused by the negative charge injected from the top. After the 50 V stress is removed, the bottom-injected charge discharges in ~1000 s similar to the 30 and 40 V cases. However, with the top electrode up in the air, the top-injected charge must diffuse across the thickness of the dielectric in order to be discharged. Experimentally we found that, after the 50 V stress, it took many hours for the actuation voltage to recover to its pristine value.

The top- and bottom-injected charges, \( Q_T \) and \( Q_B \), can be modeled as [10]:

\[
Q_T = Q_{i0} \left[ 1 - \exp\left( -\frac{t_{ON}}{\tau_C} \right) \right] \exp\left( -\frac{t_{OFF}}{\tau_D} \right) \\
Q_B = Q_{i0} \left[ 1 - \exp\left( -\frac{t_{ON}}{\tau_C} \right) \right] \exp\left( -\frac{t_{OFF}}{\tau_D} \right)
\]

where \( T \) and \( B \) denote top and bottom, respectively, \( Q_{i0} \) is the steady-state charge, \( \tau_C \) and \( \tau_D \) are charging and discharging time constants, respectively. \( t_{ON} \) and \( t_{OFF} \) are on and off times of the switch, respectively. Previously, both \( \tau_{CB} \) and \( \tau_{DB} \) were found [10] to be of the order of 10 s. We now observe that \( \tau_{CT} \ll \tau_{CB} \) and \( \tau_{DT} \ll \tau_{DB} \). The actuation-voltage shift \( \Delta V \) due to the combined effects of top charging and bottom charging is:

\[
\Delta V = \frac{h_T Q_T - h_B Q_B}{\varepsilon_0 \varepsilon_1}
\]

where \( \varepsilon_0 \) is the permittivity of vacuum; \( \varepsilon_1 \) is the relative dielectric constant. \( h_T \) and \( h_B \) are distances between the bottom electrode and top- and bottom-injected charges, respectively, assuming that the effect of the injected charge can be approximated by a sheet charge concentrated at the distance \( h \). Notice that the effects of \( Q_T \) and \( Q_B \) are weighted by \( h_T \) and \( h_B \), respectively. While there is no way to directly measure the distribution of the injected charge in the dielectric, a reasonable assumption is that \( h_T \gg h_B \). Therefore, the fact that bottom charging can compensate for top charging suggests that \( Q_T \ll Q_B \). In addition, based on a rough estimate of \( \tau \sim h_T^2 / D \) (\( D \) is diffusivity), it is not surprising that \( t_{CT} \ll t_{DB} \). Presently we cannot explain why \( t_{CT} \ll t_{DB} \) or why \( Q_T \ll Q_B \).

IV. CHARGING UNDER NEGATIVE CONTROL VOLTAGES

Previously, we reported [10] that bottom-charging behaviors were similar under both positive and negative stresses. We now observe that top-charging behaviors are also
Fig. 4. Energy band diagram of the Al/SiO₂/Cr capacitor.

similar under both positive and negative stresses, except the threshold voltage for top charging appears higher under negative stress than under positive stress. Fig. 3 shows measured actuation-voltage shifts during and after stresses at −30, −50, and −60 V, respectively. To facilitate comparison with the positive-stress cases of Fig. 2, shifts in the absolute magnitude of actuation voltages are plotted. For example, the actuation voltage for the pristine switch is −26 V and a −1 V actuation-voltage shift means that the actuation voltage is shifted to −25 V. Between −30 and −50 V, bottom charging dominates. Top charging occurs only when the stress is increased to −60 V.

The difference in top-charging thresholds may be explained by the energy band diagram of the Al/SiO₂/Cr manner. As shown in Fig. 4, the emission barriers from Al into SiO₂ are approximately 3.4 and 5.6 eV for electrons and holes, respectively. This explains why it is easier to inject electrons from Al into SiO₂ under positive voltages than to inject holes from Al into SiO₂ under negative voltages. However, the band diagram cannot be used to explain why 1) top-charging threshold is higher than bottom-charging threshold, 2) top-charging density is lower than bottom-charging density, or 3) bottom charging thresholds are similar under both positive and negative stresses. Therefore, we postulate that direct tunneling from the electrodes to the defect states in the dielectric plays a key role here. For tunneling, surface contamination of the dielectric can increase the top-charging threshold while the less than 100% contact area can decrease the top-charging density. These subjects are currently under investigation.

V. CONCLUSION

In summary, the present Al/SiO₂/Cr structure has a higher threshold for top charging than for bottom charging. When the control voltage exceeds the top-charging threshold, charging occurs at both the top and bottom of SiO₂. Top charging is faster than bottom charging, but the former saturates at a lower density than the latter. Because the top-injected charge is closer to the movable top electrode and is slower to discharge, it has a greater impact on switch operation. For the present Al/SiO₂/Cr structure, the top-charging threshold of −45 V is significantly higher than the actuation voltage of 26 V, so that a control voltage of −30 V can be used to accommodate device-to-device variation and small actuation-voltage shift due to bottom charging, without incurring top charging. For other cases where top charging is unavoidable, especially under complex control-voltage waveforms, the previously reported bottom-charging model [10] needs to be extended to include both top and bottom charging.

Charging in metal/insulator/metal capacitor structures has been studied extensively by the semiconductor industry and the combined effect of top and bottom charging have been reported [16]. By using a movable top electrode, for the first time, top charging can be delineated from bottom charging. This can have a significant impact on not only MEMS devices, but also many other electronic devices.

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