APPENDIX 17
IEWS DATA TRANSFER (XFER)
FINAL SOFTWARE REPORT
DATA ITEM NO. A005

INTEGRATED ELECTRONIC WARFARE SYSTEM
ADVANCED DEVELOPMENT MODEL (ADM)

7800987-17

PREPARED FOR:
NAVAL AIR DEVELOPMENT CENTER
WARMINSTER, PENNSYLVANIA

CONTRACT N62699-75-C-0070

RAYTHEON
ELECTROMAGNETIC SYSTEMS DIVISION

1 OCTOBER 1977

UNCLASSIFIED
APPENDIX 17
DATA TRANSFER SOFTWARE DESIGN SPECIFICATION
FINAL SOFTWARE REPORT
DATA ITEM A005

INTEGRATED ELECTRONIC WARFARE SYSTEM (IEWS)
ADVANCED DEVELOPMENT MODEL (ADM)

Contract No. N62269-75-C-0070

Prepared for:
Naval Air Development Center
Warminster, Pennsylvania

Prepared by:
RAYTHEON COMPANY
Electromagnetic Systems Division
6380 Hollister Avenue
Goleta, California 93017

1 OCTOBER 1977
Computer Program Design Specification

**TITLE OF SPEC**

IEWS Special Test Equipment Data Transfer (XFER)

<table>
<thead>
<tr>
<th>FUNCTION</th>
<th>APPROVED</th>
<th>DATE</th>
<th>FUNCTION</th>
<th>APPROVED</th>
<th>DATE</th>
</tr>
</thead>
<tbody>
<tr>
<td>WRITER</td>
<td>T. Chernesky</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**REVISIONS**

<table>
<thead>
<tr>
<th>CHK</th>
<th>DESCRIPTION</th>
<th>REV</th>
<th>CHK</th>
<th>DESCRIPTION</th>
<th>REV</th>
</tr>
</thead>
</table>

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**REVISION**

**SHEET NO.**

**REV STATUS OF SHEETS**

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1.0 Scope

1.1 Identification

This document specifies the detailed design requirements of XFER, a stand-alone non-operational software package for IEWS. XFER shall provide the STE with a memory dump or memory modify capability without requiring the IEWS operational software to be resident in IEWS. Communication to and from the STE shall follow the same protocol as that between the STE and the IEWS operational software.

1.2 Subprogram Tasks

XFER shall consist of five modules:
1) Resource management processor: XFRMP
2) Classification processor: XFCP
3) Analysis processor: XFAP
4) Techniques generator: XFTG
5) Signal sorter: XFSS

Each module shall have the capability of processing memory dump/modify requests destined for the host processor, and the capability of passing requests destined for slave processors to those slave processors.

2.0 Applicable Documents

The following documents, of the exact issue shown, form a part of this specification to the extent specified herein. In the event of conflict between the documents referenced herein and the contents of this specification, the contents of the Computer Program Design Specification for the Integrated Electronic Warfare System (IEWS) Advanced Development Model (ADM) Program shall be considered superseding requirements.
2.1 Computer Program Performance Specification


2.2 Computer Program Design Specification


2.3 Data Base Design Document

The Common Data Base Design Document, System Controller Unit, IEWS, ADM, document No. 53959-GR-0751, shall apply to this subprogram.

2.4 Miscellaneous Documents

The following documents shall apply to this subprogram.

<table>
<thead>
<tr>
<th>Document No.</th>
<th>Document Title</th>
</tr>
</thead>
<tbody>
<tr>
<td>53959-GR0756</td>
<td>Computer Subprogram Design Document, Executive, IEWS, ADM</td>
</tr>
<tr>
<td>WS-8506</td>
<td>Requirements for Digital Computer Program Documentation</td>
</tr>
<tr>
<td>Revision 1, 1 November 1971</td>
<td></td>
</tr>
</tbody>
</table>

3.0 Requirements

3.1 Subprogram Detailed Description

Each of the five XFER modules (XFRMP, XFCP, XFAP, XFTG, and XFSS) shall have a similar structure.
Each shall have two sections:
1) Initializer
2) Query

Each of the initializers shall:
1) Initialize (master clear) any slave processors
2) Clear all message flags of to-slave message buffers and from slave message buffers.
3) Newstart (put into run mode) any slave processors
4) Pass control to Query

Each of the Query sections shall poll the master and any slave processors for messages. If there is a message from the master, the XFER module shall determine if the message is destined for the host processor or a slave. If for a slave, the message shall be relayed to that slave. If not for a slave, then the message (a memory dump or memory modify) shall be processed locally. That is, local memory shall be modified, or an Executive message type 5 shall be generated (see CDBDD) as a response to the local memory dump request all messages from slaves shall be relayed to the master processor.

3.2 Subprogram Flow Diagram
Figure 1 shows a flowchart which is applicable to each of the five XFER modules.

3.3 Computer Subprogram Environment

3.3.1 Tables
Each XFER module (with the exception of the RMP) shall have an input buffer and output buffer (IN BUFF and OUT BUFF, respectively)
XFER (ALL MODULES)
MASTER

GET PROC. CODE

MSC FOR THIS PROC.?

Y

GET PROPER SLAVE'S MSG FLAG

N

MEM. READ OR WRITE

READ

GET TO-MASTER FLAG

= 0

N

WRITE

GET ADDRESS

GET 15 DATA WORDS

PUT ADDR & DATA IN TO-MASTER BUFFER

CLEAR FROM-MASTER FLAG

A

A

GET ADDRESS

GET DATA WORDS

MODIFY MEMORY

CLEAR FROM-MASTER FLAG

XFER

(ALL MODULES)
SLAVFR

GET TO-MASTER MSG, BUFFER, FLAG

=0

N

Y

COPY SLAVE'S MSG TO MASTER'S BUFFER

CLEAR SLAVE'S MSG FLAG

SET MASTERS MSG FLAG

A

XFER

(ALL MODULES)
3.3.1.1 IN BUFF

IN BUFF shall be used as the input message buffer for messages from the master. IN BUFF shall be 25 16-bit words in length. Word 0 shall be a flag word (non-zero contents mean a message is present). Remaining words shall contain an Executive message type 27 or 28 (memory dump or memory modify, respectively).

3.3.1.2 OUT BUF

OUT BUF shall be used as the output message buffer for messages to the master. OUT BUF shall be 25 16-bit words in length. Word 0 shall be a flag word (non-zero contents means a message is present). Remaining words shall be available for storage of an Executive message type 5 (memory dump response).

3.3.1.3 RMP XFER Input Buffer

The equivalent of IN BUFF in XFRMP shall be the STE - to - SC message buffer defined as $7004_{16}$ through $7017_{16}$. The format of this message buffer shall be the same as IN BUFF (word 0 shall be a flag).

3.3.1.4 RMP XFER Output Buffer

Communication from the RMP to the STE shall utilize the circular buffer defined by the contents of the following locations:

- $7000_{16}$: Address of next message to be read by the STE
- $7001_{16}$: Address of next available word in buffer
- $7002_{16}$: Address of lower memory bound buffer
- $7003_{16}$: Address +1 of upper memory bound of buffer

The contents of these locations must be initialized by the STE program, SYSTEST. A copy of the IEWS operational software EXSTE subroutine shall be used to store data in the circular buffer.
3.3.2 Variables
None

3.3.3 Constants
None

3.3.4 Flags
None

3.3.5 Indices
None

3.3.6 Common Data Base References
None

3.4 Input/Output Formats
Xfer shall receive executive messages (type 27 and 28) from the STE and shall output executive messages (type 5) to the Message type 27 is a "read memory" message. Type 28 is a "write memory" message. Type 5 is a STE - destined data extraction message. The format of these message types is shown in the following figures.

3.5 Required System Library Subroutines
None

3.6 Conditions for Initiation
Unconditional

3.7 Subprogram Limitations
None
<table>
<thead>
<tr>
<th>14</th>
<th>13</th>
<th>12</th>
<th>11</th>
<th>10</th>
<th>0</th>
<th>9</th>
<th>8</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>0</th>
</tr>
</thead>
</table>

**READ MEMORY MESSAGE FORMAT**

(Exec. Message Type 27)

- Address
- Processor No.
- No. of Words to Follow

15: 1211 87 43 0
<table>
<thead>
<tr>
<th>No.</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>28</td>
</tr>
<tr>
<td>1</td>
<td>No. of words to follow</td>
</tr>
<tr>
<td>2</td>
<td>Processor No.</td>
</tr>
<tr>
<td>3</td>
<td>Address</td>
</tr>
<tr>
<td>4</td>
<td>No. words of data</td>
</tr>
<tr>
<td>5</td>
<td>1st data word</td>
</tr>
<tr>
<td>6</td>
<td>2nd data word</td>
</tr>
<tr>
<td>7</td>
<td>3rd data word</td>
</tr>
<tr>
<td>8</td>
<td>4th data word</td>
</tr>
<tr>
<td>9</td>
<td>5th data word</td>
</tr>
<tr>
<td>10</td>
<td>6th data word</td>
</tr>
<tr>
<td>11</td>
<td>7th data word</td>
</tr>
<tr>
<td>12</td>
<td>8th data word</td>
</tr>
</tbody>
</table>

WRITE MEMORY MESSAGE FORMAT
(EXEC. MSG. TYPE 28)
3.8 Interface Description

The following block diagram shows the relationship between the subprograms of the Xfer software module.
STEST
SYSTEEST
PROGRAM

RMP XFER
MODULE
(XFRMP)

CP
XFER
MODULE
(XFCP)

AP
XFER, MOD.
(XFAP)

SS
XFER, MOD.
(XFSS)

TC
XFER, MOD.
(XFTG)

INTERF ACE
DESCRIPTION
XFER