DEVELOPMENT AND EVALUATION OF MATH LIBRARY ROUTINES FOR 1/2 AIRBORNE MICROCOMPUTER (US AIR FORCE INST. OF TECH, WRIGHT-PATTERSON AFB OH. AFB INST. OF ENGI.) UNCLASSIFIED...

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DEVELOPMENT AND EVALUATION OF MATH LIBRARY
ROUTINES FOR A 1750A AIRBORNE MICROCOMPUTER

THESIS
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Preface

The purpose of this thesis was to develop, test, and evaluate the performance of run time math library routines for those architectures conforming to MIL-STD-1750A, the instruction set architecture for the airborne computers used in Air Force avionic weapon systems. The routines implemented include several algebraic functions that are intended to serve as a benchmark for future contractor development. Appendix A contains descriptions of the pseudo-operations used to explain the design of these functions, and will be useful in following the logic.

In developing and performing the evaluation of the math library, and in learning how to use the different support tools and hardware, I have had a great deal of help from others. In that respect I am deeply indebted to my thesis advisor, Dr. Panna Nagarsenker, for her continuing patience and assistance when I needed it. Capt Steve Hotchkiss has my undying gratitude for his friendship and help in these trying times. I also wish to thank Mr. Bobby Evans and Mr. Dale Lange, from the sponsoring organization, for all the help that they gave me in getting the needed equipment and outside information. Finally, I am eternally grateful to Tim for his unending love and encouragement.

Jennifer J. Fried
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Abstract

This project produced a run-time math library for the MIL-STD-1750A embedded computer architectures. The math library consists of the algebraic functions. In addition, the steps required for a performance analysis of the math library have been outlined.

Several approximation methods were investigated. The Chebyshev Economization of Maclaurin series polynomials, and rational approximations derived from the second algorithm of Remes were determined to be the best methods available. Each function's implementation was designed to take advantage of features of MIL-STD-1750A architectures. The recommended test procedures provide measures of the average and worst case generated errors within each approximation.
I. Introduction

Background

The Air Force is interested in reducing the life-cycle costs of its avionics weapon systems. Standardization of high order languages and an Instruction Set Architecture (ISA) are two of the many ways the Air Force can reduce these costs. In the past, a major cost contributor was the proliferation of unique avionics systems and subsystems. Costs increased with respect to purchasing and inventoring small-lot spares at many bases, training technicians to maintain complex and/or unique flight and test equipment, developing and maintaining software development facilities, training programmers to write application programs in seldom used high order languages, and training programmers to maintain software (especially operating systems) in seldom used machine languages. (1: 8.1)

MIL-STD-1750A defines a standard 16-bit instruction set architecture intended primarily for avionics weapon systems. The major cost advantage of this standard ISA comes in the form of common support software tools. An extensive set of support software tools has already been developed and includes a 1750A assembler/crossassembler, a J73 compiler with 1750A ISA code generator, a linker program, a loader program, and a 1750A acceptance test program. (1: 8.4) Other cost benefits are realized through the independent development of software and hardware, (2: 1) and common maintenance and test equipment. (3: 168)
Standardization of languages also has an impact on cost reduction. "In 1978, the Department of Defense had in its inventory, software written in about 150 different programming languages. This linguistic proliferation increased maintenance problems due to programmer training requirements and lack of support tools for many of the languages." (1: 6.1) The D.O.D. and Air Force recognized this as a problem, and they took steps to correct it. The D.O.D. Instruction 5000.31, "Interim List of D.O.D. Approved High Order Programming Languages," states that only approved languages may be used for new defense system software. JOVIAL is one of the few languages approved by this instruction.

As previously mentioned, the development of a standard ISA such as MIL-STD-1750A helps reduce total life-cycle costs for Air Force avionics weapon systems. This reduction is partially due to the use of common support software tools, many of which have already been developed. As was previously mentioned, one of the support software tools that has been developed is a JOVIAL compiler that generates 1750A ISA code. However, a math library containing the algebraic and trigonometric functions required by this language has not been developed. The sponsor for this thesis is the Aeronautics System Division, Language Control Branch. They are the D.O.D. JOVIAL and ADA compiler validation site, and are responsible for the development of such libraries. Completion of this thesis, with its development of a math library for software support of 1750A systems, can help the Air Force reduce avionics weapon systems costs.
Prior to the completion of this thesis effort, there were no math libraries written to take advantage of the 1750A instruction set. In keeping with the intent of recent standardization policies of both the D.O.D and Air Force, the library created by this thesis is written in the D.O.D approved language JOVIAL. Actual coding of the library was only a small part of this thesis. Most of the detail has gone into verification, validation, and evaluation of the product. As such, the focus of this report is divided into two primary categories: software development and software testing.

Math libraries are important because they provide the programmer several tools that serve as building blocks for applications. Math libraries prevent programmers from having to recreate each function whenever one is required for use. Libraries also provides a means for using functions that take full advantage of the computer architecture for which they were written.

The design of a procedure for computing the value of functions is not mathematically complete unto itself. An understanding of a computer architecture's operation is necessary to insure that the computation of any given function is as efficient as possible, while also providing the highest degree of accuracy. Such architectural considerations include word size, number of bits in both the exponent and coefficient fields of a floating point number, the way mathematical
operations are performed by the architecture, memory size of the architecture, and execution time. Other considerations include overflow, underflow, and precision. These considerations for the mathematic functions define some of the problems addressed by this thesis effort.

Scope

This effort was limited to the design, code, and evaluation of algebraic functions. The functions were included in a math library targeted for MIL-STD-1750A computer architectures, and are the ones typically found in most FORTRAN libraries. Specifically, these functions include square root (sqrt), exponential (exp), natural logarithm (alog), and common logarithm (alog10).

All functions have been written to accept and return only extended precision floating-point values. The specific floating-point functions are invoked by using the name given above.

Performance summaries for each of the functions, and algorithms are provided in Appendix E. They may be used to determine the polynomial coefficients for computing any of functions addressed by this paper. These algorithms produce coefficients that are valid for any nonvector architecture.
Assumptions

During a design review held in May of 1985, it was made clear that certain events could cause overflow and underflow errors, and division by zero. Since the functions are to be used within an embedded avionics weapon system, it is necessary that such conditions are detected and handled gracefully. The consensus of opinion from all participants of the design review was that the functions should not be aborted, and that default values should be returned. The error conditions and values returned are discussed in the individual design sections of this thesis. This constitutes an important assumption on how to handle such error conditions, and needs further investigation before implementation on a real-time system.

Another factor discussed during the design review was the distinction between fixed-point and floating-point functions. Floating-point functions have greater precision than fixed-point algorithms, but take longer to execute. Although the fixed-point functions are faster, the algebraic math routines and the JOVIAL computer language do not lend themselves to this method of calculation. Therefore, as stated earlier, only the floating-point algebraic math library functions have been implemented.

General Approach

The approach used during this thesis effort, is termed the "logicalized" model of a software system development cycle. This
approach was considered a better alternative than the more commonly used "waterfall" method of software system development. The "waterfall" method is composed of a neat, concise and logical ordering of a series of steps, each of which must be accomplished in order to obtain a final software product. These steps are performed in order and include systems analysis, requirements definition, preliminary design, detailed design, coding, testing, and implementation.

The "logicalized" model is similar to the "waterfall" model just described, but it is more concerned with the problem definition part of the cycle (see Figure 1). This approach is more useful in eliminating errors that typically occur during the requirements definition and design phases of the "waterfall" method. Errors generated during these phases typically occur because designers have a tendency to shift between abstract high-level design issues and physical implementation considerations. Thayer (5: 335-41) and Boehm et al. (6: 125-33) made it clear that these problems exist, and that design errors not only outnumber other errors, but that they are also more persistent. For this reason, more attention was given to the top-down decomposition and abstract (logical) modeling of this particular software system. Such a structured approach recommends a dichotomy between the logical design issues, and implementation issues.
Figure 1 Chart of: (a) Waterfall; and (b) Logicalized Model of a Software Development Cycle
Table 1 Information Flow of the Logicalized Software Development Cycle

<table>
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<tr>
<th>PHASE</th>
<th>INPUT</th>
<th>TASK</th>
<th>OUTPUT</th>
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<tr>
<td>ANALYSIS</td>
<td>Interviews, random data, and so on</td>
<td>Model problem and implied solution</td>
<td>Abstract model of implied solution</td>
</tr>
<tr>
<td>DESIGN</td>
<td>Abstract model of implied solution and environmental constraints</td>
<td>Model an implementable solution</td>
<td>Abstract model of implementable solution</td>
</tr>
<tr>
<td>CODE</td>
<td>Abstract model of an implementable solution</td>
<td>Implement solution</td>
<td>Executable solution</td>
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The information flow of a logicalized model is summarized in Table 1, and is "analogous to an artist's conception of a building, i.e. there is enough information to allow the customer and designer to communicate and to establish the building's pluses and minuses, but not enough detail to begin construction. A series of reviews, refinements, and the imposition of local building ordinances, for example, are necessary before construction can start." (7:14)

Therefore, the approach taken for this project was similar to that just described. The ASD/Language Control Branch established the requirements for a MIL-STD-1750A run-time math library written in the D.O.D approved high-order language JOVIAL. During a design review and
several other meetings, certain design considerations were refined. Then a “logical” model was established as a baseline. This was accomplished by using the refined problem statement, and researching the different methods for approximating the different algebraic functions.

The baseline model served as a reference from which all decisions regarding actual implementation could be made. Before proceeding to the next phase of development, two such decisions had to be made. These decisions were to determine which testing methods and which performance evaluation techniques would be used after coding was complete. These decisions determined what sort of tests would catch all possible errors, and determined what techniques could be used to establish a confidence level for the final product.

Up until this point, the abstract model has been devoid of any implementation considerations. However, after it was clear that the abstract design was complete and consistent with the requirements, it became necessary to consider changes to fit the problem into the MIL-STD-1750A environment. Before any changes could be made, it was necessary to complete the following steps: study the architecture and ISA defined by MIL-STD-1750A; determine what resources were available, such as software support tools and hardware; and then to learn how to use the available resources. From there, it was possible to develop an abstract model of an implementable solution. This model took advantage of those environmental factors that affected the speed and accuracy of computation for each function approximation.
The major subset of the logicalized software engineering methodology just described is called structured programming. Structured programming can be understood as the decomposition of a problem in order to establish a manageable problem structure. The highest conceptual level represents a general description of the problem, and each level of decomposition provides more detail into the problem. This decomposition is carried out until the problem is almost in coded form, and is often called a stepwise refinement of the problem. All implementation considerations are left until the lowest levels of refinement.

The goals of structured programming must be to minimize: the number of errors that occur during the development process; the effort required to correct errors in sections of code found to be deficient; the effort required to upgrade sections when more reliable, functional, or efficient techniques are discovered; and the life-cycle costs of the software. (8: 32) It must also reduce the complexity of the problem.

Structured flowcharting is a technique used to support these structured programming concepts and goals, and is “designed to reduce labels and unstructured branching, encourage a single entry/single exit approach, aid in the use of top-down design techniques, and enhance modularization. The approach encourages the designer to conceive of the system in high-level constructs and not in terms of individual detailed statements.” (7: 116) The structured flowcharting technique was used throughout the development of this project, not only because of the reasons just mentioned, but also for its simplicity and understandability from a reviewer standpoint.
Sequence of Presentation

This thesis addresses the design and performance evaluation of a run-time math library that is targeted to MIL-STD-1750A architectures. The requirements definition for this problem has already been discussed (Chapter 1 - Problem/Scope). The next topic discussed is the theoretical development of this thesis effort (Chapter 2). This is followed by a discussion of the detailed design considerations that were made during implementation of the library functions (Chapter 3). The last aspects covered in this report are the test and performance evaluation methods considered (Chapter 4) and conclusions (Chapter 5).

Appendices include algorithms useful for determining the pseudo-code operations used in the structured flowcharts (Appendix A), source listings for the implemented functions (Appendix B), support software developed in conjunction with this thesis (Appendix C), the VAX VMS command files required to compile, link, and run the developed product (Appendix D), and the coefficients for each of the functions (Appendix E).
II. Theoretical Development

General Discussion

The purpose of this thesis was to create and analyze algebraic functions developed for 1750A architectures. This chapter is concerned with the design theory of the algorithms used to approximate those functions. Within the given constraints, the emphasis for each of the designs is to compute results as quickly and as accurately as possible.

One way of computing a value quickly is to select an approximation that converges rapidly towards the value of the true function, \( f(x) \). There were several methods of approximation that were considered; however, the polynomial and relational approximations described by Cody and Waite (4: 17-84) were found to be the best. The coefficients given by Cody and Waite were derived by using Chebyshev Economization of the Taylor series for each function for the approximation itself, or as a starting point for computing a rational approximation via the second algorithm of Remes. An excellent reference for Chebyshev Economization is Conte and de Boor (9: 265-273), and an excellent reference for the second algorithm of Remes is Ralston. (10: 301-306)

Another means of reducing the amount of processing time required to compute a result is to take advantage of certain aspects of the computer’s architecture, as well as the different execution times for different instructions within the ISA. For example, incrementing the exponent field of a floating-point value is not only faster, but more accurate than the
equivalent operation of multiplying by two, or examining the sign bit of a variable is faster than comparing the entire value to zero. These techniques have been used, and are referenced in the design descriptions as pseudo-operations. These operations are equivalent to those described by Cody and Waite (4: 9), and are listed in Appendix A.

The accuracy of an approximation may be dependent upon the domain over which the function is approximated. For example, if the domain of an approximation is halved, the error may be reduced by a factor of about $2^{-(n+1)}$ for all polynomials of degree $n$. (11: 59) This can be shown to be true for most functions, but not all of them. Domain reduction has no effect on accuracy in approximations of certain functions; however, it still serves as an excellent guide when designing an application. This is due to the way computer architectures perform operations and store mathematical values for floating-point numbers. The most significant bits of a number are always maintained, and since only a finite number of bits are available to represent the value, it is possible that bits from a fractional representation may be lost during operations on large numbers.

**Approximation Techniques**

The MIL-STD-1750A ISA doesn’t call for the implementation of the elementary functions as standard instruction operators, so it is necessary to design software routines of optimum efficiency to replace them. The word "optimum" could be given a variety of precise definitions, but presumably it refers to an average execution time and storage space.
Unfortunately, there is no known way to derive or prove such an "optimal" design. For these reasons, the search for the appropriate approximation technique was limited to polynomial and rational approximations.

Some of the most popular methods of approximation used are called Chebyshev approximations. Chebyshev approximations are often referred to as "minimax" approximations because they are used to minimize the maximum "error" between the true function \( f(x) \), and the approximation of \( f(x) \). However, these methods of approximation are not without their problems, and there is a price, even though it is small, to be paid for using them. For example, the sum-of-squares of the errors in a Chebyshev approximation will be higher than if a least-squares method of approximation is used. However, since Chebyshev approximations assure that an error is never greater than a given amount, they were selected by this study.

**Polynomials.** The first class of approximations discussed are polynomials, and are the simplest of all the classes of approximations considered. The most important subclass of the polynomials is the class \( \tau_n \) (Chebyshev), and are polynomials not exceeding degree \( n \). The Chebyshev polynomials are especially important, and gave rise to the general concept of Chebyshev "approximations" discussed in the preceding paragraph.

The motivation for using Chebyshev polynomials over all other polynomials is their property of least maximum error, and their error behavior over the entire interval of the approximated function. Through the use of Theorem 1, the Alternation Theorem given below, Chebyshev was able to prove for all the polynomials of degree \( n \) with a leading
coefficient of 1, that the Chebyshev polynomial divided by $2^{n-1}$ has the least maximum error in the interval $[-1,1]$. In other words, no other polynomial of the type mentioned will have a smaller error than $\tau_n(x)/2^{n-1}$. In order for a polynomial $P_n(x)$ to be considered a Chebyshev approximation of the function $f(x)$, the theorem requires that the maximum discrepancy between $f(x)$ and $P_n(x)$ occur with alternating signs at $n+2$ points over the interval $[-1,1]$.

**Alternation Theorem:** The polynomial $P_n$ of degree $n$ that best approximates $f$ is characterized by the existence of at least $n+2$ "points of alternation".

The other motivation for the use of Chebyshev polynomials is that its generated errors are more well behaved than the errors generated by other polynomials. For example, approximations, based on the Maclaurin series whose interval includes zero, have errors that are very nonuniform -- small near the middle, but very large at the end points. It is more desirable to use an approximation whose behavior is more uniform instead of powers of $x$. Since, as stated in Theorem 1, the Chebyshev polynomials spread the error over the entire interval, they provide this more desirable behavior.

**Definition of the Chebyshev Polynomials.** The Chebyshev polynomials form an orthogonal set, and are defined by the following equation.

$$\tau_n(x) = \cos(n\theta) \quad \theta = \arccos(x) \quad n = 1, 2, \ldots$$

(1)
From elementary trigonometry, \( \cos(n\theta) \) is a polynomial of degree \( n \) in \( \cos(\theta) \), and \( \cos(\arccos(x)) = x \); therefore, it follows that the Chebyshev polynomials defined by \( T_n(x) = \cos(n \arccos(x)) \) are polynomials of degree \( n \).

By substituting \( \arccos(x) \) for \( \theta \) and \( T_n(x) \) for \( \cos(n \arccos(x)) \) in the identity function shown in equation (2), the recurrence relation defined in (3) is formed.

\[
\cos((n + 1)\theta) + \cos((n - 1)\theta) = 2 \cos(\theta) \cos(n\theta)
\]

(2)

\[
T_{n+1}(x) = 2x T_n(x) - T_{n-1}(x)
\]

(3)

Let \( T_0 = 1 \) and \( T_1 = x \), then from the recurrence relation defined in (3), successive polynomials of greater degree can be generated as in column \( A \) of Table 2.

By using the results in column \( A \) of Table 2, the powers of the Chebyshev polynomials can be found. That is, it is possible to express the powers of \( x \) in terms of \( T_n \). An example of the powers of \( T \) are shown in Table 2 column \( A \). Appendix A contains an algorithm that generates both the Chebyshev polynomials, and their powers.
Chebyshev Economization. As already mentioned, the Maclaurin series can be used to approximate many functions. In addition to the disadvantages that have already been mentioned for using this series as an approximation, the Maclaurin series also converges very slowly. That is, it takes several multiplications and additions to obtain a desired accuracy. One way of obtaining a lower degree polynomial, and still maintain the desired accuracy, is to use a technique that is called "telescoping" or "Chebyshev Economization". In other words, the polynomial can be expressed in a manner similar to that shown in (4).

\[ P_n(x) = d_0 \tau_0(x) + \ldots + d_n \tau_n \]  

(4)

To compute the economized polynomial approximation to the function \( f(x) \) of absolute accuracy \( \epsilon \) on the interval \([-1, 1]\), use the following procedure as outlined by Conte et.al. (9: 271-272)

Table 2  
(A) Chebyshev Polynomials; (B) Powers of Chebyshev Polynomials

<table>
<thead>
<tr>
<th>( A )</th>
<th>( B )</th>
</tr>
</thead>
<tbody>
<tr>
<td>( \tau_0 = 1 )</td>
<td>( x^0 = 1 )</td>
</tr>
<tr>
<td>( \tau_1 = x )</td>
<td>( x^1 = x )</td>
</tr>
<tr>
<td>( \tau_2 = 2x \tau_1 - \tau_0 = 2x^2 - 1 )</td>
<td>( x^2 = \frac{1}{2}(2x^2 - 1 + 1) = \frac{1}{2}(\tau_2 + \tau_0) )</td>
</tr>
<tr>
<td>( \tau_3 = 2x \tau_2 - \tau_1 = 4x^3 - 3x )</td>
<td>( x^3 = \frac{1}{4}(4x^3 - 3x + 3x) = \frac{1}{4}(\tau_3 + 3\tau_1) )</td>
</tr>
</tbody>
</table>
Step 1. Get a power series expansion for \( f(x) \) valid on \([-1, 1]\); typically, calculate the Maclaurin or Taylor series expansion for \( f(x) \) around \( x = 0 \).

Step 2. Truncate the power series to obtain a polynomial as in (5), which approximates \( f(x) \) on \([-1, 1]\) within an error \( \epsilon_a \), where \( \epsilon_a \) is smaller than \( \epsilon \), and \( \epsilon_a \) is defined as in (6). The result of \( \epsilon_a \) is the maximum absolute value, within the interval \([-1, 1]\), of the product of the first truncated coefficient, \( x \) to the power of \( n + 1 \), and the \( n + 1 \) derivative of the function \( f(x) \).

\[
P_a(x) = a_0 + a_1 x + \ldots + a_n x^n
\]  
(5)

\[
\epsilon_a = R_a(x) = a_{n+1} x^{n+1} f^{(n+1)}(x)
\]  
(6)

Step 3. By making use of a table similar to that shown in Table 2 column \( \mathcal{B} \), expand the polynomial \( P_a(x) \) into a Chebyshev series as defined in (4). In other words, substitute the far right-hand-side of the equations in Table 2 column \( \mathcal{B} \), with the appropriate powers of \( x \) contained in the polynomial formed by Step 2 of this algorithm. The result is similar to that shown in (7), but of a greater degree.
Step 4. Retain the first \( k + 1 \) terms in this series, i.e. find equation (7), choosing \( k \) as the smallest possible integer such that equation (8) holds true.

\[
P_k^*(x) = d_0 \tau_0(x) + \ldots + d_k \tau_k
\]

\[
\varepsilon_n + d_{k+1} + \ldots + d_a \leq \varepsilon
\]

Step 5. Convert the result of Step 4 into a power series polynomial similar to (5), by making use of a table similar to that in Table 2 column \( A \). In other words, substitute the right-hand-side values of Table 2 column \( A \), into the equation formed by Step 4. Simplify the result.

Rational Approximations. In most instances, rational approximations will generate a least maximum error that is as small or smaller than a Chebyshev polynomial, and will also cost less in terms of the number of multiplications and additions required to compute them. Therefore, they deserved attention in this study.

As stated earlier, the approximation techniques considered by this thesis are classified as Chebyshev approximations. These methods, through their exploitation of Theorem 1, provide approximations whose maximum error is less than those generated by other techniques. There are several algorithms that generate rational approximations that can be considered Chebyshev approximations; however, the ones that generate the
Input $f(x), m, k, [a, b]$

$C_0 ... C_N$

Calculate the Pade approximation $R_{mk}(x)$ and the members of the sequence.

Calculate the economized approximation $C_{mk}(x)$.

Calculate the minimum maximum error approximation by the algorithm in this section using $C_{mk}(x)$ as the initial approximation.

Figure 2 Calculation of Remes Rational Approximations

20
most uniform approximations are those generated by the second algorithm of Remes. This algorithm is easily automated, and is described in detail by the following subsection.

The Second Algorithm of Remes. The method used in this description is similar to that outlined by Ralston (10: 301-305), and is summarized in Figure 2.

Let \( f(x) \) be a continuous function that is to be approximated over the interval \([a, b]\), and let the interval include the point 0.0. Furthermore, let (9) equal the error of any rational approximation of the form shown in (10).

\[
\begin{align*}
\epsilon_{m,k}(x) &= \max |f(x) - R_{m,k}(x)| \\
\text{Step 1 of the algorithm names the input required for this algorithm.} \\
\text{The input value } f(x) \text{ is the function being approximated. If the algorithm is being run on a machine with higher precision than the error for which}
\end{align*}
\]
the function is being approximated, then the built-in functions of the
machine can be used for \( f(x) \). If the machine that the algorithm is to run
on is of the same precision for which the approximation is to be made,
then a reasonable substitute, such as a truncated power series that is of
equal or greater precision than what is being approximated, can be used.

The other inputs include: \( m \), \( k \), \([a, b]\), and \( C_0 \ldots C_N \). The values
\( m \) and \( k \) represent the degree of the polynomials found in the
numerator and denominator, respectively. The interval \([a, b]\) is the
interval for which the approximation is valid, and should include the point
zero, as it will allow the coefficient \( b_0 \), of the denominator, to always
be one. The values \( C_0 \ldots C_N \) represent the first \( N + 1 \) coefficients of
the power series polynomial that is being converted to a rational
approximation. The value \( N \) represents the sum of the degree of the
polynomials used in the numerator and the denominator \((m + k)\).

The second step of the algorithm is to compute a series of Pade
approximations and their error coefficients. The Pade approximations are
of the form depicted in (11), with the restrictions that \( 0 \leq i \leq m \) and
\( 0 \leq j - i \leq k \). For example, the sequence of Pade approximations computed
for an \( R_{2,2} \) approximation would only include \( R^{(0)}_{0,0}(x) \), \( R^{(1)}_{1,0}(x) \),
\( R^{(2)}_{1,1}(x) \), \( R^{(3)}_{2,1}(x) \), and \( R^{(4)}_{2,2}(x) \). The error of the approximations is
equal to the first power of \( x \) truncated from the power series, multiplied
by the error coefficients shown in (12). The error calculations used would
only include: \( d^{(0,0)}_1 \), \( d^{(1,0)}_2 \), \( d^{(1,1)}_3 \), \( d^{(2,1)}_4 \), and \( d^{(2,2)}_5 \).
The coefficients for each of the sequence of Padé approximations are computed using (13) and (14). Equation (13) forms a set of \( m \) linear equations, which when solved, determines the value of the coefficients used in the denominator. Those values can then be directly substituted into the set of equations formed by (14), and will determine the value of the coefficients for the numerator.

\[
\sum_{j=0}^{k} C_{N+s-j} b_j = 0 \quad s = 0, 1, \ldots, N - m - 1 \quad (C_j = 0 \text{ if } j < 0, \quad b_0 = 1)
\]

\[
a_r = \sum_{j=0}^{r} C_{r-j} b_j \quad r = 0, 1, \ldots, m \quad (b_j = 0 \text{ if } j > k)
\]

The third step of the Remes algorithm is to compute the economized approximation \( C_{m,k}(x) \). To complete this step, it is necessary to compute the Chebyshev polynomial \( \tau_{N+1} \). This polynomial can be determined by using equation (3) of the previous subsection. Once the coefficients of \( \tau_{N+1} \) are found, then the values \( \gamma \) from (15) can be directly substituted into (16), and thus solve \( C_{m,k}(x) \). The value \( t_j \) in (15) is the coefficient...
for $w^j$ in $\tau_{N+1}(u)$. The rational approximation must also be normalized, that is, the numerator and denominator must be divided by $b_0$, such that $b_0$ will remain equal to 1.

$$\gamma_0 = -d_{N+1}^{(m,k)} t_0 / 2^N \quad j = 0, \ldots, N - 1 \quad (15)$$

$$\gamma_{j+1} = \frac{d_{N+1}^{(m,k)}}{d_{j+1}^{(i,j-1)}} 2^N$$

$$C_{m,k}(x) = P_m(x) + \sum_{j=0}^{N-1} \gamma_{j+1} P_j(x) + \gamma_0$$

$$Q_k(x) + \sum_{j=0}^{N-1} \gamma_{j+1} Q_{j-1}(x)$$

The final step of the Remes algorithm is an iterative one. Now that the initial approximation to the function has been found, it becomes necessary to find the $N + 2$ points of alternation. This can be done through interpolation, or by dividing the interval into several small pieces and solving for each point on a division. This method works, and all that is necessary is a little bookkeeping to maintain a list of the $N + 2$ points of alternation. This step consists of the following three procedures.
Procedure 1. Solve the system of \( N + 2 \) equations for the \( N + 2 \) unknowns \( a_0^{(0)}, \ldots, a_m^{(0)}, b_1^{(0)}, \ldots, b_k^{(0)}, \) and \( E^{(0)} \) as shown in expression (17). Note that \( E^{(0)} \) is the magnitude of error in the approximation at each of the points \( x_1^{(0)} \), and for the first iteration can be assumed to be 0.

\[
\left[ x_1^{(0)} \right] - \frac{\sum_{j=0}^{m} a_j^{(0)} x_j^{(0)}}{\sum_{j=0}^{k} b_j^{(0)} x_j^{(0)}} = (-1)^n E^{(0)}
\]

Procedure 2. Find \( h_0(x) \) as shown in (18). The function \( h_0(x) \) then has a magnitude of \( |E^{(0)}| \) with alternating signs at \( x_i \), \( i=0, \ldots, N+1 \). In the neighborhood of each \( x_i^{(0)} \), there is a point \( x_i^{(1)} \) at which \( h_0(x) \) has an extremum of the same sign as that of \( f(x) - R^{(0)}_m(x) \) at \( x_i^{(0)} \). Replace each \( x_i^{(0)} \) by the corresponding \( x_i^{(1)} \). If \( x_i^{(0)} \), the point at which \( h_0(x) \) has its maximum magnitude, is one of the points \( x_i^{(1)} \), do not perform procedure 3. If not, replace one of the points \( x_i^{(1)} \) by \( x_i^{(0)} \) in such a way that \( h_0(x) \) still alternates in sign on the points \( x_i^{(0)} \).

\[
h_0(x) = f(x) - \frac{\sum_{j=0}^{m} a_j^{(0)} x_j^{(0)}}{\sum_{j=0}^{k} b_j^{(0)} x_j^{(0)}} [b_0^{(0)} = 1]
\]
Procedure 3. Repeat procedures 1 and 2 using the points $x_0^{(1)}, \ldots, x_{m+1}^{(1)}$ in (17). This process generates a sequence of rational approximations which will converge to an optimum if the initial extrema were sufficiently close.
III. Development and Design of the Functions

General Discussion

This chapter deals with the detailed design of each of the specific functions. Each design has an associated structured flowchart, and each box within the flowchart has been numbered for ease of reference. Pseudo-operations are used throughout each of the flowcharts, and includes those defined by Cody and Waite (4: 9-10). Furthermore, a few additional pseudo-operations have been introduced. (see Appendix A)

Although the approximation methods used are those suggested by Cody and Waite, the actual design implementations are significantly different. The designs proposed by Cody and Waite are guidelines for a broad class of computer, and weren't specifically targeted towards a 1750A architecture. Therefore, the designs have been tailored somewhat.

The coefficients for each of the functions were either taken from Cody and Waite, or are modifications of those provided by Cody and Waite. These modifications are discussed in their appropriate subsection.

Square Root Implementation

The square root of every non-negative floating point number "X" can be computed. Computation is composed of three steps: the reduction of
Figure 3: Square Root Structured Flowchart
the given argument "X" into the parameters "f" and "e" using base 2,

\[ X = f \times 2^e, \quad \frac{1}{2} \leq f < 1 \]  \hspace{1cm} (19)

\[ \sqrt{X} - \sqrt{f} \times 2^{e/2}, \quad \text{if } e \text{ is even} \]  \hspace{1cm} (20)

\[ \sqrt{X} - \left( \frac{\sqrt{f}}{\sqrt{2}} \right) \times 2^{(e+1)/2}, \quad \text{if } e \text{ is odd} \]  \hspace{1cm} (21)

the computation of \( \sqrt{f} \), and the reconstruction of \( \sqrt{X} \) from the results.

The variable "X" is the argument passed to the square root function. Since JOVIAL treats formal arguments as read only, upon program entry, the value of "X" is assigned to "F". (step 1 of Figure 3) The variable "F" is then used throughout the remainder of the procedure.

The next step is to check if "F" is either zero or one. (step 2 of Figure 3) If it is, then "F" is its own square root; therefore, this value is returned by the procedure. (step 3 of Figure 3). If "F" was neither a zero or a one, then it is checked to see whether it is negative. If it is negative, under normal circumstances, an error would be assumed, and the procedure would terminate rather than evaluating for a complex number. Due to the nature on embedded avionics systems, an error should not be fatal. In this light, rather than attempting to evaluate a complex number, the absolute value of the input argument is formed. (steps 4 and 5 of
Figure 3) The built-in function for absolute value was found to give inconsistent results, so the absolute value is found by: \( F = -F \). If this method of error correction proves inappropriate at a later date, it would not be difficult to modify. Perhaps a different default value should be assumed, or an error indicator could be established.

The next step of the algorithm is to obtain the exponent portion of the input argument. (step 6 of Figure 3) JOVIAL's specified tables make this an easy conversion. When the input argument was placed into "F", "F" had previously been established as a table whose elements identify the components of the floating-point number. Therefore, the item "Fexp" is actually the exponent portion of the floating-point number. Immediately following the extraction of the exponent, this same exponent portion of the floating-point number "F" is cleared or set to zero.

The next few steps (steps 7 through 10 of Figure 3) are to compute a polynomial approximation for \( \text{sqrt}(F) \). Specifically, the computation begins with an initial approximation of \( y_0 \) shown in (22) with successively more accurate approximations being obtained through the use of Newton's iteration in the form of Heron's formula.

\[
y_0 = \cdot41731 + .59016 \cdot f
\] (22)

\[
y_i = (y_{i-1} + f/y_{i-1}) / 2
\] (23)
The coefficients used in this algorithm are those presented by Cody and Waite. (4: 23) The approximation described by Cody and Waite is in the form shown in (23). Aside from the original calculation of \( y_0 \), by examining steps 8 through 10 of Figure 3, note that the Newton iteration is performed three times. Since each iteration doubles the number of correct significant digits in the square root, this assures an accuracy of 63.32 bits. (4: 23) The next step is to determine whether or not the exponent field from the floating-point number originally input was odd or even. (step 11 of Figure 3) Depending upon the result of this evaluation, different actions are taken. If the number is odd, additional calculations are necessary as shown in equation (21). The instruction for determining whether the number is odd is not a separate function. The power of the JOVIAL language permit testing of specific bits. Using this tool, the low-order bit can be checked to determine if it is zero or one. A zero signifies an even number and a one signifies an odd number which is just what the procedure checks. Given that multiplication is preferred, multiplications are more efficient than division. The calculation \( \sqrt{f} / \sqrt{2} \) is represented by \( y_j \cdot \sqrt{.5} \), where the decimal representation of \( \sqrt{.5} \) is the constant .7071067811865.

The final step prior to returning with the result is to form the exponent portion of the result. (step 13 of Figure 3)
Exponential Implementation

There are three steps in calculating the exponential of a floating-point number. The first step is the reduction of the given argument to a related argument in a small interval symmetric about the origin. The second step is the computation of the exponential for the reduced argument, and the final step is the reconstruction of the desired function from its components.

The exponential is formed using the following general procedure. Let

\[ X = N \cdot \ln(2) + g, \text{ with } |g| \leq \ln(2)/2 \quad \text{then} \]

\[ \exp(X) = \exp(g) \cdot C^N \]  

(24)

The accuracy of \( g \) is the basis for the accuracy of the function value. Let \( y = \exp(g) \), then \( \frac{dy}{y} = dg \). This means that the relative error in \( \exp(g) \) is approximately the absolute error in \( g \). This error is proportional to the magnitude of \( X \) when \( X \) is exact because of the finite word length of the computer. The only way to achieve small absolute error in \( g \) is to extend the effective precision of the computer during the computation of \( g \). In most cases, the following computation is used.
\[ g = \left( X_1 - N \cdot C_1 \right) \cdot X_2 - N \cdot C_2 \] where

\[ X_1 + X_2 = X. \]

\[ X_1 \text{ is the integer part of } X, \]

\[ C_1 + C_2 \text{ represents } \ln(C) \text{ to more than working precision} \]

This method gives extra digits of precision equivalent to the number of extra digits in the representation of \( \ln(C) \) when \( N \) is small enough that \( N \cdot C_1 \) is representable exactly in the machine. If this exact representation cannot be accomplished, the computation is equivalent to not using extra precision. Therefore, the magnitude of \( N \) has a practical limit which results in a limit on the magnitude of \( X \).

There is also a largest and smallest \( X \) such the \( \exp(X) \) can be represented in the machine. For example, if \( \text{SMALLX} \) is the smallest positive floating-point number and \( \text{BIGX} \) is the largest without causing overflow, then \( \exp(X) \) can be represented only for those values of \( X \) that between \( \ln(\text{SMALLX}) \) and \( \ln(\text{BIGX}) \). The value \( N \cdot C_1 \) will be representable exactly in a machine for any \( X \) within the specified bounds, because a \( C_1 \) can always be chosen to fit the bound. Obviously, careful argument reduction cannot compensate for inaccuracies in \( X \). (4:61)
The variable "Arg" is the argument passed to the square root function. Since JOVIAL treats formal arguments as read only, upon program entry, the value of "Arg" is assigned to "X". (step 1 of Figure 4) The variable "X" is then used throughout the remainder of the procedure.

The constant "BIGX" (see Table 3), which has been assigned a value that is slightly less than the natural logarithm of the largest positive finite floating-point number (step 2 of Figure 4), is compared with the input argument. If the argument is larger that this value, an error would occur during calculating its exponential. Since this application is destined for embedded avionics systems, a solution to this error situation must be found that does not result in a degradation of the system. The selected solution involves replacing the input argument with the constant "BIGX". Obviously, other possible options are available to resolve the error condition, and another solution can easily replace the existing methodology.

The constant "SMALLX" (see Table 3), which has been assigned a value that is slightly greater than the natural logarithm of the smallest positive finite floating-point number (step 4 of Figure 4), is compared

<table>
<thead>
<tr>
<th>BIGX</th>
<th>88.02969193111</th>
</tr>
</thead>
<tbody>
<tr>
<td>SMALLX</td>
<td>-89.41598629223</td>
</tr>
<tr>
<td>EPS</td>
<td>9.094947017729E-13</td>
</tr>
<tr>
<td>ONEOVERLN</td>
<td>1.4426950408890</td>
</tr>
<tr>
<td>LN2</td>
<td>0.6931471805599</td>
</tr>
</tbody>
</table>

Table 3 Constants for Exponential Determination
Figure 4 Exponential Structured Flowchart
with the input argument. If the argument is smaller than this value, an error would occur during calculating its exponential. Again the discussion in the previous paragraph concerning the resolution of an error condition in an embedded avionics application still holds true. The selected solution involves replacing the input argument with the constant "SMALLX". Obviously, other possible options are available to resolve the error condition, and another solution can easily replace the existing methodology.

The next step is to check if "X" is either larger than a positive eps or smaller than a negative eps. (step 6 of Figure 4) In either case, if it is, the exponential function returns a value of 1 and terminates processing. The value of eps (see Table 3) is selected with \( e^x = 1.0 \) to machine precision such that \(|X| < \text{eps}\) and \( p_1 \times X^2 \) will not underflow for \(|X| \leq \text{eps}\). Cody and Waite have suggested that \( \text{eps} = 2^{-t/2} \) where there are \( t \) base-2 digits in the significand.

The next step (step 8 in Figure 4) involves extracting the integer portion of the floating-point number that results from the following the calculation: \( X \times [1 / \ln(2)] \). As noted in the description of the square root function, multiplication is not as costly as division. Therefore, the value of \( 1 / \ln(2) \) has been calculated and used as a constant. (see Table 3) This newly formed integer is then transformed into a floating-point number. The JOVIAL specified table construct is put to use here rather than calling the two functions INTRND and FLOAT. Extraction of a specific portion of a floating-point number simply involves naming its component parts and using these names to access the needed part.
This particular construct is an extremely efficient method for doing this type of accessing, and is not confined to JOVIAL. It is also available in the Ada language.

The computation provided by Cody and Waite that is specified for no guard digits is use to create a new, more precise number.

\[ g = \left( (X_1 - XN \cdot C_1) \cdot X_2 \right) - XN \cdot C_2, \quad \text{where} \]

\[ X_1 = \text{the floating-point value of the integer portion of } X, \]
\[ X_2 = X - X_1, \]
\[ C_1 = 0.693359375, \]
\[ C_2 = -2.1219444005470E-4 \]

Now, that the value of the values of the X's and N's are known, equation (25) can be evaluated for \( g \). (step 9 in Figure 4) This is followed by the determination of the rational functions \( R(g) \) which approximate \( \exp(g) / 2 \). The factor of 0.5 is inserted to counteract wobbling precision. The calculation of the coefficients for the approximation are determined by the number of bits in the significand. For this architecture, the number of bits selected are between 30 and 42 inclusively. This results in the coefficient list of Table 4 on the next page.
Table 4 Coefficients for Polynomial Approximation to Exp

<table>
<thead>
<tr>
<th>Coefficient</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>p₀</td>
<td>99999 99999 999 E+0</td>
</tr>
<tr>
<td>p₁</td>
<td>25497 759 E-2</td>
</tr>
<tr>
<td>q₀</td>
<td>0.50000 00000 000 E+0</td>
</tr>
<tr>
<td>q₁</td>
<td>51764 522 E-1</td>
</tr>
<tr>
<td>q₂</td>
<td>0.29729 36368 224 E-3</td>
</tr>
</tbody>
</table>

The first step in calculating \( Rg \) (step 10 in Figure 4), requires the formation of \( g^2 \). This value is then used to form \( g \cdot P(z) \) and \( Q(z) \) using nested multiplication. These values are then used to form \( Rg \). Just prior to returning the value generated through all these calculations, an additional step is performed to rescale the number. (step 11 in Figure 4)

**Natural Logarithm Implementation**

The calculation of the logarithm required three steps. First, the given argument is reduced to a related argument in a small, logarithmically symmetric interval about one. The second step involves the computation of the logarithm for this reduced argument. Finally, the desired logarithm must be reconstructed from its components.
Upon entry into this routine, the value of the input is checked to see if it is either zero or less than zero. (step 1 of Figure 5) If it is either zero or negative, under normal circumstances, an error would be assumed, and the procedure would terminate. In this function, the negative value of the largest floating-point number is returned. (step 2 of Figure 5) As previously mentioned, due to the critical nature of embedded avionics systems, an error should not be fatal. It should provide an alternate path to a graceful completion of the function.

Many methods exist for calculating the logarithm of a reduced argument. Cody and Waite have chosen the following method. (4, 42) The initial assumption is made that the argument is in the following form

\[ X = \pm f \times 2^e, \text{ where } 0.5 \leq f < 1 \]

Determine the value of \( N \) and the scaled value of \( f \) such that

\[ X = f \times 2^N, \text{ where } 0.5 \leq f < 1 \]

Initially, \( f \) is assigned the value of the input argument. This allows for modification of the input floating-point number. Then an estimate for \( N \) is made. \( N \) is given the value of the exponent of the input floating-point number, and then this same exponent field is erased. (step 3 in Figure 5)

The value of \( \sqrt{0.5} \) has been previously determined and stored as a constant for use by this routine. Depending on the value of \( f \), one of two
Figure 5  Natural Logarithm Structured Flowchart
Table 5 Coefficients for Polynomial Approximation to Alog

<table>
<thead>
<tr>
<th>a0</th>
<th>0.3733916896316E+1</th>
</tr>
</thead>
<tbody>
<tr>
<td>a1</td>
<td>-0.6326086623386E+0</td>
</tr>
<tr>
<td>a2</td>
<td>0.4444551510980E-2</td>
</tr>
<tr>
<td>b0</td>
<td>0.4480700275574E+2</td>
</tr>
<tr>
<td>b1</td>
<td>-0.1431235435589E+2</td>
</tr>
<tr>
<td>b2</td>
<td>0.1000000000000B+1</td>
</tr>
</tbody>
</table>

distinct paths may be taken. The value of f is compared to the \( \sqrt{0.5} \), and znum and zdem will vary accordingly.

After forming \( z = \frac{znum}{zdem} \) and \( w = z^2 \), evaluate \( r(z^2) = w \cdot \frac{A(w)}{B(w)} \). Both A(w) and B(w) are polynomials in the w coefficients given in Table 5. (step 8 in Figure 5)

Common Logarithm Implementation

Obviously, from the structure chart for this function (Figure 6), all the work is done by the Alog function. Since the JOVIAL language does not support multiple entry points, the common logarithm function had to be formed in this manner. The result of this function is generated through the multiplication of the natural logarithm of the input argument with the natural logarithm of "e". This latter item is encoded as a constant to avoid wasted effort to recalculate for every use of this function. All the
restrictions that were imposed on the natural logarithm of a number also apply here.
IV Validation Verification and Performance Evaluation

General Discussion

This chapter is concerned with describing the methodology used for determining the correctness and performance qualities of the implemented functions. Due to problems in the availability of hardware and the associated support software, the testing and performance evaluations are somewhat limited. Hardware became available towards the middle of the thesis effort, but software tools used for development were incompatible with those required by the available 1750A. The loader used by the available 1750A equipment, expects files of a different format than what is created by the software development tools. Rather than developing a new loader, a routine was written that converts load modules into a format required by the 1750A loader. The reformatting procedure is listed in Appendix C.

Another problem that had to be overcome before testing and evaluation could be considered, was the availability of input/output (I/O) routines. Without I/O routines, further considerations for testing would be fruitless. No I/O packages were available, and as a consequence, had to be created. This delayed testing efforts considerably, as an I/O routine had to be developed with the use of the MIL-STD-1750A standard ISA, rather
than with a high-order language. The I/O package developed is listed in Appendix B, and is only capable of writing to a user console.

Performance analysis requires the comparison of 1750A results, with those generated on a machine of higher precision. Unfortunately, this requirement made the newly created I/O routine insufficient for this task. An available console driver has a routine that writes user specified areas of 1750A memory to magnetic disk. By storing a function's results in a specified area of 1750A memory, the test results can then be dumped to disk for an eventual upload to a VAX 11/780A. The results are then available for input to the different software test packages. However, the record format of the 1750A memory dump is not in a friendly format, and must be converted to a readable form. At the time of this writing, a routine for making the disk file readable is not completely debugged. However, it is at a point where it could be completed by another programmer.

The aforementioned problems have limited the amount of time available for designing extensive test procedures. Therefore, validation, verification, and performance analysis is confined to: manual static analysis methods, critical value testing, and measurement of each algorithms generated error.
Manual Static Analysis Methods

To most people, manual static analysis is called "desk checking". Static analysis involves the search for any inconsistencies between design tools (i.e. flowcharts), design details (chapter 3), program headers, and program comments. This method is useful for finding errors caused by the translation of design into code, as well as possible design errors. An inconsistency may indicate potential problems. This methodology was used, and all inconsistencies that were found were resolved.

Critical Value Testing

Critical value testing is an attempt to "break" the software, and requires the selection of specific arguments that could possibly cause problems. A knowledge of each of the algorithms is required to select proper arguments. Individual test cases are not listed here, but the reader may find specific information by examining the test procedures listed in Appendix B.

It is possible to generalize the tests performed without listing the specific test cases. Potential test arguments are those whose intermediate results could generate an overflow or underflow, or are arguments lying in the fringe of computational abnormality. These
arguments will help detect problem areas, and will give an indication as to how robust each function is.

In addition, arguments that test each path of the algorithm have been selected. Path testing is limited to insuring that every path of an algorithm is tested, and does not imply that every possible path combination is taken.

Performance Evaluation

As was mentioned in the introduction of this chapter, screen output to the user console and hard copies of computed results are insufficient for performance evaluation. Their use would imply a visual comparison of generated results against published tables. Such a technique limits the number of comparisons that could be made, and would cause doubt as to the credibility of the comparisons. At best, it would provide a good feeling for the quality of each function's performance. Therefore, it is better to automate the process completely, and compare the generated results against another machine generated standard.

The performance evaluation of the functions involves the computation of two important statistics: the maximum relative error (MRE), and the root-mean-square relative error (RE). Their values are determined through the use of (43) and (44), where \( F(x) \) is the test result and \( f(x) \) is the comparison value generated by the same extended-precision function call.
written for the VAX 11/780.

\[ MRE = \max_{x_i} \left| \frac{F(x_i) + f(x_i)}{f(x_i)} \right| \]  

(43)

\[ RE = \sqrt{\frac{1}{n} \sum_{i=1}^{n} \left( \frac{F(x_i) + f(x_i)}{f(x_i)} \right)^2} \]  

(44)

This method of error checking is an automatic tabular comparison, where the VAX routines serve as the accepted standard. The test routine tests densely packed samples of evenly spaced arguments spread throughout \([-3\pi, 3\pi]\) for floating-point algorithms, and \([-1, 1]\) for fixed-point algorithms. When regenerating arguments within the test modules, it is important not to introduce unnecessary errors. This means that arguments in the VAX should have its lower order bits padded with zeros. The most-significant bits must be equivalent to the number of bits in the 1750A argument, and no extra precision should be introduced.

The method of argument generation just described is recommended by Cody (12: 762), and is the method used at the NASA Lewis Research Center. This method is preferred to a random-number test because it measures the relative error throughout an entire interval. Using densely packed arguments also gives valuable insight to problems of different argument ranges. If the evenly spaced interval is set to a power of two (representable on both machines), and is not less than the
least-significant bit of the 1750A argument, then an initial argument can be chosen, such that, zero padding will only have to be performed once. For example, if an initial floating-point argument is \(-3.1415\) and the chosen interval is \(2^{-2}\), the second argument will be \(-3.1415 + 2^{-2}\). Additional padding is not necessary, because "carries" are cascaded forward and do not increase the number of most-significant bits in the next argument. Arguments used in the function calls on both machines must be the same, and must be generated in the same order.

Extra care is needed while reading the 1750A results from disk. Each of the 1750A results are stored in an unformatted file, and must be read into a binary record. This record is moved, bit-by-bit, to a variable of the appropriate type (VAX 11/780 fixed-point or floating-point). The bit-by-bit manipulation is accomplished through the use of JOVIAL specified tables, and prevents conversion errors associated with formatted input.

Before a comparison of the two results (one from the 1750A, and the other from the VAX) can be made, the results generated within the test module must be reduced to the same precision (same number of most-significant bits) as those from the 1750A. The precision reduction gives a rounded result that can be used to determine the MRE and RE, and will give a meaningful interpretation to the inherited error of the 1750A functions.
V Conclusions and Recommendations

Conclusions

The purpose of this thesis was to develop and to do performance evaluation on a run-time math library developed specifically for MIL-STD-1750A architectures. The library consists of the floating-point implementation of several algebraic functions. Performance evaluation was the major effort of this thesis, but not in the manner intended.

Function approximations are accomplished through the use of either Chebyshev or rational approximations. The two different approximation methods were discussed in chapter two, and are useful in understanding certain design considerations. The values of each polynomial's coefficients were derived by (or were modifications of those derived by) Cody and Waite. (4: 17-84) However, the implementation designs are significantly different from those suggested by Cody and Waite. The primary difference between the implemented designs and those suggested by Cody and Waite, are the methods of argument reduction required of each function.

Performance evaluation turned out to be the major effort, but not because of extensive or elaborate testing of the library functions. Most of the effort involved overcoming the following problems:
1.) There were several compiler bugs in the original 1750A compiler used. Assembly listings had to be reviewed, in order to verify each compilation of the source code.

2.) The use of a simulator for performance evaluation was ruled out because of the limited number instructions that could be simulated, its inability to simulate the use of floating-point data, and the relative speed at which results were calculated. The simulator also lacked a facility for writing results to mass storage. Storage of results on an external device is necessary for input to software test packages.

3.) A new compiler and linker was introduced near the midpoint of the thesis effort, and required a long learning curve in order to use them.

4.) Once a 1750A machine became available, it was determined that all its support software was intended for use with files created by the old compiler and linker.

5.) Rather than use a compiler and linker that had several deficiencies, or write a new loader routine, it was decided to write a support tool that would convert load modules into a format expected by the available loader.

6.) The reformatting program required the use of JOVIAL and its specified table features. It also required the use of FORTRAN routines to perform the I/O of source and target files. The FORTRAN and JOVIAL interfaces did not operate as expected, and the use of COMMON/COMPOOL areas wouldn't work. This required parameter passing between the routines, and the documentation for this type of interface was very inadequate; however, the problems were eventually resolved.
7.) The reformatting tool was written for use on a VAX 11/780. It was assumed that the JOVIAL compiler was free of bugs for a VAX target. However, when the reformat routine was being debugged, it was discovered that JOVIAL table names could be overlayed, but corresponding table items weren't overlayed with them. This problem took a long time to discover, and an additional amount of time to design around.

8.) I/O routines have not been written for the 1750A, and had to be developed. These routines are only capable of writing to a console screen.

9.) Screen output is insufficient for generating the thousands of results that would be needed during testing and evaluation, so another means of capturing the data had to be developed. Due to the lack of time and inexperience in the internal I/O communications techniques of the 1750A hardware, development of a disk I/O routine was not a feasible alternative. It was determined that results could be stored in specific locations of memory, and then an available console routine could be used to write the information to disk. An additional problem was encountered when it was discovered that the record format of the disk files is not in a VAX friendly format, and another routine had to be written to unpack the stored results.

These problems limited the scope of this thesis effort to developing the following: designs; code that is free of syntax errors; the development of command files for compiling, assembling, and linking routines written for the 1750A; tools for formatting load modules that are capable of being loaded into a Sperry 1631 implementation of the MIL-STD-1750A; and tools that unpack test results stored on an RT/11
formatted floppy disk. Generic test algorithms are provided, but are not written in a high-order-language. They provide the basic structure for critical range testing, and a means of evaluating and measuring each functions performance.

Recommendations

The products produced by this thesis effort are at point where design of the intended performance evaluation can begin. All the groundwork has been provided, and should be adequate for someone to continue the effort. Many of the aforementioned problems have been resolved, and support tools and command files are provided to shorten the learning curve that follow-on programmers will have to experience.

The following recommendations should be considered if this effort is continued.

1.) If the effort is limited to the use of JOVIAL, an analysis should be made for determining how to handle exceptions detected at run time. Exceptions include arguments outside legally defined limits.

2.) Since Ada has features for exception handling, all the library functions should also be developed and implemented in Ada.

3.) Another point may be in favor or using Ada is that it also allows the creation of generic packages and subprograms. The generic
subprograms define a template, and generic parameters provide the facility for tailoring the template to fit a particular need at translation time. In other words, one subprogram could provide calculations for both fixed-point or floating-point arguments, based on how it is used at compile time. Because a generic package would not be able to take advantage of the specific hardware functions unique to floating-point and fixed point routines, this may result in a degradation of performance.

4.) Initially, it was discussed that all the math library routines should be written in both JOVIAL and Ada with the intent that a comparative evaluation could be done on the two languages. Unfortunately, an Ada compiler targeted to the 1750A is not yet available. When a compiler does become available, it is recommended that a new Ada math library be developed and this comparative evaluation be performed.

5.) The compiler problems, mentioned above, should be corrected, and 1750A architectures and associated support software should be acquired before more time is allocated to the effort.
Appendix A

The following pseudo-operations were used in describing the implementation designs of the different mathematic functions.

**ADX(X,N):** augments the integer exponent of a floating-point representation of X by N. This scales the argument X by $2^N$.

For example,

$$\text{ADX}(1.0,2) = 4.0$$

**FIX(X):** returns the fixed-point representation of the floating-point value X. This operation requires explicit conversion in JOVIAL.

**FLOAT(X):** returns the floating-point representation of the fixed-point argument X. This operation requires explicit conversion in JOVIAL.

**ODD(X):** determines whether the argument X is odd. For an integer, the least-significant bit is checked directly. For a floating-point number, the integer portion is checked. A description of the floating-point process for this determination is given below.
To determine whether the integer portion is odd, knowledge of the internal representation of the 1750A floating-point number is necessary. The argument X is a JOVIAL specified table item that makes the components shown in Figure 7 easily accessible. Within this table is an integer item that overlays the exponent field of X. This exponent field is the tool needed to check whether the integer portion is odd or even. Since X has a value of one or greater, and all floating-point values are normalized, the exponent can be used to point to the least significant bit of the integer field. Because X is positive, a one in the least significant bit would indicate the integer portion is odd. A limit on the maximum value of the coefficient has been imposed by the functions that use this routine. This limit prevents the least-significant bit of the integer portion from falling in the exponent or "LSB" area of the floating-point coefficient (see Figure 7).
Since the 1750A architecture requires that all floating-point values be normalized, the most-significant bit is in the first bit position following the sign bit. The decimal-point is assumed to be positioned immediately behind the sign bit, but immediately in front of the most-significant bit. The exponent represents a power of two; therefore, if $c$ represents the value of the exponent field, the value of the floating-point number is: coefficient $\times 2^c$. Equivalently, it is obvious that the decimal-point floats $c$ places to the left if $c$ is negative, or $c$ places to the right if positive.

Knowledge of how floating-point numbers are stored can be used to determine whether the integer portion of a number is odd. The following example gives an explanation of the process.

Given the following machine representation of a floating-point number, determine whether its integer portion is odd. In the example below, the decimal-point was inserted only for clarity.

```
0.11000000000000000000000000000000 100000000000000000000000000000000
```

Since the sign bit of the exponent is zero, the value of the coefficient is positive. The following two numbers are summed together to determine the value represented by this coefficient:
The exponent field is in bold text, and has the value one. Therefore, the value of this floating-point representation is, the coefficient (.75) multiplied by two to-the-power-of the exponent (1), or 1.5.

\[ .75 \times 2^1 = 1.5 \]

Another way to compute the result is to shift the decimal-point in a direction as indicated by the exponent. The exponent in this case is +1, so the decimal-point is shifted one position to the right. The number can then be computed in a similar manner as described above.

This last method demonstrates how to determine whether this example is even or odd. If the decimal-point is shifted 1 position to the right, this number will have 1 integer bit and 38 fractional bits. The integer bits always occupy the left-most position of the number. If the exponent is thought of as a pointer from the left-most side of the number, the least-significant integer bit can be found. The exponent in this example points to bit position one. Since the bit is set to 1, this example's integer value is odd. \[ \]
**INT(X):** return the integer portion of the floating-point argument X. The description ODD(X) given above determines the least-significant bit of the integer portion of the floating-point argument. This is used to extract the entire integer portion of the argument (bits 0 through the least significant bit).
DATE: 30 August 1985
VERSION: 1.0
NAME: ALog
MODULE NUMBER: 1.0
DESCRIPTION: This function is called to compute the natural log of
of the argument 'Arg'. Since
\[ \log_{10}(X) = \frac{\ln(X)}{\ln(10)} \]
ALog is also called by ALog10 to do its computations.

PASSED VARIABLES: Arg - an extended precision floating-point variable

RETURNS: The natural log of arg in extended precision float

MODULES CALLED: None

AUTHOR: Capt. Jennifer Fried

HISTORY: This project was undertaken as a thesis project for
partial fulfillment of requirements for an MS degree
in Information Science from the Air Force Institute
of Technology. Sponsoring organization is the ASD
Language Control Branch, Wright Patterson AFB, Oh.

START

DEF PROC ALog RENT(Arg) F 39;
BEGIN
  ITEM Arg F 39;
  ITEM Nn S 7;
  ITEM Xn F 39;
  ITEM Znum F 39;
  ITEM Zden F 39;
  ITEM Zz F 39;
  ITEM Rz F 39;
  ITEM Rz2 F 39;
  ITEM We F 39;
  ITEM We F 39;
  ITEM We F 39;
  ITEM We F 39;
  ITEM We F 39;
  ITEM We F 39;

  TABLE Overlays (0) W 3;
BEGIN
  ITEM Ff F 39 POS(0,0);
  ITEM Fexp S 7 POS(8,1);
END

  CONSTANT ITEM Zero F 39 = 0.0;
  CONSTANT ITEM P\Five F 39 = 0.5;
  CONSTANT ITEM Sqr-UpFive F 39 = 0.7071067615859785;
  CONSTANT ITEM A0 F 39 = 0.37339158976318E+1;
  CONSTANT ITEM A1 F 39 = -0.63260865233658E+0;
  CONSTANT ITEM A2 F 39 = 0.4444551510980E-2;
  CONSTANT ITEM B0 F 39 = 0.4480700275574E+2;
  CONSTANT ITEM B1 F 39 = -0.1431235435669E+2;
  CONSTANT ITEM B2 F 39 = 0.1000000000000E+1;

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CONSTANT ITEM C1 F 39 = 0.69335983750000;
CONSTANT ITEM C2 F 39 = -2.1219444006949E-4;

IF Arg <= Zero;
   ALog = -HUGEFLOAT(39);
ELSE
   BEGIN
      Ff(0) = Arg;
      Mn = Fexp(0);
      Fexp(0) = 0;

      IF Ff(0) > SqrtPtFive;
         BEGIN
            Znum = (Ff(0) - PtFive) - PtFive;
            Zden = (Ff(0) * PtFive) + PtFive;
         END
      ELSE
         BEGIN
            Znum = Ff(0) - PtFive;
            Zden = (Znum * PtFive) + PtFive;
         END

      Zz = Znum / Zden;
      We = Zz * Zz;

      Aw = (A2 * We + A1) * We + AO;
      Be = (We + B1) * We + BO;
      Rz2 = We * Aw / Be;
      Rx = Zz + Zz * Rz2;

      Xn = (* F 39 *) * Mn;
      ALog = (Xn + C2 + Rx) + Xn * C1;
   END

RETURN;
END
TERM
**DATE:** 30 August 1985

**VERSION:** 1.0

**NAME:** ALog10

**MODULE NUMBER:** 1.0

**DESCRIPTION:**
This subroutine is called to compute the base 10 log of the passed argument. Since
\[ ALog10 = ALog \times \log(e) \]
It makes a call to ALog.

**PASSED VARIABLES:** Arg - an extended-precision floating-point variable

**RETURN:** The floating-point rep of log(Arg)

**MODULES CALLED:** ALog

**AUTHOR:** Capt. Jennifer Fried

**HISTORY:** This project was undertaken as a thesis project for partial fulfillment of requirements for an MS degree in Information Science from the Air Force Institute of Technology. Sponsoring organization is the ASD Language Control Branch, Wright Patterson AFB, Oh.

```fortran
START

REF PROC ALog RENT(Arg) F 39;
BEGIN
 ITEM Arg F 39;
END

DEF PROC ALog10 RENT(Arg) F 39;
BEGIN
 ITEM Arg F 39;
CONSTANT ITEM Log'e' F 39 = 0.4342944819033;

ALog10 = ALog(Arg) * Log'e';
RETURN;
END
```
Returns the extended-precision floating-point value for $e^{\text{Arg}}$

**Passed Variables:** Arg - an extended precision floating-point variable

**Returns:** $e^{\text{Arg}}$

**Modules Called:** None

**Author:** Capt. Jennifer Fried

**History:** This project was undertaken as a thesis project for partial fulfillment of requirements for an MS degree in Information Science from the Air Force Institute of Technology. Sponsoring organization is the ASD Language Control Branch, Wright Patterson AFB, Oh.

```
START

DEF PROC Exp REHNT (Arg) F 39;
BEGIN
  ITEM AR0 F 39;
  ITEM X0 F 39;
  ITEM Xn F 39;
  ITEM G0 F 39;
  ITEM X1 F 39;
  ITEM X2 F 39;
  ITEM Zn F 39;
  ITEM P0 F 39;
  ITEM Qz F 39;
  ITEM M0 S 7;

TABLE Overlays (0) W 3;
BEGIN
  ITEM Arg F 39 POS(0,0);
  ITEM Rexp S 7 POS(0,1);
END

CONSTANT ITEM Xmax F 39 = 1.701411834599E+38;
CONSTANT ITEM Xmin F 39 = 1.469387338527E-39;
CONSTANT ITEM Xbig F 39 = 88.0296919231111;
CONSTANT ITEM Xsmall F 39 = -88.41598629223;
CONSTANT ITEM Eps F 39 = 9.09494701729E-13;
CONSTANT ITEM P0 F 39 = 0.24999999999999E+0;
CONSTANT ITEM P1 F 39 = 0.5950425497759E-2;
CONSTANT ITEM Q0 F 39 = 0.50000000000000E+0;
CONSTANT ITEM Q1 F 39 = 0.5356751764522E-1;
CONSTANT ITEM Q2 F 39 = 0.2972936368224E-3;
```

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CONSTANT ITEM C1    F 39 = 0.693359375;
CONSTANT ITEM C2    F 39 = -2.1219444005470E-4;
CONSTANT ITEM Ln2    F 39 = 0.6931471805599;
CONSTANT ITEM OneOverLn2 F 39 = 1.4426950408889;
CONSTANT ITEM One    F 39 = 1.0;
CONSTANT ITEM PtFive F 39 = 0.5;

Xx = Arg;
IF Arg > Xbig;
    Xx = Xbig;
ENDIF
IF Xx < Xsmall;
    Xx = Xsmall;
ENDIF
IF (Xx < Eps) AND (Xx > -Eps);
    Exp = One;
ELSE
    BEGIN
        Mn = (* F 39 *) (Xx * OneOverLn2);
        Xn = (* F 39 *) (Mn);
        X1 = (* F 39 *) (Xx);
        X2 = Xx - X1;
        Qg = (X1 - Xn * C1) + X2 - Xn * C2;
        Zx = Gg * Gg;
        Pz = (P1 * Zx + P0) * Gg;
        Qz = (Q2 * Zx + Q1) * Zx + Q0;
        Rg(0) = PtFive + Gg * Pz / (Qz - Pz);
        Rexp(0) = Rexp(0) + Mn + (* F 39 *) (1);
        Exp = Rg(0);
    END
RETURN;
END

START

DEF PROC Sqrt REPLY(Xx) F 39;
BEGIN

TABLE Overlays (0) W 6;
BEGIN
ITEM Ff F 39 POS(0,0);
ITEM Fexp S 7 POS(0,1);
ITEM Vy F 39 POS(0,3);
ITEM Vexp S 7 POS(0,4);
END
ITEM Xx F 39;
ITEM Nn S 7;
ITEM Nn S 8;
ITEM Nbit B 16;
OVERLAY Nn : Nbit;

CONSTANT ITEM SqrtOneHalf F 39 = 0.70710678118665;
CONSTANT ITEM C1 F 39 = 0.414213562373095;
CONSTANT ITEM C2 F 39 = 0.590163462874552;
CONSTANT ITEM One F 39 = 1.0;
CONSTANT ITEM Zero F 39 = 0.0;
CONSTANT ITEM OneInt S 7 = 1;

Ff(O) = Xx;
IF (Ff(O) = Zero) OR (Ff(O) = One);
Sqrt = Ff(O);
ELSE
BEGIN
IF Ff(O) < Zero;
Ff(O) = -Ff(O);
\[ \text{Mn} = \text{Fexp}(0); \]
\[ \text{Fexp}(0) = 0; \]
\[ \text{Yy}(0) = \text{C1} + \text{C2} \times \text{Ff}(0); \]
\[ \text{FOR} \ i = 1 \text{ BY } 1 \text{ WHILE } i \leq 3; \]
\[ \text{BEGIN} \]
\[ \text{Yy}(0) = \text{Yy}(0) + \text{Ff}(0) / \text{Yy}(0); \]
\[ \text{Yexp}(0) = \text{Yexp}(0) - \text{Oneint}; \]
\[ \text{END} \]
\[ \text{IF} \ \text{BIT}(	ext{Mbit},15,1) = \text{1B'1'}; \]
\[ \text{BEGIN} \]
\[ \text{Yy}(0) = \text{Yy}(0) \times \text{SqrtOneHalf}; \]
\[ \text{Mn} = \text{Mn} + 1; \]
\[ \text{END} \]
\[ \text{Mn} = \text{Mn} / 2; \]
\[ \text{Yexp}(0) = \text{Yexp}(0) + \text{Mn}; \]
\[ \text{Sqrt} = \text{Yy}(0); \]
\[ \text{END}; \]
\[ \text{RETURN;} \]
\[ \text{END} \]
\[ \text{TERM} \]
DATE: 19 JULY 1985
VERSION: 1.0
NAME: MathLib
MODULE NUMBER: 1.0
DESCRIPTION:
This compool is required by any JOVIAL program that needs to
reference any of the math functions written for floating-point
or fixed-point computations
PASSED VARIABLES: N/A
RETURNS: N/A
MODULES CALLED: N/A
AUTHOR: Capt. Steven A. Hotchkiss and
        Capt. Jennifer Fried
HISTORY: This project was undertaken as a thesis project for
         partial fulfillment of requirements for an MS degree
         in Information Science from the Air Force Institute
         of Technology. Sponsoring organization is the ASD
         Language Control Branch, Wright Patterson AFB, Oh.

START

COMPOOL MathLib;

REF PROC Exp Rext(Arg) F 39;
  BEGIN
  ITEM Arg F 39;
  END

REF PROC ALog Rext(Arg) F 39;
  BEGIN
  ITEM Arg F 39;
  END

REF PROC ALog10 Rext(Arg) F 39;
  BEGIN
  ITEM Arg F 39;
  END

REF PROC Sqrt Rext(Arg) F 39;
  BEGIN
  ITEM Arg F 39;
  END

REF PROC Sin Rext(Xx) A 1,30;
  BEGIN
  ITEM Xx A 1,30;
  END

REF PROC Cos Rext(Xx) A 1,30;
  BEGIN
  ITEM Xx A 1,30;
  END

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REF PROC ATanf RENT(xx) F 39;
BEGIN
ITEM xx F 39;
END

TERM
* DATE: 29 August 1985
* VERSION: 1.0
* NAME: IoRefs
* MODULE NUMBER: 1.0
* DESCRIPTION:
  * This Compool is necessary to reference routines that were
  * necessary for testing and performance evaluation of all math
  * functions developed for the 1750.
* PASSED VARIABLES: N/A
* RETURNS: N/A
* MODULES CALLED: N/A
* AUTHOR: Capt. Steven A. Hotchkiss and
  * Capt. Jennifer Fried
* HISTORY: This project was undertaken as a thesis project for
  * partial fulfillment of requirements for an MS degree
  * in Information Science from the Air Force Institute
  * of Technology. Sponsoring organization is the ASD
  * Language Control Branch, Wright Patterson AFB, Oh.

START

COMPOOL IoRefs;

* The following ITEMS are required to print a carriage return and
* line feed on a terminal connected to a MIL-STD-1750 computer

DEF ITEM Carriage STATIC U 16 = 2573;
DEF ITEM CALF STATIC C 2;
OVERLAY Carriage: CALF;

* The following referenced subroutine is written in 1750 Assembly language
* and is used to print character strings only. Noncharacter types will
* have to be converted before calling this routine. The following DEFINE is
* recommended for all routines calling ObcSim:

  DEFINE WRITE_STRING(A) '"Printc(WORDSIZex1A),LOC(1A))"';

* An example of a typical call follows:

  ITEM Example C 2;

  * WRITE_STRING(Example);

REF PROC Printc PRINTC(Length, Message);
BEGIN
ITEM Length U (BITSWORD-1);
ITEM Message P;
END

* The following referenced routine is necessary for routines wishing
  to convert floating-point values to a character string

REF PROC FItToChar (Arg) C 20;
BEGIN
ITEM Arg F 39;
END

* The following referenced routine is necessary for routines wishing
  to convert fixed-point values to a character string. The variable
  IntOverlay must be overlayed on top of a fixed-point variable and
  BitsInFrac is an integer value indicating the number of fractional
  bits in the fixed-point value.

REF PROC FixToChar (IntOverlay, BitsInFrac) C 20;
BEGIN
ITEM IntOverlay S 31;
ITEM BitsInFrac U 8;
END

TERM
This routine is used to convert fixed-point values into character representation. This routine was necessary for testing and performance evaluation of math routines developed for the 1750.

**Passed Variables:**
- `IntOverlay` - An integer variable overlayed on top of a fixed-point value.
- `BitsInFrac` - The number of fractional bits of the fixed-point argument.

**Returns:**
- A 20 character representation of the argument.

**Modules Called:**
- `FixToChar`

**Author:**
- Capt. Steven A. Hotchkiss and Capt. Jennifer Fried

**History:**
This project was undertaken as a thesis project for partial fulfillment of requirements for an MS degree in Information Science from the Air Force Institute of Technology. Sponsoring organization is the A&G Language Control Branch, Wright Patterson AFB, Oh.

---

START

REF PROC `FixToChar(Arg) C 20;`
BEGIN
ITEM Arg F 39;
END

"************ FixToChar Procedure ************"

DEF PROC `FixToChar(IntOverlay, BitsInFrac) C 20;`
BEGIN
ITEM IntOverlay S 31;
ITEM BitsInFrac U 8;
TABLE Overlays (O) W 3;
BEGIN
ITEM Arg F 39 POS(0,0);
ITEM ArgExp S 7 POS(8,1);
END

Arg(O) = (* F 39 *)(IntOverlay);
ArgExp(O) = ArgExp(O) - (* S 7 *)(BitsInFrac);

FixToChar = FixToChar(Arg(O))
RETURN;
END

TERM
This routine is used to convert floating-point values into character representation. This routine was necessary for testing and performance evaluation of math routines developed for the 1730.

PASSED VARIABLES: Arg - the value to be converted
RETURNS: a 20 character representation of the argument

AUTHOR: Capt. Steven R. Hotchkiss and Capt. Jennifer Fried

HISTORY: This project was undertaken as a thesis project for partial fulfillment of requirements for an MS degree in Information Science from the Air Force Institute of Technology. Sponsoring organization is the ASD Language Control Branch, Wright Patterson AFB, Oh.

START

DEF PROC FitToChar (Arg) C 20;
BEGIN
DEFINE Yes "1B'1';
DEFINE No "1B'0';
ITEM Arg F 39;
ITEM Fraction F 39;
ITEM Temp F 39;
ITEM Result C 20;
ITEM lx U 8;
ITEM ly U 8;
ITEM ExpCnt U 8;
ITEM NegExp B;
ITEM CharVal U 8;
ITEM CharRep C 1;
OVERLAY CharRep: CharVal;
ITEM ZeroRep STATIC C 1 = '0';
ITEM ZeroVal STATIC U 8;
OVERLAY ZeroRep: ZeroVal;
CONSTANT ITEM Zero F 39 = 0.0;
CONSTANT ITEM One F 39 = 1.0;
CONSTANT ITEM TenFloat F 39 = 10.0;
CONSTANT ITEM PtFive F 39 = 0.5;
CONSTANT ITEM PtOne F 39 = 0.1;
RESULT = '0.000000000000E+00';

IF Arg < Zero;
  BEGIN
    Fraction = -Arg;
    BYTE(Result,0) = '-';
  END
ELSE
  Fraction = Arg;

IF Fraction < PtOne;
  NegExp = Yes;
ELSE
  NegExp = No;

ExpCnt = 0;
WHILE (Fraction > One);
  BEGIN
    ExpCnt = ExpCnt + 1;
    Fraction = Fraction / TenFloat;
  END

IF (NegExp = Yes) AND (Fraction <> Zero);
  BEGIN
    BYTE(Result,17) = '-';
  END

WHILE (Fraction < PtOne);
  BEGIN
    ExpCnt = ExpCnt + 1;
    Fraction = Fraction * TenFloat;
  END

ly = 0;
WHILE ((Fraction <> Zero) AND (ly < 13));
  BEGIN
    Temp = Fraction * TenFloat;
    IF ly = 12;
      Temp = Temp + PtFive;
    CharVal = (* U 8 *)( Temp );
    Fraction = Temp - (* F 39 *)( CharVal );
    CharVal = CharVal + ZeroVal;
    BYTE(Result,ly+3) = CharRep;
    ly = ly + 1;
  END

CharVal = (* U 8 *)(ExpCnt MOD 10) + ZeroVal;
BYTE(Result,19) = CharRep;
CharVal = (* U 8 *)(ExpCnt / 10) + ZeroVal;
BYTE(Result,18) = CharRep;

FltToChar = Result;
RETURN;
END

TERM
This module is called to print a character string onto a console that is connected to a Mil-Std-1750 computer.

DESCRIPTION:

This module is called to print a character string onto a console that is connected to a Mil-Std-1750 computer.

PASSED VARIABLES:

- LENGTH - this variable contains a count of the number of characters to print
- MESSAGE - this is a location pointer for the string to be printed

RETURNS:

prints messages on user console

MODULES CALLED:

This module uses the following modules:

- PRINTC
- LOOP
- LABEL
- DATA
- LOOPTEST

AUTHOR:

Capt. Steven A. Hotchkiss and Capt. Jennifer Fried

HISTORY:

This project was undertaken as a thesis project for partial fulfillment of requirements for an MS degree in Information Science from the Air Force Institute of Technology. Sponsoring organization is the ASD Language Control Branch, Wright Patterson AFB, Oh.

---

$ 4-SEP-85/15:09:29 $ PRINTOFF . DO NOT LIST METAS

* START OF META DEFINITIONS *

<table>
<thead>
<tr>
<th>DATAS</th>
<th>LR'IA</th>
<th>3</th>
<th>. REPEATED PRESET META</th>
</tr>
</thead>
<tbody>
<tr>
<td>LF(0)</td>
<td>EQU</td>
<td>$</td>
<td></td>
</tr>
<tr>
<td>LOOP</td>
<td>2,1,NUM(GF)-1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>UO(0)</td>
<td>GF(...),TEST,3,DATAS</td>
<td></td>
<td></td>
</tr>
<tr>
<td>GOTO</td>
<td>TEST</td>
<td></td>
<td></td>
</tr>
<tr>
<td>NORMA</td>
<td>LABEL</td>
<td></td>
<td></td>
</tr>
<tr>
<td>DATA</td>
<td>GF(...)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>TEST</td>
<td>LOOPTEST</td>
<td></td>
<td></td>
</tr>
<tr>
<td>_DATAS</td>
<td>META</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>LOOP</td>
<td>1,1,GF(...),1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>DATA</td>
<td>GF(...)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LOOPTEST</td>
<td>_</td>
<td></td>
<td></td>
</tr>
<tr>
<td>MEND</td>
<td>LENGTH</td>
<td>25,9999</td>
<td></td>
</tr>
</tbody>
</table>

* SECTION META 0 . CSECT META

<table>
<thead>
<tr>
<th>=SLKX...</th>
<th>CSECT</th>
<th>LOOP</th>
<th>2,1,31</th>
</tr>
</thead>
</table>
* GENERATE REG EQUATES META
* REG META
XNC LOOP 0,1,15
META EQU XNC
LOOPTEST META
MEND
*
* END OF META DEFINITIONS
*
*
* BASE REG EQUATES
*
B12 EQU 12
B13 EQU 13
B14 EQU 14
B15 EQU 15
*
* CONDITION CODE EQUATES
*
TEMP EQU 0
LT EQU 1
LE EQU 2
GT EQU 3
GE EQU 4
LM EQU 5
LMGE EQU 6
LMGE EQU 7
LEQ EQU 8
LGEQ EQU 9
GEQ EQU 10
LGEQ EQU 11
LGEQ EQU 12
LGEQ EQU 13
LGEQ EQU 14
LGEQ EQU 15
*
* END OF EQUATES
*
REG SECTION PRINT
DEFINE PRINTC
PSSDATA$ EQU 3
PSSCMD$ EQU 4
PSSCODE$ EQU 2
* NO DATA DECLARATIONS
* 10 BARE/TYPE/ABSOLUTE DECLARATIONS
* LOCAL AUTOMATIC DATA *** SIZE IN WORDS — 2 DECIMAL : 2 hex ***
* LOCAL AUTOMATIC DATA FOR PROC PRINTC
* STACK FRAME *** SIZE IN WORDS — 2 DECIMAL : 2 HEX ***
BK_DOSREF EQU HEX(0) SIZE = 2
LENGTH_3 EQU HEX(0) SIZE = 1
MESSAGE_3 EQU HEX(1) SIZE = 1
* EN() OF LOCAL AUTOMATIC DECLARATIONS
* PSECT $DATA IS EMPTY

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R2 = NUMBER OF CHARACTERS IN STRING
R3 = LOCATION OF CHARACTER STRING

PSSCODE ORIGIN HEX(0)

PRINTC EQU $1
ORIGIN HEX(0002)

AISP R2,1 . ADJUST CHARACTER COUNT
SRA R2,1 . 1ST TWO COMMANDS EQUIVALENT TO
. R2 = ROUND(R2/2)
BLE LB__0002 . BRANCH OUT IF ILLEGAL CHAR COUNT

OUTPUT EQU $2
XIO R5,ACS . READ CONSOLE STATUS
TSR 1,R5 . CHECK STATUS BIT 1
BEZ OUTPUT . IF OFF, LOOP BACK UNTIL CONSOLE READY
L R5,0,R3 . GET NEXT TWO CHARACTERS OF MESSAGE
XIO R5,C0 . PRINT BOTH CHARACTERS
AISP R3,1 . POINT TO NEXT TWO CHARACTERS
SOJ R2,OUTPUT . DECREMENT LOOP COUNT, GO BACK IF MORE

LB__0002 EQU $3
AISP R15,2
POP R2,R3
URS R15
ORIGIN HEX(0000)
PSHM R2,R3
SISP R15,2
ORIGIN HEX(0013)

END
This routine is used to convert ITS LINK files into a format that can be loaded into the SPERRY 1031 computer (1750A architecture). The ITS files are '.SO' files and must be in the 80 column record format described in the EMAD ITS Load Module ICD (CDAL #1005 contract #F30607-83-C-0244). Use of the command file LINK1750.COM to link all compiled modules will ensure that these records are of the right format. The format of the SPERRY loader records are defined in Appendix B of its programmer reference manual. The bytes of all binary data fields must be swapped (i.e. the high order bits of a word are swapped with the low order 8). The only type ITS records converted are binary and end record types. It also ignores all protection indicators, and can not handle expanded memory jobs. When all object files are copied into a single object for linking by the ITS LINKER, the main procedure must be copied into the file first!!!!!!! Otherwise, this application will have no way of determining the point that execution is to begin. The 'end' record created by the ITS linker contains the lowest address of the load module, and this application assumes that the routine begins at that point. The ITS file contains data fields that are in HEX character representation, and the SPERRY 1031 expects binary data fields; therefore the ITS data must also be converted to binary.

START

!*COMPOOL ('loData');
!*COMPOOL ('loCalls');
!*COMPOOL ('RATHCpl');
PROGRAM RefMat;

BEGIN
ChkSum = 48 '0000';
FirstPass = True;
LoDPT = -1;
Eof = False;
Buff = 0;
BuffPtr(0) = 1;
BuffPtr(1) = 1;

" Initialize 10 Files "
IntFil;

" Get Header Info for Loader File "
GetHdr;

WHILE NOT Eof;
BEGIN

" Read the first 80 column record "
ReadFil(ltsRecEof);

" Put Loader info into contiguous Memory Locations "
LdInRec = Cnt(0) - Ascii0;
AddrC(0) = Addr(0);
AddrR(0) = Addr2(0);
AddrS(0) = Addr3(0);
AddrH(0) = Addr4(0);
AddrD(0) = Addr5(0);
AddrE(0) = Addr6(0);
AddrF(0) = Addr7(0);

" Initialize the Output Buffers "
FOR lx: 1 BY 1 WHILE lx<33;
  CharToBin(lx) = 0;
END

FOR lx: 0 BY 1 WHILE lx<63;
  OutBuff(lx) = 0;
END

" Convert Char To Bin and Pack it "
FOR lx: 1 BY 1 WHILE lx<32;
BEGIN
  IF (Ascii0<=CharToBin(lx)) AND (CharToBin(lx)<=Ascii9);
    CharToBin(lx) = CharToBin(lx) - Ascii0;
  ELSE
    IF (Ascii9<=CharToBin(lx)) AND (CharToBin(lx)<=AsciiF);
      CharToBin(lx) = CharToBin(lx) - Ascii9 + 10;
    HalfByte(lx) = Nibbles(lx);
END

IF Typ(0) = '.';
BEGIN "This is a binary record"
  IF BuffPtr(Buff) + LdInRec <=61 AND LoDPT = LAddr(0);
  BEGIN " Old Record and still room for more data fields "
END

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Flip Flop the position of each byte of a 1750A word

FOR Ix: 0 BY 1 WHILE Ix < WdslnRcd;
BEGIN
  BufByte0(BufPtr(Buff)+Ix) = FieldL(Ix+1);
  BufByte1(BufPtr(Buff)+Ix) = FieldH(Ix+1);
END

Point to where info from next ITS 80 column record
is to be placed into this loader record
BufPtr(Buff) = BufPtr(Buff) + WdslnRcd;

Update load point so the next ITS record can be checked to
see if it belongs in this loader record
LdPt = LdPt + WdslnRcd;

IF BufPtr(Buff) = 61;
BEGIN " Loader Record is full and needs to be written "
  WdslnBuffer = 60;
  RcdTyp1 = AsciIB;
  WriteRcd;
END

ELSE
BEGIN " Old record and not enough room -- or new record "
  IF LdPt = Laddr(O);
  BEGIN " Same loader record, but not enough room for all "
    " data fields in ITS record"
    " Swap Bytes of words going into loader record "
    FOR Ix: 0 BY 1 WHILE BufPtr(Buff)+Ix < 61;
    BEGIN
      BufByte0(BufPtr(Buff)+Ix) = FieldL(Ix+1);
      BufByte1(BufPtr(Buff)+Ix) = FieldH(Ix+1);
      LdPt = LdPt + 1;
    END
    " write the full record out "
    WdslnBuffer = 60;
    RcdTyp1 = AsciIB;
    WriteRcd;
    " Set the load point for this new loader record "
    LdRcd(O) = LdPt;
  ELSE
    " Swap bytes of the other ITS data fields and place them into "
    " record. If the next ITS record doesn't have the load point "
    " computed here, it should be the first entries for another "
    " loader record"
    FOR Iy: Ix BY 1 WHILE Iy < WdslnRcd;
    BEGIN
      BufByte0(BufPtr(Buff)+Iy) = FieldL(Iy+1);
      BufByte1(BufPtr(Buff)+Iy) = FieldH(Iy+1);
      LdPt = LdPt + 1;
    END
END
END "some record not enough room"

ELSE

BEGIN "this is the start of a new loader record"

IF NOT FirstPass;
BEGIN

IF BufPtr(Buff) <> 1;
BEGIN "the last record didn't get filled up, so it hasnt been written yet. The routine WriteRcd sets BufPtr to 1 before exit"

RecTyp1 = AsciiB;
WdsInBuffer = BufPtr(Buff) - 1;
WriteRcd;
END
END "end not first pass"

FirstPass = False;

"set the load point for this loader record"
LdPt = Laddr(O);

"swap bytes of its data fields going into loader record"
FOR lx: 0 BY 1 WHILE lx < WdsInRcd;
BEGIN
BufByteO(lx+1) = Field(lx+1);
BufByte1(lx+1) = Field(lx+1);
END

BufPtr(Buff) = WdsInRcd + 1;
LdPt = Laddr(O) + WdsInRcd;

END "end new record"

END "end of old record not enough room — or new record"

END "end of this is a binary record"

ELSE

BEGIN "this is an execution address record"
IF Typ(O) = 'E';
BEGIN
RecTyp1 = 8261; "blank E"
OutBuff(O) = Laddr(O);
OutBuff(l) = 30; "ascii record separator"
WriteRcd;
END
END "end execution address record"

END "end while loop"

"write end of file loader record"
RecTyp1 = 8262; "blank F"
OutBuff(0) = 30; " ascii record separator "

" Clean up Files used "
ClnUp;

END
TERM
This routine is called by RefMat to do 10 stuff that needs to be done throughout the main procedure. Three types of SPERRY 1031 loader records are written: Binary, Execution, and End of file. If the record type is a binary record, this routine computes a checksum for it and tacks it on to the end of the record. Then the record type is written out followed by the binary record. If the record type is an execution record or an end of file record, the record type is written out followed by the record. The variable 'Buff' is a global variable that points to the record to be written.

START

CALLeref('RefCpl');
CALLeref('IoData');

REF PROC Printf(RcdTyp,Buffer);
LINKAGE FORTRAN;
BEGIN
ITEM RcdTyp S 15;
ITEM Buffer C 126;
END

DEF PROC WriteRcd;
BEGIN
LoopCnt = ldsinBuffer;
If RcdTyp = AscllB;
BEGIN
ChkSum = 4B'0000';
OutBuff(0) = lrdRd(0);
ChkSum = ChkSum XOR OutBuff(0);
OutBuff(1) = ldsinBuffer;
ChkSum = ChkSum XOR OutBuff(1);

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FOR lx: 1 BY 1 WHILE lx <= LoopCnt;
BEGIN
    OUTBUFF(lx+1) = Bufld(lx);
    ChkSum = ChkSum XOR OutBuffB(lx+1);
END

    OutBuffB(lx+1) = ChkSum;
    Printf(RedTypI,OutFld);
END
ELSE
    Printf(RedTypI,OutFld);

    BufPtr(Buff) = 1;
    Buff = ABS(1-Buff);

RETURN;
END
TEAM
START

COMPOOL loCalls;

REF PROC WriteRcd;
   BEGIN
   END

REF PROC GetHdr;
   ILINKAGE FORTRAN;
   BEGIN
   END

REF PROC Readf(:ItsRcd,Eof);
   ILINKAGE FORTRAN;
   BEGIN
   ITEM ItsRcd C 90;
   ITEM Eof B 1;
   END

REF PROC Printf(RcdTyp, Buffer);
   ILINKAGE FORTRAN;
   BEGIN
   ITEM RcdTyp S 15;
   ITEM Buffer C 128;
   END

REF PROC CInUp;
   ILINKAGE FORTRAN;
   BEGIN
   END

REF PROC IntFill;
   ILINKAGE FORTRAN;
   BEGIN
   END

TERM
START

COMPOOL IoData;

DEF ITEM Infil  C 10;
DEF ITEM Outfil  C 10;
DEF ITEM Fline  C 6;
DEF ITEM Header  C 80;

DEF TABLE ItsTable(0) W 20;
BEGIN
  ITEM Addr  C 4  POS(18,00);
  ITEM Typ  C 1  POS(18,01);
  ITEM Cnt  C 1  POS(24,01);
  ITEM Cnt1 S 7  POS(24,01);
  ITEM Wd1 C 4  POS(08,03);
  ITEM Wd2 C 4  POS(16,05);
  ITEM Wd3 C 4  POS(24,07);
  ITEM Wd4 C 4  POS(00,10);
  ITEM Wd5 C 4  POS(08,12);
  ITEM Wd6 C 4  POS(16,14);
  ITEM Wd7 C 4  POS(24,16);
END
DEF ITEM ItsRcd C 80;
OVERLAY ItsRcd: ItsTable;

DEF TABLE OutRcd (0:62) T 16 H;
BEGIN
  ITEM OutBuff S 15 POS(0,0);
  ITEM OutBuffB B 16 POS(0,0);
END
DEF ITEM OutFid C 128;
OVERLAY OutRcd: OutFid;

DEF ITEM Eof B 1;
DEF ITEM RcdTyp1 S 15;
DEF ITEM RcdTyp C 2;
OVERLAY RcdTyp1: RcdTyp;

OVERLAY Infil, Outfil, Flnam, Header, ltsRcd, OutFld, Eof, RcdTyp;

TERM
DEVELOPMENT AND EVALUATION OF MATH LIBRARY ROUTINES FOR A 1750A AIRBORNE MICROCOMPUTER(U) AIR FORCE INST OF TECH WRIGHT-PATTERSON AFB OH SCHOOL OF ENGI.
MICROCOPY RESOLUTION TEST CHART
This compool contains all the variables and tables that are used to unpack ITS linker records, packs them and converts the HEX characters to binary data fields, and then places them into a SPERRY 1631 loader record format.

```
START

COMPOOL RnntCpl;

DEF ITEM ChkSum B 16;
DEF ITEM FirstPass B 1;
DEF ITEM LdPt S 15;
DEF ITEM Buff U 8;
DEF ITEM IX U 8;
DEF ITEM ly U 8;
DEF ITEM WdslnAcck S 15;
DEF ITEM LoopCnt S 15;
DEF ITEM WdslnBuffer S 15;
DEF ITEM Zero STATIC C 1 = '0';
DEF ITEM AsciiO STATIC S 7;
OVERLAY Zero: AsciiO;
DEF ITEM Nine STATIC C 1 = '9';
DEF ITEM Ascii9 STATIC S 7;
OVERLAY Nine: Ascii9;
DEF ITEM AA STATIC C 1 = 'A';
DEF ITEM AsciiA STATIC S 7;
OVERLAY AA: AsciiA;
DEF ITEM FF STATIC C 1 = 'F';
DEF ITEM AsciiF STATIC S 7;
OVERLAY FF: AsciiF;
DEF ITEM BB STATIC C 2 = 'B';
DEF ITEM AsciiB STATIC S 15;
OVERLAY BB: AsciiB;
```
DEF TABLE LoadPoint (0);
    BEGIN
        ITEM LowAd S 15;
    END

DEF TABLE BufStuf (0:1);
    BEGIN
        ITEM BufPtr S 7;
    END

DEF TABLE PackedRcd (0) W 8;
    BEGIN
        ITEM AddrC C 4 POS(0,0);
        ITEM Md1C C 4 POS(0,1);
        ITEM Md2C C 4 POS(0,2);
        ITEM Md3C C 4 POS(0,3);
        ITEM Md4C C 4 POS(0,4);
        ITEM Md5C C 4 POS(0,5);
        ITEM Md6C C 4 POS(0,6);
        ITEM Md7C C 4 POS(0,7);
    END

DEF TABLE CharConvert (1:32) T 8 W;
    BEGIN
        ITEM CharToBin S 7 POS(0,0);
        ITEM Nibbles S 3 POS(4,0);
    END

OVERLAY PackedRcd: CharConvert;

DEF TABLE HexBuf (1:32) T 4 W;
    BEGIN
        ITEM HalfByte S 3 POS(0,0);
    END

DEF TABLE Pakits (0:7) T 16 W;
    BEGIN
        ITEM Laddr S 15 POS(0,0);
    END

DEF TABLE BinFields (0:7) T 16 W;
    BEGIN
        ITEM Field S 15 POS(0,0);
        ITEM FieldH S 7 POS(0,0);
        ITEM FieldL S 7 POS(8,0);
    END

OVERLAY HexBuf,ix: Pakits: BinFields;

DEF TABLE DatFields (0) W 1;
    BEGIN
        ITEM BufByte0 S 7 POS(0,0);
        ITEM BufByte1 S 7 POS(8,0);
        ITEM BufAd S 15 POS(0,0);
    END

TERM
Subroutine IntFll

IMPLICIT INTEGER (A-Z)

CHARACTER*5  Filnam
CHARACTER*10  Infil, Outfil

WRITE(*,*)' Enter File Name (Max 6 Characters)'
READ(*,10)Filnam

10 FORMAT(A)

I = INDEX(Filnam,'.') - 1
IF (I.LT.0) THEN
   I = INDEX(Filnam,' ') - 1
   IF (I.LT.0) THEN
      I = 6
   ENDIF
ENDIF

Infil = Filnam(I:1)//'.SO'
Outfil = Filnam(I:1)//'.DAT'
WRITE(*,*)'Input File = ',Infil,'Output file = ',Outfil

OPEN(UNIT = 2, NAME = Infil, TYPE = 'OLD', FORM = 'FORMATTED')
OPEN(UNIT = 3, NAME = Outfil, TYPE = 'NEW', FORM = 'UNFORMATTED')
END
Subroutine GetHdr

```fortran
IMPLICIT INTEGER (A-Z)

INTEGER*2 Spacer1
CHARACTER*80 Header
CHARACTER*1 RS
DATA RS/30/

Spacer1 = 0
WRITE(*,*), 'Enter Optional 1 Line Header Text'
Read(*,10)Header
10 FORMAT(ABO)
WRITE(*,*), Header
WRITE(3), Header//RS
DO 20 I=42,64
   WRITE(3,Spacer1)
20   CONTINUE

END
```
Subroutine Printf(RecTyp, OutFid)

IMPLICIT INTEGER (A-Z)

CHARACTER*2 RecTyp
INTEGER*2 OutFid(1:63)

WRITE(3,RecTyp,(OutFid(1), l = 1,63))
WRITE(*,*)'Write next record'

END
Subroutine Readf(ItsRcd, Eof)

IMPLICIT INTEGER (A-Z)

CHARACTER*80 ItsRcd
LOGICAL*4 Eof

Eof = .FALSE.

READ(2,10, END = 20) ItsRcd

10 FORMAT(ABD)

WRITE(*,*)ItsRcd

GOTO 30

20 Eof = .TRUE.

30 CONTINUE

END
This routine is called by the routine ReMkat to close the files it used for I0.

PASSED VARIABLES: None

RETURNS:

MODULES CALLED: None

GLOBAL VARIABLES: All variables used are global, and are defined in the common (COMPOOL) called loData.

AUTHOR: Capt. Steven A. Hotchkiss and Capt. Jennifer Fried

HISTORY: This project was undertaken as a thesis project for partial fulfillment of requirements for an MS degree in Information Science from the Air Force Institute of Technology. Sponsoring organization is the ASD Language Control Branch, Wright Patterson AFB, Oh.
$ | ASM1750 — Assemble a 1750 source module
$ |
$ | file
$ |
file = input source name of module  file.Sl
$ |
$ | Create Assembler input file UI that designates MIL-Std-1750A as the target
$ | rather than the alternate 1750A target
$ |
$ CREATE 'P1'.UI
| ASSEMBLE TARGET=M1750A
$ TV 'P1'.UI
$ |
$ | ASSIGN 'P1'.UI  UI  | UPDATE INPUT COMMANDS FILE (INPUT)
$ | ASSIGN 'P1'.SI  SI  | 1750A ASSEMBLY SOURCE FILE (INPUT)
$ | ASSIGN 'P1'.OBJ  DO  | OBJECT OUTPUT
$ | ASSIGN 'P1'.SO  SO  | SYMBOLIC OUTPUT
$ | ASSIGN 'P1'.LD  LD  | LISTING OUTPUT
$ | ASSIGN LIB_JOVIAL_1750A  01  | LIBRARY INPUT
$ |
$ | SET VERIFY
| M1750A
$ |
$ | DEASSIGN SI
$ | DEASSIGN UI
$ | DEASSIGN DO
$ | DEASSIGN SO
$ | DEASSIGN LD
$ | DEASSIGN 01
$ |
$ | DELETE 'P1'.UI;*
$ | SET NOVERIFY
$ | JOU1750 — JOVIAL COMPILE FOR MIL-STD-1750A TARGET
$ | JOU1750 file [.filetype] [options]
$ | e.g., @JOU1750 TEST1 .SRC /SYNTAX_ONLY/STATISTICS
$ | @JOU1750 TEST2 /MACHINE_CODE/CROSS
$ |
$ | Note: if the filetype is JOV, options may be typed as 2nd parameter.
$ | If a filetype is supplied, it must be preceded by a "@" as shown.
$ |
$ | Resulting object module has type .OBJ
$ |
$ | SET VERIFY
$ | JOVIAL 'P1' 'P2'/TARGET=1750A/NOINFO/CROSS/ASSEM'P3'
$ LINK1750 — Link one or more 1750A target object modules.
$ LINK1750 file
$ file = object file (containing one or more object modules)
$ create object file by first deleting all .obj files for
$ COMPOOLs that don't contain any DEFs. Then use the
$ following commands to create the object file
$ ! COPY *.OBJ file.O
$ ! RENAME file.O file.OBJ
$ ! OBJ files created by the compiler and the assembler can be copied to the
$ ! same OBJ file, but the WAX will give an incompatible files warning. Ignore
$ ! the warning, the copy is made anyway
$ ! Create Linker input file "UI"
$ SET VERIFY
$ CREATE 'P1'.UI
.LINK DATA, LIST, DEBUG, INPUTS
ALLOCATE LOCATION=1000 MODULES.
LINKEND
$ ASSGN 'P1'.UI UI LINKER CONTROL (INPUT)
$ ASSGN 'P1'.OBJ 00 OBJECT MODULE(S) (INPUT)
$ ASSGN 'P1'.SO SO LOAD MODULE (OUTPUT)
$ ASSGN 'P1'.LO LO LINKER LIST FILE (OUTPUT)
$ ASSGN LIB.JAVIAL.1750A 01 LIBRARY OBJECT FILE (INPUT)
$ ITSLINK 1750A Linker...reads logic device UI
! Output on SO and LO
$ DEASSIGN UI
$ DEASSIGN 00
$ DEASSIGN SO
$ DEASSIGN LO
$ DEASSIGN 0I
$ !
$ DELETE 'P1'.UI;*
$ SET NOVERIFY
LOGIN.COM  This command procedure is invoked with each login.
and may be changed to tailor your environment.

Set standard aliases. Note that several UNIX-like aliases are set up.

SET NOVERIFY
SET PROTECTION=(SYSTEM:R,OWNER:RWED, GROUP:RW, WORLD:RWE)/DEFAULT
$SYMBOLS:
  $  BQ    := SHOW QUEUE/BATCH
  $  CD    := SET DEFAULT
  $  DS    := DIRECTORY /SIZE
  $  E     := EDIT
  $  HOME  := SET DEFAULT DSK$ADOL:[ADOL.HOTCHA]
  $  LO    := @LOGOUT.COM
  $  LS    := DIRECTORY
  $  PQ    := SHOW QUEUE SYS$PRINT
  $  PS    := SHOW PROCESS    ! Like UNIX ps command
  $  PND   := SHOW DEFAULT    ! Like UNIX penv command
  $  R     := RNUM
  $  SD    := SHOW DEVICES
  $  SG    := SHOW SYMBOLS /GLOBAL /ALL
  $  ST    := SHOW TERMINAL   ! Like UNIX who command
  $  WHO   := SHOW USERS      ! Like UNIX who command
  $  SHQ   := SHOW QUEUE SLAM$QUEUES/ALL
  $  S80   := SET TERMINAL/WIDTH=80
  $  S132  := SET TERMINAL/WIDTH=132
  $  JOU1750 := @JOU1750
  $  LINK1750 := @LINK1750
  $  SIM1750 := @SIM1750
  $  ASM1750 := @ASM1750
  $  UNLOCK := @UNPROTECT

End user defined keyins.

$ ! DEFINE JOURLIBRARY FOR AUTOMATIC SEARCHING FOR VAX TARGET
$ ! ASSIGN JOURLIB:JOURLIB.OLB LINK$LIBRARY
$ ! The following defines the 1750A support tools pseudo-commands:
$ !
$ ! LINK30A := LINKITS
$ ! RAIDX := $TOOLS:RAID
$ !
$ ! END LOGIN.COM
$ !
$FINISH:
$    EXIT

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Appendix E
with TEXT_10;
use TEXT_10;
with TCHEBSHEF_PACKAGE;
use TCHEBSHEF_PACKAGE;

procedure TCHEBSHEF_ECONOMIZATION is

-- This procedure is the main driver for the Tchebyshef economization
-- of a polynomial.

ECONOMIZED_POLYNOMIAL: FLOAT_VECTOR (0..MAX_DEGREE) :=
(0..MAX_DEGREE => 0.0);
-- The is the resulting economized coefficients to the polynomial
SUM: FLOAT_VECTOR (0..MAX_DEGREE) := (0..MAX_DEGREE => 0.0);
-- This value is a temporary work area for the sum of the columns
-- of the work matrix
WORK_MATRIX: FLOAT_MATRIX (0..MAX_DEGREE, 0..MAX_DEGREE) :=
(0..MAX_DEGREE => (0..MAX_DEGREE => 0.0));
-- Temporary work area for forming the economized coefficients
procedure DISPLAY_VECTOR (PRINT_VECTOR: in VECTOR) is
   --The sole purpose of this routine is to display an integer vector
   package INT_10 is new INTEGER_10 (integer);
   use INT_10;

begin
   --Display Vector.
   for I in 0..DEGREE_OF_POLYNOMIAL loop
      put (I);
      put (" ");
      put (PRINTVECTOR (I));
      new_line;
   end loop;
   end DISPLAYVECTOR;
procedure DISPLAY_FLOAT_VECTOR (PRINTVECTOR: in FLOAT_VECTOR) is
   -- The sole purpose of this routine is to display a floating point vector
   package INT_10 is new INTEGER_10 (integer);
   use INT_10;
   package FLT_10 is new FLOAT_10 (float);
   use FLT_10;

begin
   -- Display Float Vector.
   for I in 0..DEGREE_OF_POLYNOMIAL loop
      put (I);
      put (" ");
      put (PRINT_VECTOR (I));
      new_line;
   end loop;
   end DISPLAY_FLOAT_VECTOR;
procedure DISPLAY_MATRIX (PRINT_MATRIX: in MATRIX) is
--The sole purpose of this routine is to display an integer matrix

package INT_10 is new INTEGER_10 (integer);
use INT_10;
package FLT_10 is new FLOAT_10 (float);
use FLT_10;

begin  --Display Matrix.
    for I in 0..DEGREE_OF_POLYNOMIAL loop
        put (I);
        put (' ');
        for J in 0..DEGREE_OF_POLYNOMIAL loop
            put (PRINT_MATRIX (I,J));
            put (' ');
        end loop;
        new_line;
    end loop;
end DISPLAY_MATRIX;
procedure DISPLAY_FLOAT_MATRIX (PRINT_MATRIX: in FLOAT_MATRIX) is
—The sole purpose of this routine is to display a floating point matrix

  package INT_10 is new INTEGER_10 (integer);
  use INT_10;
  package FLT_10 is new FLOAT_10 (float);
  use FLT_10;

  begin —Display Float Matrix.
    for I in 0..DEGREE_OF_POLYNOMIAL loop
      put (I);
      put (" ");
    for J in 0..DEGREE_OF_POLYNOMIAL loop
      put (PRINT_MATRIX (I,J));
      put (" ");
    end loop;
    new_line;
  end loop;
end DISPLAY_FLOAT_MATRIX;
begin --Tchebyshev Economization.

INPUT_COEFFICIENTS;
put ("Input Coefficients");
new_line;
DISPLAY_FLOAT_VECTOR (COEFFICIENTS);

COMPUTE_TCHEBYSHEF_POLYNOMIAL;
new_line;
put ("Tchebyshef Polynomial");
new_line;
DISPLAY_MATRIX (TCHEBYSHEF_POLYNOMIALS);

COMPUTE_POWERS_OF_TCHEBYSHEF;
new_line;
put ("Powers of Tchebyshef");
new_line;
DISPLAY_FLOAT_MATRIX (POWERS_OF_TCHEBYSHEF);

--Generate the work matrix used in the final calculations of the economized polynomial. Again the matrix is lower triangular.
for I in 0..DEGREE_OF_POLYNOMIAL loop
  for J in 0..I loop
    WORK_MATRIX (I,J) := float(MULTIPLIER(I)) * POWERS_OF_TCHEBYSHEF (I,J) * COEFFICIENTS (I);
  end loop;
end loop;

--Accumulate the sum of the work matrix columns
for I in 0..DEGREE_OF_POLYNOMIAL loop
  for J in 0..DEGREE_OF_POLYNOMIAL loop
    SUM(J) := SUM(J) + (WORK_MATRIX(I,J));
  end loop;
end loop;

--Perform the final additions and multiplications to form the result.
for I in 0..(DEGREE_OF_POLYNOMIAL - 1) loop
  for J in 0..I loop
    ECONOMIZED_POLYNOMIAL (J) := ECONOMIZED_POLYNOMIAL (J) +
      float(TCHEBYSHEF_POLYNOMIALS (I,J)) * SUM (I);
  end loop;
end loop;

new_line;
put ("Economized Polynomial");
new_line;
DISPLAY_FLOAT_VECTOR (ECONOMIZED_POLYNOMIAL);

end TCHEBYSHEF_ECONOMIZATION;
package Tchebyshev_package is

-- This package receives the coefficients of a polynomial that is to be
economized, computes its Tchebyshev polynomial, and the powers of
Tchebyshev matrix.

-- Unconstrained type declarations
type MATRIX is array (integer range <> , integer range <> ) of integer;
-- Matrix of integer values, used to contain the Tchebyshev polynomials
type FLOAT_MATRIX is array (integer range <> , integer range <> ) of float;
-- Matrix of floating point values, used to contain the powers of
-- Tchebyshev

type VECTOR is array (integer range <> ) of integer;
-- Vector of integer values, used to contain the multiplier of the matrix

type FLOAT_VECTOR is array (integer range <> ) of float;
-- Vector of floating point values, used to contain the coefficients of
-- the polynomial

-- Variable declarations
MAX_DIGIT: integer := 19;
-- The maximum number of digits permitted in a number is nine.
-- This value represents the maximum input string length for two numbers
-- and a slash, "/".
MAX_DEGREE: integer := 9;
-- The maximum value of the largest exponent of the polynomial
DEGREE_OF_POLYNOMIAL: integer := 0;
-- The actual value of the largest exponent as input by the user

COEFFICIENTS: FLOAT_VECTOR (0..MAX_DEGREE) := (0..MAX_DEGREE => 0.0);
-- Contains a coefficient for each degree of the polynomial that was
-- specified by the user
MULTIPLIER: VECTOR (0..MAX_DEGREE) := (0..MAX_DEGREE => 0);
-- This vector contains the reciprocal of the values contained on
-- the diagonal of the Tchebyshev polynomial matrix.
-- Used in generating the economized polynomial.
TCHEBYSHEF_POLYNOMIALS: MATRIX (O..MAX_DEGREE, 0..MAX_DEGREE) :=
(0..MAX_DEGREE => (0..MAX_DEGREE => 0));
—The matrix obtained when using the Tchebyshef formula.
POWERS_OF_TCHEBYSHEF: FLOAT_MATRIX (0..MAX_DEGREE, 0..MAX_DEGREE) :=
(0..MAX_DEGREE => (0..MAX_DEGREE => 0.0));
—The matrix formed when applying the second step of the economization
— algorithm

function STRING_TO_INT (S: string) return integer;
—This function is used to convert the input coefficient string into an
— integer value that equates to the numerator and the denominator.

—These procedures perform the functions specified by this package
procedure INPUT_COEFFICIENTS;
—Get the input coefficients for the polynomial
procedure COMPUTE_TCHEBYSHEF_POLYNOMIAL;
—Generate the Tchebyshef polynomial matrix
procedure COMPUTE_POWERS_OF_TCHEBYSHEF;
—Generate the powers of Tchebyshef matrix

end TCHEBYSHEF_PACKAGE;
package body TCHEDYSHEF.PACKAGE is

function STRING_TO_INT (S: string) return integer is
    --String to integer equivalent conversion.
    CHAR : character;       --Individual number in each
    DIGIT : integer;        --Individual number in each placeholder
    MULTIPLIER : integer := 1;   --Tens value of the output integer
    FINAL_RESULT : integer := 0; --Output integer being generated.
    POSITION : integer := S'last;  --Pointer into input string
    end if;

    --If the original input was negative, then negate the results.
    elsif CHAR = '-' then
        FINAL_RESULT := -FINAL_RESULT;
    end if;

    --Adjust the pointer into the input string to point to the next
— character to the left.
POSITION := POSITION - 1;
end loop;

— Conversion finished, return the generated integer.
return FINAL_RESULT;
end STRING_TO_INT;
procedure INPUT_COEFFICIENTS is
   -- This procedure obtains the information about the input polynomial and
   -- converts the coefficients into floating point format
package INT_10 is new INTEGER_10(integer);
use INT_10;

POWERS: integer := 3;
   -- Indicates whether all powers, only the even, or only the odd powers
   -- are present in the input polynomial. Originally set to out of
   -- bounds condition to verify proper input.
STEPS: integer := 2;
   -- Increment value for entering the coefficients of the polynomial
INITIAL: integer := 0;
   -- Starting value for the value of the exponent
COUNTER: integer;
   -- Loop counter through the input string
NUMERATOR: integer;
   -- Numerator of the coefficient
DENOMINATOR: integer;
   -- Denominator of the coefficient
CONVERT_STRING: string (1..MAX_DIGIT);
   -- String representation of the coefficient
LAST_DIGIT: integer;
   -- Actual length of the input string
begin
   -- Input Coefficients.
   -- Obtain the value of the largest exponent of the polynomial.
   -- It must be between 2 and 9.
   while DEGREE_OF_POLYNOMIAL < 2 or DEGREE_OF_POLYNOMIAL > MAX_DEGREE loop
      put ("Enter the degree of polynomial desired. (Minimum is 2): ");
      get (DEGREE_OF_POLYNOMIAL);
      new_line;
   end loop;
   -- Obtain an indicator for the type of the polynomial's exponents
   while POWERS < 0 or POWERS > 2 loop
      put ("Enter 0 for coefficients for ALL powers of X");
      new_line;
      put ("Enter 1 for coefficients for ODD powers of X");
      new_line;
      put ("Enter 2 for coefficients for EVEN powers of X");
      new_line;
      get (POWERS);
   end loop;
   -- Set the initial and incremental values for obtaining the polynomial
   -- coefficients. Saves time.
   if POWERS = 0 then
      STEPS := 1;
   elsif POWERS = 1 then
      INITIAL := 1;
   end if;
end;
—Obtain the coefficients for each element of the polynomial
put ("Enter the coefficients of the series being expanded by");
new_line;
put (" entering a fraction, i.e. -2/3 or +2/3 or 2/3");
new_line;
put ("Coefficient for X** ");
new_line;
—Loop through all elements
while INITIAL <= DEGREE_OF_POLYNOMIAL loop
    put (INITIAL);
    put (" = ");
    get_line (CONVERT_STRING,LAST_DIGIT);
    new_line;
    COUNTER := 1;
    —Step through the input string looking for the "/" which separates
    — the numerator from the denominator. If one does not exist, or it
    — appears in either the first or the last position in the string,
    — then the coefficient must be reentered.
    while COUNTER <= LAST_DIGIT loop
        if (CONVERT_STRING (COUNTER) = '/') and
            (COUNTER /= CONVERT_STRING'first and
             COUNTER /= LAST_DIGIT) then
            declare
                — Once the "/" has been located and is in a proper location
                — obtain the numerator string and the denominator string.
                NUMERATOR_STRING: string renames
                    CONVERT_STRING (CONVERT_STRING'first..(COUNTER - 1));
                DENOMINATOR_STRING: string renames
                    CONVERT_STRING ((COUNTER + 1)..LAST_DIGIT);
            begin — Block
                — Convert the two strings into integers
                NUMERATOR := STRING_TO_INT (NUMERATOR_STRING);
                DENOMINATOR := STRING_TO_INT (DENOMINATOR_STRING);
                — If the denominator is a valid value, then generate the floating
                — point value for the coefficient
                if DENOMINATOR /= 0 then
                    COEFFICIENTS (INITIAL) := float(NUMERATOR) / float(DENOMINATOR);
                    — Increment to the next element in the polynomial.
                    INITIAL := INITIAL + STEPS;
                    end if;
                — Indicate that this coefficient has been found and converted
                COUNTER := LAST_DIGIT;
                end; — Block
            end if;
            — Point to the next character in the input string
            COUNTER := COUNTER + 1;
    end loop;
end loop;
end INPUT_COEFFICIENTS;
procedure COMPUTE_TCHEBYSHEF_POLYNOMIAL is
   --Generate the matrix of the Tchebyshef polynomial. The procedure uses
   --values of the matrix elements that have already been found.
   --The algorithm is recursive in that respect.
begin
   --Compute Tchebyshef Polynomial.

   --The first two elements must be initialized to allow the following
   --passes to use them.
   TCHEBYSHEF_POLYNOMIALS (0,0) := 1;
   TCHEBYSHEF_POLYNOMIALS (1,1) := 1;

   --Loop through the lower triangular portion of the matrix
   --and calculate the Tchebyshef polynomial values.
   for I in 2..MAX_DEGREE loop
      for J in 0..I-2 loop
         TCHEBYSHEF_POLYNOMIALS (I,J) :=
         TCHEBYSHEF_POLYNOMIALS (I,J) - TCHEBYSHEF_POLYNOMIALS (I-2,J);
      end loop;

      for J in 0..I-1 loop
         TCHEBYSHEF_POLYNOMIALS (I,J + 1) :=
         TCHEBYSHEF_POLYNOMIALS (I,J + 1) +
         (2 * TCHEBYSHEF_POLYNOMIALS (I-1,J));
      end loop;
   end loop;
   end COMPUTE_TCHEBYSHEF_POLYNOMIAL;
procedure COMPLETE_POWERS_OF_TCHEBYSHEF is
-- Compute the matrix for the powers of Tchebyshef

COEFFICIENT_LIST: FLOAT_VECTOR (0..MAX_DEGREE) :=
(0..MAX_DEGREE => 0.0);
INDEX: integer := DEGREE_OF_POLYNOMIAL;
STEP: integer;
POINTER: integer;

begin --Compute Powers of Tchebyshef.
while INDEX >= 0 loop
  MULTIPLIER (INDEX) := 1 / TCHEBYSHEF_POLYNOMIALS (INDEX, INDEX);
  STEP := INDEX;
  while STEP >= 0 loop
    COEFFICIENT_LIST (STEP) := float(TCHEBYSHEF_POLYNOMIALS (INDEX, STEP));
    STEP := STEP - 1;
  end loop;
  POIERS..OF..TCHEBYSHEF (INDEX,INDEX) := 1.0;
  STEP := INDEX - 2;
  while STEP >= 0 loop
    POIERS..OF..TCHEBYSHEF (INDEX,STEP) :=
      -(COEFFICIENT_LIST (STEP))
      / float(TCHEBYSHEF_POLYNOMIALS (STEP,STEP));
    POINTER := STEP;
    while POINTER >= 0 loop
      COEFFICIENT_LIST (POINTER) :=
      COEFFICIENT_LIST (POINTER) + POIERS..OF..TCHEBYSHEF (INDEX,STEP)
      * float(TCHEBYSHEF_POLYNOMIALS (STEP,POINTER));
      POINTER := POINTER - 2;
    end loop;
    STEP := STEP - 2;
  end loop;
  INDEX := INDEX - 1;
end loop;
end COMPLETE_POWERS_OF_TCHEBYSHEF;

end TCHEBYSHEF_PACKAGE;
Date: 28 November 1985
Version: 1.0
Name: Approx_Driver
Module Number: 1.0
Description: This routine loops until a user is done approximating whichever function he desires
Passed Variables: None
Returns: None
Globals Used: Choice
Modules Called: MENU
Author: Capt. Steven A. Hotchkiss and
Capt. Jennifer Fried
History: Developed as a thesis and ADA project

with GLOBAL_DATABASE; use GLOBAL_DATABASE;
with APPROXIMATORS; use APPROXIMATORS;
with TEXT_10; use TEXT_10;
procedure APPROX_DRIVER is

NUM: Integer := 0;
DEN: Integer := 1;
CHOICE, KEY : character;
QUIT : character := '7';

package INT_10 is new INTEGER_10(INTEGER);
use INT_10;

package FLT_10 is new FLOAT_10(LONG_FLOAT);
use FLT_10;

begin
set_page_length(24);

-- initialize data points
COMPUTE_TCHEBYSHEV;

-- let the user approximate as many functions as needed
while (CHOICE /= QUIT) loop

   -- select function to approximate
   -- by giving users a menu of options
   MENU(CHOICE);

   -- use the built functions to make a more accurate approximation
   COMPUTE_PADE_APPROXIMATIONS;
   COMPUTE_CNK;

   if CHOICE /= QUIT then
      for i in 0..M loop
         if C(NUM,i) /= 0.0 or C(DEN,i) /= 0.0 then
            put("a==");
            put(i);
            put(" \rightarrow ");
            put(C(NUM,i));
            put(" b==");
            put(i);
            put(" \rightarrow ");
            put(C(DEN,i));
            new_line;
         end if;
      end loop;
   end if;

   put("Hit any key to continue");
   get(KEY);
   new_line;

end loop;

end APPROX_DRIVER;
package GLOBAL_DATABASE is

type LONG_FLOAT is digits 9;
type VECTOR is array(integer range 0..25) of LONG_FLOAT;
type MATRIX is array(integer range 0..25, integer range 0..25) of LONG_FLOAT;
type PADE_MATRIX is array (integer range 0..25, integer range 0..1, integer range 0..25) of LONG_FLOAT;

T: MATRIX; -- Matrix containing the coefficients of different powers of Tchebyshev polynomials
R: PADE_MATRIX; -- Used to contain the series of Pade approx
               -- R(S,M or D,C)
               -- S is the series number
               -- M or D, N - 0 for the numerator
               -- D - 1 for the denominator
               -- C - coefficient for a power of X for the particular series' numerator or denominator

D: VECTOR; -- Error values of Pade approximations
M: Integer; -- Power of the numerator polynomial
K: Integer; -- Power of the denominator polynomial
N: integer; -- Power of the initial power series
MACLAURIN: VECTOR; -- Contains the coefficients for the different powers of "X" for the power series

COEFFICIENT: string(1..33); -- Used to contain user entered coefficients
EPS: LONG_FLOAT; -- Convergent epsilon
C: MATRIX; -- Final rational approximation

end GLOBAL_DATABASE;
DATE: 28 November 1985
Version: 1.0
Name: COMMON_PROCS
Module Number: 3.0
Description: This package contains procedures that are invoked throughout the system.
Passed Variables: N/A
Returns: N/A
Globals Used: N/A
Modules Called: N/A
Author: Capt. Steven A. Hotchkiss and Capt. Jennifer Fried
History: Developed as a thesis and ADA project

with GLOBAL_DATABASE; use GLOBAL_DATABASE;
package COMMON_PROCS is
    procedure POWER_PROMPT(NUM, DEN: out integer; Epsilon: out LONG_FLOAT);
    procedure GET_COEFFICIENTS(STRUCTURE: in character; POWER: in integer);
    function PRODUCT(FROM, TO, BY: integer) return LONG_FLOAT;
    function FACTORIAL(NUMBER: integer) return LONG_FLOAT;
end COMMON_PROCS;
with TEXT_10; use TEXT_10;
package body COMMON_PROCS is

    package INT_10 is new INTEGER_10(integer);
    use INT_10;

    package FLT_10 is new FLOAT_10(LONG_FLOAT);
    use FLT_10;

    procedure POWER_PROMPT(NUM, DEN: out integer; Epsilon: out LONG_FLOAT) is
    begin
        set_page_length(24);
        loop
            new_page;
            put("Enter the power of the numerator (must be integer) ");
            get(NUM);
            new_line;

            put("Enter the power of the denominator (must be integer) ");
            get(DEN);
            new_line;

            put("Enter the epsilon of convergence.");
            put("This must be a real fraction and entered as 0.x");
            put("Where x is any string of digits up to 9 in length ");
            get(EPSILON);
            new_line;

            exit;
        end loop;
        exception
            when data_error =>
                put_line("Invalid Entry. Reenter data");
    end POWER_PROMPT;
procedure GET_COEFFICIENTS(STRUCTURE: in character; POWER: in integer) is

    COEFF : LONG_FLOAT := 0.0;
    FROM, TO, BY : integer;

procedure GET_POWER(NUMBER: in integer; COEFF: out LONG_FLOAT) is

    LAST_SUBP : integer := COEFFICIENT'last-1;
    NUMERATOR : boolean := TRUE;
    OUT_COEFF : LONG_FLOAT;
    CHAR_PTR : integer;
    NUM, DEN : string(1..15);
    INPUT_ERROR : exception;

procedure COMPUTE_REAL_COEFF(NUM, DEN: in string;
    COEFF: out LONG_FLOAT) is

    CHAR_PTR: integer := NUM'first;
    NUMERATOR: LONG_FLOAT := 0.0;
    DENOMINATOR, SIGN: LONG_FLOAT := 1.0;

begin -- COMPUTE_REAL_COEFF

    if (NUM(CHAR_PTR) = '+') then
        CHAR_PTR := CHAR_PTR + 1;
    elsif (NUM(CHAR_PTR) = '-') then
        SIGN := -SIGN;
        CHAR_PTR := CHAR_PTR + 1;
    end if;

    while ((NUM(CHAR_PTR) /= '') and (CHAR_PTR <= NUM'last)) loop
        NUMERATOR := NUMERATOR * 10.0 +
            LONG_FLOAT(character'pos(NUM(CHARPTR)) -
                character'pos('0'));
    end loop;

    CHAR_PTR := DEN'first;
    if (DEN(CHAR_PTR) = '+') then
        CHAR_PTR := CHAR_PTR + 1;
    elsif (DEN(CHAR_PTR) = '-') then
        SIGN := -SIGN;
        CHAR_PTR := CHAR_PTR + 1;
    end if;

    while ((DEN(CHAR_PTR) /= '') and (CHAR_PTR <= DEN'last)) loop
        NUMERATOR := NUMERATOR * 10.0 +
            LONG_FLOAT(character'pos(DEN(CHAR_PTR)) -
                character'pos('0'));
    end loop;

    COEFF := NUMERATOR/DENOMINATOR*SIGN;

end COMPUTE_REAL_COEFF;
begin — GET_POWER

new_page;

loop
  — prompt the user
  put("Enter the coefficients for x**");
  put(NUMBER);
  put(" == ");
  get(COEFFICIENT);

  — pack and separate
  for I in COEFFICIENT'range loop
    if ('0' <= COEFFICIENT(I) and COEFFICIENT(I) <= '9') or
       COEFFICIENT(I) = '-' or COEFFICIENT(I) = '+' or
       COEFFICIENT(I) = '/' or COEFFICIENT(I) = '' then
      if COEFFICIENT(I) = '/' then
        CHAR_PTR := DEN'MTE first;
        NUMERATOR := FALSE;
      elsif COEFFICIENT(I) = '+' or COEFFICIENT(I) = '-' or
            ('0' <= COEFFICIENT(I) and COEFFICIENT(I) <= '9')
      then
        if NUMERATOR then
          NUM(CHAR_PTR) := COEFFICIENT(I);
        else
          DEN(CHAR_PTR) := COEFFICIENT(I);
        end if;
        CHAR_PTR := CHAR_PTR + 1;
      end if;
    else
      raise INPUT_ERROR;
    end if;
  end loop;

  exit;
end loop;

COMPUTE_REAL_COEFF(NUM, DEN, OUT_COEFF);
COEFF := OUT_COEFF;
put(OUT_COEFF);
new_line;

exception
  when INPUT_ERROR => put_line("Input Error. Reenter value.");
  new_line;
end GET_POWER;
begin  -- GET_COEFFICIENTS

set_page_length(24);
new_page;

put_line("Enter the coefficients for each");
put_line("power of the 'X' in fractional form.");
put_line("If a sign is entered, it must be the ");
put_line("first character. No blanks are allowed.");
put_line("The max allowable size is 9 digits per");
put_line("number.");
new_line;
put_line("Sample entries:  1/2 , +1/2 , or -1/2");
new_line;

TO := POWER;
case STRUCTURE is
  when '1' => FROM := 0;
    BV := 1;
  when '2' => FROM := 0;
    BV := 2;
  when '3' => FROM := 1;
    BV := 2;
  when others => FROM := 0;
    BV := 1;
end case;

while (FROM < TO) loop
  GET_POWER<FROM, COEFF>;
  MACLAURIN<FROM> := COEFF;
  FROM := FROM + BV;
end loop;

end GET_COEFFICIENTS;
function PRODUCT(FROM, TO, BY: integer) return LONG_FLOAT is

    RESULT: LONG_FLOAT := 1.0;
    LOOP_TEST: integer := FROM;

    begin

        while (LOOP_TEST <= TO) loop
            RESULT := RESULT * LONG_FLOAT(LOOP_TEST);
            LOOP_TEST := LOOP_TEST + BY;
        end loop;

        return RESULT;

    end PRODUCT;
function FACTORIAL (NUMBER: integer) return LONG_FLOAT is

    RESULT: LONG_FLOAT := 1.0;

    begin

        for I in 2..NUMBER loop
            RESULT := RESULT * LONG_FLOAT(I);
        end loop;

        return RESULT;

    end FACTORIAL;

end COMMON_PROCS;
The package contains modules that are called to either compute a predefined power series expansion of a function or allow a user to enter their own.

Passed Variables: N/A

Returned: N/A

Global Variables: GLOBAL_DATABASE

Modules Called: None

Author: Capt. Steven A. Hotchkiss and Capt. Jennifer Fried

History: Developed as a thesis and ADA project

package FUNCTION_PACKAGE is

    procedure SIN_SERIES;
    procedure TAN_SERIES;
    procedure ASIN_SERIES;
    procedure ATAN_SERIES;
    procedure EXP_SERIES;
    procedure BUILD_SERIES;

end FUNCTION_PACKAGE;
with GLOBAL_DATABASE; use GLOBAL_DATABASE;
with COMMON_PROCS; use COMMON_PROCS;
with TEXT_10; use TEXT_10;
package body FUNCTION_PACKAGE is

procedure SIN_SERIES is
  N: integer;
  begin
    -- get the powers of the numerator and denominator polynomials.
    -- Also prompt the user for a convergent epsilon.
    POWER_PROMPT(N,K,EPS);
    POWER_PROMPT(N,K,EPS);
    -- Compute the power of the Maclaurin series. It is the sum of the power
    -- of the numerator, denominator, and the value two
    N := N + K + 2;
    -- Compute the initial approximating polynomial
    for I in 0..25 loop
      MACLAURIN(I) := 0.0;
    end loop;
    for I in 1..((N+1)/2) loop
      MACLAURIN(I*2-1) := -1.0**(I-1)/FACTOIAL(2*I-1);
    end loop;
  end SIN_SERIES;
procedure TAN.Series is

N: integer;
begin
— get the powers of the numerator and denominator polynomials.
— Also prompt the user for a convergent epsilon.
POWERT>PROMPT(N,K, EPS);
— Compute the power of the Maclaurin series. It is the sum of the power
— of the numerator, denominator, and the value two
N := M + K + 2;
— Compute the initial approximating polynomial
for I in 0..25 loop
MACLAIRIN(1) := 0.0;
end loop;
MACLAIIRIN(1) := 1.0;
for I in 1..((N+1)/2) loop
MACLAIRIN(2*I+1) := PRODUCT(2, 2*I, 2) /
FACTORIAL(2*I+1);
end loop;
end TAN.Series;
procedure ASIN_SERIES is

N: integer;

begin
— get the powers of the numerator and denominator polynomials.
— Also prompt the user for a convergent epsilon.
POWER_PROMPT(N, K, EPS);

— Compute the power of the Maclaurin series. It is the sum of the power
— of the numerator, denominator, and the value two
N := M + K + 2;

— Compute the initial approximating polynomial
for I in 0..25 loop
   MACLAURIN(1) := 0.0;
   end loop;

for I in 1..((N+1)/2) loop
   MACLAURIN(I*2-1) := PRODUCT(1, ((I-2)*2+1), 2) / 
                      PRODUCT(2, (I*2-2), 2) * 
                      LONG_FLOAT(I*2-1);
end loop;

end ASIN_SERIES;
procedure ATAN_SERIES is

N: integer;
begin
-- get the powers of the numerator and denominator polynomials.
-- Also prompt the user for a convergent epsilon.
POWER_PROMPT(M,K,EPS);

-- Compute the power of the Maclaurin series. It is the sum of the power
-- of the numerator, denominator, and the value two
N := M + K + 2;

-- Compute the initial approximating polynomial
for I in 0..25 loop
   MACLAURIN(I) := 0.0;
end loop;

for I in 1..((N+1)/2) loop
   MACLAURIN(2*I-1) := -1.0**(I-1)/FACTORIAL(2*I-1);
end loop;
end ATAN_SERIES;
procedure BUILD_SERIES is

    N : integer;
    STRUCTURE : character;

begin

    set_page_length(24);

    — get the powers of the numerator and denominator polynomials.
    — Also prompt the user for a convergent epsilon.
    POWER_PROMPT(N,K,EPS);

    — Compute the power of the Maclaurin series. It is the sum of the power
    — of the numerator, denominator, and the value two
    N := M + K + 2;

    — Compute the initial approximating polynomial
    for I in 0..25 loop
        MACLAURIN(I) := 0.0;
    end loop;

    — Prompt the user for the structure of the polynomial
    new_page;

    L1: loop
        put_line("Enter 1 if all powers of X");
        put_line("Enter 2 if only even powers of X");
        put("Enter 3 if only odd powers of X ");
        get(STRUCTURE);
        new_line;

        if '1' > STRUCTURE or STRUCTURE > '3' then
            put_line("Bad Entry. Try again.");
        else
            GET_COEFFICIENTS(STRUCTURE,N);
        end if;

        exit L1;
    end loop L1;

end BUILD_SERIES;
procedure EXP_SERIES is

N: integer;

begin
  -- get the powers of the numerator and denominator polynomials.
  -- Also prompt the user for a convergent epsilon.
  POWER_PROMPT(N,K,EPS);

  -- Compute the power of the Maclaurin series. It is the sum of the power
  -- of the numerator, denominator, and the value two
  N := M + K + 2;

  -- Compute the initial approximating polynomial
  for I in 0..25 loop
    MACLAURIN(I) := 0.0;
  end loop;

  for I in 0..N loop
    MACLAURIN(I) := 1.0 / FACTORIAL(I);
  end loop;

  end EXP_SERIES;
end FUNCTION_PACKAGE;
package APPROXIMATORS is

   procedure COMPUTE_TCHEBYSHEV;

   procedure MENU(CHOICE: out character);

   procedure COMPUTE_PADE_APPROXIMATIONS;

   procedure COMPUTE_CMK;

end APPROXIMATORS;
with GLOBAL_DATABASE; use GLOBAL_DATABASE;
with FUNCTION_PACKAGE; use FUNCTION_PACKAGE;
with TEXT_IO; use TEXT_IO;
package body APPROXIMATORS is

    package FLT_IO is new FLOAT_IO(LONG_FLOAT); use FLT_IO;

    procedure COMPUTE_TCHEBYSHEV is
        begin
            -- build the global table "T" containing the coefficients for
            -- each of a series of Tchebyshev polynomials
            T(0,0) := 1.0;
            T(1,1) := 1.0;
            T(2,0) := -1.0;
            T(2,2) := 2.0;

            for I in 3..25 loop
                for J in 0..24 loop
                    T(I,J) := T(I,J) - T(I-2,J);
                end loop;

                for J in 0..24 loop
                    T(I,J+1) := T(I,J+1) + 2.0 * T(I-1,J);
                end loop;
            end loop;

            end COMPUTE_TCHEBYSHEV;
procedure MENU(CHOICE: out character) is

    OUT_CHOICE: character;
    BAD_CHOICE: exception;

begin

    set_page_length(24);

    -- clear screen and print menu
    new_page;
    put_line("Choose function to be approximated");
    new_line;

    put_line("Enter 1 for sin");
    put_line("Enter 2 for tan");
    put_line("Enter 3 for arcsin");
    put_line("Enter 4 for arctan");
    put_line("Enter 5 for exp");
    put_line("Enter 6 for user defined function");
    put_line("Enter 7 to quit");

    loop

        for I in MACLAURIN'range loop
            MACLAURIN(I) := 0.0;
        end loop;

        new_line;
        put("=> ");
        get(OUT_CHOICE);
        CHOICE := OUT_CHOICE;

        case OUT_CHOICE is

            when '1' => SIN_SERIES;
            when '2' => TAN_SERIES;
            when '3' => ASIN_SERIES;
            when '4' => ATAN_SERIES;
            when '5' => EXP_SERIES;
            when '6' => BUILD_SERIES;
            when '7' => null;
            when others => raise BAD_CHOICE;

        end case;

        exit;

    end loop;

exception

    when BAD_CHOICE =>
        put_line("Invalid entry. Try again");

end MENU;
procedure COMPUTE_PADE_APPROXIMATIONS is

N_MAX: integer;
NUM: integer := 0;
DEN: integer := 1;
TEMP: LONG_FLOAT;
WORK: matrix;
B: vector;

begin

— this procedure converts the initial approximating polynomial
— (the Maclaurin power series) into a rational approximation
— clear out this PADE approximation's numerator
— and denominator polynomials
for SERIES in 0..25 loop
  for NUM_DEN in 0..1 loop
    for COEFFICIENT in 0..25 loop
      R(SERIES,NUM_DEN,COEFFICIENT) := 0.0;
    end loop;
  end loop;
end loop;

for I in 0..N loop — loop for all powers of the numerator
  for J in 0..K loop — loop for all powers of the denominator
    if (I >= J) then
      — build a work matrix to solve simultaneous equations
      B(0) := 1.0;
      N_MAX := I + J;
      for S in 0..(N_MAX - I - 1) loop
        for N1 in 0..J loop
          WORK(S+1,N1) := MACLAURIN(abs(N_MAX - S - N1));
          if (N1 = 0) then
            B(S+1) := -MACLAURIN(abs(N_MAX - S - N1));
          end if;
        end loop;
      end loop;
      — Solve simultaneous equations for denominator coefficients
      for N1 in 1..J loop
        if (WORK(N1,N1) = 0.0) then
          SETUP:
          for N2 in 1..J loop
            if (WORK(N2,N1) /= 0.0) then
              TEMP := B(N2);
              B(N2) := B(N1);
              B(N1) := TEMP;
              for N3 in 1..J loop
                TEMP := WORK(N2,N3);
                WORK(N2,N3) := WORK(N1,N3);
                WORK(N1,N3) := TEMP;
              end loop;
              exit SETUP;
            end if;
          end loop;
        end if;
      end loop;
    end if;
  end loop;
end loop;
end COMPUTE_PADE_APPROXIMATIONS;
end if;

TEMP := WORK(N1, M1);
if TEMP /= 0.0 then
  B(N1) := B(N1)/TEMP;
else
  B(N1) := 0.0;
end if;

for M2 in 1..J loop
  if TEMP /= 0.0 then
    WORK(N1,M2) := WORK(N1,M2)/TEMP;
  else
    WORK(N1,M2) := 0.0;
  end if;
end loop;

for M2 in 1..J loop
  if (N1 /= N2) then
    TEMP := -WORK(N2,N1);
    for N3 in 1..J loop
      WORK(N2,N3) := WORK(N2,N3) + WORK(N1,N3) * TEMP;
    end loop;
    B(N2) := B(N2) + B(N1) * TEMP;
  end if;
end loop;

end loop;

-- use denominator coefficients to compute the numerator
-- coefficients, and build the series of Pade approximations
for N1 in 0..I loop
  for M2 in 0..N1 loop
    R(I+J,NUM,N1) := R(I+J,NUM,N1) + B(N2) * MACLAURIN(N1-N2)/B(0);
  end loop;
end loop;

for N1 in reverse 0..I loop
  R(I+J,DEN,N1) := B(N1)/B(0);
  B(N1) := B(N1) / B(0);
end loop;

-- Compute the D's that are used to compute C(m,k)
-- D(I+J+1) = SUM L=0 to J (MacLaurnin(I+J+1-L)*B(L))
D(I+J+1) := 0.0;
for L in 0..J loop
  D(I+J+1) := D(I+J+1) + MACLAURIN(I+J+1-L) * B(L);
end loop;

end if;
end loop;
end loop;

end COMPUTE_PADE_APPROXIMATIONS;
procedure COMPUTE_CMK is
  A: integer := 0;
  B: integer := 1;
  LAMDA: VECTOR;

begin

  -- Compute the Ladas (alpha=1)
  LAMDA(0) := -(D(M+1) * T(1,0)) / (2.0 ** (M+1));

  for J in 0 .. (M+1) loop
    if D(J+1) /= 0.0 then
      LAMDA(J+1) := (D(M+1) * T(M+1, J+1)) / ((2.0 ** (M+1)) * D(J+1));
    else
      LAMDA(J+1) := 0.0;
    end if;
  end loop;

  -- Load Pm(X) and Qm(X) with their A and B coefficients respectively
  for I in 0 .. M loop
    C(A, I) := R(M+1, A, I);
  end loop;

  for I in 0 .. K loop
    C(B, I) := R(M+1, B, I);
  end loop;

  -- Compute coefficients "A" of numerator and "B" of denominator
  for J in 0 .. (M+1) loop
    for K in 0 .. 25 loop
      R(J, A, K) := R(J, A, K) * LAMDA(J+1);
      C(A, K) := C(A, K) + R(J, A, K);
      R(J, B, K) := R(J, B, K) * LAMDA(J+1);
      C(B, K) := C(B, K) + R(J, B, K);
    end loop;
  end loop;
  C(A, 0) := C(A, 0) + LAMDA(0);

  for I in reverse 0 .. 25 loop
    C(A, I) := C(A, I) / C(B, 0);
    C(B, I) := C(B, I) / C(B, 0);
  end loop;

end COMPUTE_CMK;

end APPROXIMATORS;
Bibliography

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VITA

Captain Jennifer J. Fried was born on 19 October 1951 at Ft. Sill, Oklahoma. In May of 1969, she graduated from High School in Newport News, Virginia. She later enlisted in the United States Air Force as a Computer Programmer, and was assigned to Holloman AFB, New Mexico. In 1979, she was accepted into the Airman Education and Commissioning Program and attended New Mexico State University. Upon receiving a Bachelor of Science in Computer Science and Mathematics in January 1981, she was sent to Officer Training School. Upon graduation, she was stationed at Peterson AFB, Colorado where she became Chief of the Missile Warning/Space Computer Test Section. While working toward a degree of Master of Science in Computer Data Management, she was selected to enter the School of Engineering, Air Force Institute of Technology, in June of 1984.

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Thesis Chairman: Panna B. Nagarsenker
Associate Professor of Mathematics and Computer Science

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Report Document Page

1. REPORT SECURITY CLASSIFICATION
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2a. SECURITY CLASSIFICATION AUTHORITY
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5a. NAME OF PERFORMING ORGANIZATION
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16. SUPPLEMENTARY NOTATION

19. ABSTRACT
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DISTRIBUTION/AVAILABILITY OF ABSTRACT
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This project produced a run-time math library for the MIL-STD-1750A embedded computer architectures. The math library consists of the algebraic functions. In addition, the steps required for a performance analysis of the math library have been outlined.

Several approximation methods were investigated. The Chebyshev Economization of Maclaurin series polynomials, and rational approximations derived from the second algorithm of Remes were determined to be the best methods available. Each function's implementation was designed to take advantage of features of MIL-STD-1750A architectures. The recommended test procedures provide measures of the average and worst case generated errors within each approximation.
END

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