ELECTRICAL ANALYSIS
OF B-52/FB-111 AMAC
AND
RELEASE CIRCUITRY
UTILIZING SNEAK CIRCUIT
ANALYSIS TECHNIQUE

Final Report

THE CASING AEROSPACE COMPANY
HOUSTON, TEXAS
OCTOBER 31, 1975
THE BOEING COMPANY

TITLE: ELECTRICAL ANALYSIS OF B-52/FB-111 AMAC AND RELEASE CIRCUITRY UTILIZING SNEAK CIRCUIT ANALYSIS TECHNIQUES, FB-111 NETWORK TREES.

MODEL B-52/FB-111

CONTRACT: F29601-76-C0017

PREPARED BY: ORG. 5-2932

SUPERVISED BY: C. J. HILL

APPROVED BY: D. A. Heuer

SNEAK CIRCUIT ANALYSIS GROUP
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<td>Final Report</td>
<td>Electrical Analysis of B-52/FB-111 AMAC and Release Circuitry Utilizing Sneak Circuit Analysis Techniques</td>
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<td>B-52 Network Trees</td>
<td>B-52 AMAC and Release Circuitry</td>
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<td>FB-111 AMAC and Release Circuitry</td>
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<td>DESCRIPTION</td>
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SNEAK CIRCUITS NETWORK TREE

Ref Des. AMAC-L

- Switched to 28 VDC when option sel sw is in Air Ret. and Air free fall positions, and code sw is enabled.
- Switched to 28 VDC when option sel sw is in GND Ret. Air Ret. GND free fall + Air free fall positions, and code sw is enabled.
- Switched to 28 VDC when option sel sw is in GND free fall and Air free fall positions and code sw is enabled.

PROJECT
FB111-A

TITLE
AMAC-L

QC
DATE
ANALYST
DATE
NODAL SET

Mou-703
9-25-75
9-17-75
2

Boeing Aerospace Company - Houston, Texas C-7
SNEAK CIRCUITS NETWORK TREE

Figure 1: Sneak Circuits Network Tree Diagram

- Switched to 28VDC when option sel sw is in Air Ret. and Air free fall positions, and code sw is enabled.
- Switched to 28 VDC when option sel sw is in GND Ret., Air Ret., GND free fall + Air free fall positions, and code sw is enabled.
- Switched to 28 VDC when option sel sw is in GND free fall and Air free fall positions and code sw is enabled.

Ref. Des.: AMAC-R

PROJECT
FB111-A

TITLE
AMAC-R

QC Date
5.3.75

Analyst Date
9.18.75

Nodal Set
3

Notes: NA
Switched to 28 VDC when option select SW is in Air Ret. and Air free fall positions, and code SW is enabled.

Switched to 28 VDC when option select SW is in GND Ret., Air Ret., GND free fall & Air free fall positions, and code SW is enabled.

Switched to 28 VDC when option select SW is in GND free fall and Air free fall positions and code SW is enabled.

Ref. Des. AMAC-4

PROJECT

RB111-A

TITLE

AMAC-4

QC DATE ANALYST DATE NODAL SET

Switched to 28VDC when option sel sw is in Air Ret. and Air free fall positions, and code sw is enabled.

Switched to 28VDC when option sel sw is in GND Ret. Air Ret. GND free fall + Air free fall positions, and code sw is enabled.

Switched to 28VDC when option sel sw is in GND free fall and Air free fall positions and code sw is enabled.

Ref. Des. AMAC-5
SNEAK CIRCUITS NETWORK TREE

PWR CODE SET

FAKE BID - NO INFO ON CONTROLLER CIRCUITS

J9/B CODE SET

PROJECT
FB-111A

TITLE
FAKE BID

QC DATE

ANALYST DATE 9-18-75

NODAL SET 10

SNEAK CIRCUITS NETWORK TREE

PWR CODE SET

FAKE BID - NO INFO ON CONTROLLER CIRCUITS

J9/D CODE SET

PROJECT
FB-111A

TITLE
FAKE BID

QC DATE

ANALYST DATE 9-18-75

NODAL SET 11
SNEAK CIRCUITS NETWORK TREE

FAKE BID - NO INFO ON CONTROLLER CIRCUITS

J10/2 CODE SW

PROJECT
FB-111A

TITLE
FAKE BID

QC DATE ANALYST DATE NODAL SET

Rev.

12

13
SNEAK CIRCUITS NETWORK TREE

PWR
CODE SET

FAKE BID-
NO INFO ON
CONTROLLER
CIRCUITS

J10/C
CODE SW

SNEAK CIRCUITS NETWORK TREE

PWR
CODE SET

FAKE BID-
NO INFO ON
CONTROLLER
CIRCUITS

J9/H
CODE SET
PROJECT FB-111A  TITLE FAKE BID

FAKE BID - NO INFO ON CONTROLLER CIRCUITS

J10/E CODE SW

PROJECT FB-111A  TITLE FAKE BID

FAKE BID - NO INFO ON CONTROLLER CIRCUITS

J10/E CODE SW
Ref Des is 403A1 unless shown otherwise.
STA 4 OUTPUT
STA SL LTCC

REF DES IS 403A1 UNLESS
SHOWN OTHERWISE
SNEAK CIRCUITS NETWORK TREE

PWR/CPU

STA 5 OUTPUT
STA SL LTCC

NSC435
403A1
JDD

PWR/TEST

303A1
N-5

PWR/SCIU

(Blocking, Diode)
(INVERTER TO CPU)

STA 5 OUTPUT
CTR PR LTCC

LTSTA5/LAMP
LTSTA5/LGND

STA LAMP
NL-5

LEGEND
LAMPS

LTST315/LAMP
LTST315/LGND

ICD
GND/DIM

SW GND
TO BYPASS RES.

NSC434

LR2-5

LR1-5

REF DES IS 403A1 UNLESS SHOWN OTHERWISE.

OBJECT

FB 111-A

TITLE

STATION SELECT - STATION 5

QC

DATE

9-25-75

ANALYST

DATE

1-12-75

NODAL SET

QC 3 C

BOEING AEROSPACE COMPANY - HOUSTON, TEXAS C-27
SNLAK CIRCUITS NETWORK TREE

REF DES IS 403A1 UNLESS SHOWN OTHERWISE

PROJECT
FP-111-A

TITLE
STATION SELECT - STATION 8

DATE 9-25-75
ANALYST
DATE 9-12-75
MODAL SET 0033
REF DES IS 403A1 UNLESS SHOWN OTHERWISE
See Nodal set 36
Output is a series of pulses (50-40)
whose frequency is determined by
the dial setting of the intervalometer.
SNEAK CIRCUITS NETWORK TREE

PWR ESDC 28 VDC FS 314A1

CD2

EXT. STOR (314A1) Jettison B

EMERGENCY

J10 Jettison Switch

RHS 53

Safety
Relay B

SK2

GRD JTN
SW. RHS 53

K13

SK13

Release

F81 Enable Relay A

J1003/33

BRT Stares Jtn.-Input

CPU

GND

× Relay Contacts open
when landing gear is out

△ Switch Nomenclature
DOES NOT AGREE WITH PANEL
Nomenclature. DER # FB-111A-3

PROJECT
FB III-A

TITLE
EXT. Stores Jettison B

DATE
9-25-75

ANALYST
J. Verris

DATE
9-16-75

NODAL SET
50

BOEING AEROSPACE COMPANY - HOUSTON, TEXAS C-38
See Nodal Set 60
D2-118576-3

SNEAK CIRCUITS NETWORK TREE

PWR ESDC
28 VDC 314A1

C81 Ext. Stores
(314A1) Jettison A

EXT. Stores Jettison Switch
RHS 50

S10

S11

312

CH30-9/0

Safety Relay F
357 (607)

GRP JTSN
Sw.

RHS 50

RI19

K14

(607)

RHS 55.933

K14

6042

RELEASE ENABLE RELAY B

SK14

6042

REI

J1000/36

EXT. Stores Jettison J history

CPU

V GND

* Relay Contacts open
when landing gear is out.
See Nodal Set 26
See Nodal Set 47.
See Nodal Set 34
See NS 26
**SNEAK CIRCUITS NETWORK TREE**

**SEE NODAL SET 61**

<table>
<thead>
<tr>
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**SNEAK CIRCUITS NETWORK TREE**

**SEE NODAL SET 61**

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</table>
SNEAK CIRCUITS NETWORK TREE

CENTRAL PROGRAM UNIT
"OUTPUT STA. SEL. RELAY"

DUE/CPU

(SPU-L)
K2
RS 301

DUE/CPU

(SPU-R)
K2
RS 300

DUE/CPU

(SPU-3)
K2
RS 286 295 311

DUE/CPU

(SPU-4)
K2
RS 277 287 317

DUE/CPU

(SPU-5)
K2
RS 279 288 428

(SPL-6)
K2
RS 380 381

J9001/40 (SPU-R)
J10/5 (300)
J10/15 (309)

J3/D
(W5)
RELEASE INHIBIT
GROUND TEST CONN.

J6022/32 (Fixed Power 1)

J6022/32 (Fixed Power 2)

NO DATA

RS 50-K13
(607)

RELEASE
ENABLE
RELAY A

ENABLE

*Includes NS 432

<table>
<thead>
<tr>
<th>PROJECT</th>
<th>TITLE</th>
<th>CPU - OUTPUT STATION SELECT COMMANDS</th>
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QC          DATE       ANALYST     DATE       NODAL SET

P. Stokes  9-25-75       9-17-75       67
SNEAK CIRCUITS NETWORK TREE

PWR/cpu

CPU

BIOS/7

SPU-L

k4/N001

NS C301

k4

GND
D2-178576-3

SNEAK CIRCUITS NETWORK TREE

CPU
J 1005/8
SPU-L
K16/NO01
K.16
NS 0325/

OUTPUT SIM L LATCH RESET

CPU

Pu12/cpu

PROJECT
TMAN

DATE
9-25-75

ANALYST
P. Valke

DATE
9-22-75

NODAL SET
0071

BOEING AEROSPACE COMPANY - HOUSTON, TEXAS C-55
SNEAK CIRCUITS NETWORK TREE

Pwr/cpu

CPU

L WPN IDENT
LINE 3

24/25

OPEN

SRAM DISCO.

OBJECT TITLE

FBIII-A L WPN IDENT LINE 3

AC-1 DATE 9-25-75 ANALYST DATE 6-22-75 NODAL SET 06.71/

BOEING AEROSPACE COMPANY - HOUSTON, TEXAS C-58
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<thead>
<tr>
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<td>&quot;Out Station L Option 3&quot; Command</td>
<td>9-25-75</td>
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<td>9-17-75</td>
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<td>&quot;Out Station L Option 4&quot; Command</td>
<td>9-25-75</td>
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<td>9-17-75</td>
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</table>
SNEAK CIRCUITS NETWORK TREE

PROJECT TITLE
FB-111A
OUTPUT OPTIONS 5, SPU-L

QC DATE ANALYST DATE NODAL SET
D. Stokes 9-25-75 M. Bornach 9/22/75 79

SNEAK CIRCUITS NETWORK TREE

PROJECT TITLE
FB-111A
OUTPUT OPTION 7, SPU-L

QC DATE ANALYST DATE NODAL SET
D. Stokes 9-25-75 M. Bornach 9/22/75 80

SPU-L
K11, SET, OUTPUT OPTION 7
(THIS RLY NOT INSTALLED)
NO CROSS REFERENCES

SPU-L

SPU-L

SPU-L
**SNEAK CIRCUITS NETWORK TREE**

**PWR/CPU** 3/05/79

**SPU-L**

**K12, SET, OUTPUT OPTION 8**

**x reference NS 325**

---

**FB-111A**  
**OUTPUT OPTION 8, SPU-L**

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<td>Stok</td>
<td>9-25-75</td>
<td>Mr. Wrench</td>
<td>9/22/75</td>
<td>81</td>
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**SNEAK CIRCUITS NETWORK TREE**

**PWR/CPU** 3/05/20

**SPU-L**

**K13, OUTPUT OPTION 9**

**x reference NS 325**

---

**FB-111A**  
**OUTPUT OPTION 9, SPU-L**

<table>
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<td>Stok</td>
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<td>9/22/75</td>
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PROJECT
FB-111A

TITLE
OUTPUT OPTION 10, SPU-L

DATE
9-25-75

ANALYST
M. Barrett

DATE
9-22-75

NODAL SET
83

PROJECT
FB-111A

TITLE
OUTPUT STA-P SEL

DATE
9-25-75

ANALYST
M. Barrett

DATE
9-22-75

NODAL SET
84
SNEAK CIRCUITS NETWORK TREE

DATE 9-25-75  ANALYST DATE 9-22-75  NODAL SET 85

PROJECT FB-111A  TITLE STORE JETTISON, SPU-R

SNEAK CIRCUITS NETWORK TREE

DATE 9-25-75  ANALYST DATE 9-22-75  NODAL SET 86

PROJECT FB-111A  TITLE 2 LATCH RESET, SPU-R
**PROJECT**

**FB-111A**

**TITLE**

**SNEAK CIRCUITS NETWORK TREE**

**DATE**

9-25-75

**ANALYST**

9/22/75

**NODAL SET**

87

---

**PROJECT**

**FB-111A**

**TITLE**

**SNEAK CIRCUITS NETWORK TREE**

**DATE**

9-25-75

**ANALYST**

9/22/75

**NODAL SET**

88
PROJECT: FB111A
TITLE: "Store Jettison" — SPUR-R

ANALYST: BAY
DATE: 9-17-75
NODAL SET: 92

OBJECT: FB111A
TITLE: "Output Station R Option 1" — SPUR-R

ANALYST: BAY
DATE: 9-17-75
NODAL SET: 93
K9A & K9B ARE COILS FOR MAG-LATCH RELAYS. THE CONTACTS SWITCH 36-115VAC TO SRAM DISCONNECT: OUT OF SCOPE.
"Output Station R Option 7" PWR/CPU (CPU)

NO CONNECTION
Ki1 is not INSTALLED.

"Output Station R Option B" PWR/CPU (CPU)

K12-5 (Spu-R)
PROJECT: FB11A
TITLE: "OUTPUT STATION R Option 9" - SPU-R

DATE: 9-25-75  ANALYST: L. Rom  DATE: 9-17-75  NODAL SET: 100

SNEAK CIRCUITS NETWORK TREE

"OUTPUT STATION R Option 9" PWR/CPU (CPU)

K13 (SPU-R)

SNEAK CIRCUITS NETWORK TREE

"OUTPUT STATION R Option 10" PWR/CPU (CPU)

K14 (SPU-R)
SEE N.S. 102
### SNEAK CIRCUITS NETWORK TREE

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<td>OUTPUT STA SEL RELAY</td>
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<td>OUTPUT LATCH RESET</td>
<td>1</td>
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<td>25</td>
<td>OUTPUT STORE STN</td>
<td>8</td>
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<td>27</td>
<td>OUTPUT PVU STN</td>
<td>9</td>
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<td>28</td>
<td>INPUT PVU PRESENT</td>
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<td>OUTPUT OPTION 9</td>
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<td>OUTPUT RPU STA SEL</td>
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<td>STA 2 - EJECT RACK</td>
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**PROJECT**  
FB-111A

**TITLE**  
CPU TO FIXPYL2 AND RPU

**QC DATE**  
9-26-75

**ANALYST DATE**  
9/17/75

**NODAL SET**  
125 - 144
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<tr>
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<td>SALVO SEL</td>
<td>P. Sikes</td>
<td>9-26-75</td>
<td>M. Barnett</td>
<td>9/19/75</td>
<td>145</td>
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**Diagram:**
- Power/CP1
  - 31006/49
    - SALVO SEL
      - RPU
      - 30001/44

**SNEAK CIRCUITS NETWORK TREE**

**Diagram:**
- Power/CP1
  - 31008/2
    - SPU-3
      - K19, OUTPUT LATCH RESET
        - Reference NX 294

**PROJECT**
- FB-111A
**TITLE**
- OUTPUT LATCH RESET

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<td>P. Sikes</td>
<td>9-26-75</td>
<td>M. Barnett</td>
<td>9/19/75</td>
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PROJECT  TITLE
FB-III A  EJECT RACK RPU

DATE  ANALYST  DATE  NODAL SET
9-26-75  M. Mathews  9/19/75  149

PROJECT  TITLE
FB-III A  OUTPUT PYLON JTSN

DATE  ANALYST  DATE  NODAL SET
9-26-75  M. Mathews  9/19/75  150
**INPUT-PYLON PRESENT**

(Pivot Pylon 2-70)

**PROJECT** | **TITLE** | **QC** | **DATE** | **ANALYST** | **DATE** | **NODAL SET**
---|---|---|---|---|---|---
FB-111A | INPUT-PYLON PRESENT | 9-26-75 | 9-19-75 | 151

**PROJECT** | **TITLE** | **QC** | **DATE** | **ANALYST** | **DATE** | **NODAL SET**
---|---|---|---|---|---|---
FB-111A | OUTPUT-STORE 3TSN SPU-3 | 9-26-75 | 9-19-75 | 152

**INPUT-PYLON PRESENT**

(Pivot Pylon 2-70)
No X References

WPN IDENT LINE 4

PUR/CPM
J1007/14

BRU 3A/1
J606/23

(IRM EJECT - NOT BID)
SNFAK
ClIJITS
NETWORK
TREE

PROJECT
FB111-A

TITLE
Output Option 5, SPU-3

DATE
9-29-75
ANALYST
J. Verges

NODAL SET
163

PROJECT
FB111-A

TITLE
Output Option 7, SPU-3

DATE
9-29-75
ANALYST
J. Verges
SNEAK CIRCUITS NETWORK TREE

CPU
\( \text{Output Option 8} \)
\( J1007/23 \)
\( \text{EN} \)
\( K15 \)
\( (SPU-3) \)
\( \text{GND} \)

PROJECT
FB111-A

TITLE
Output Option 8, SPU-3

REZ

PROJECT
FB111-A

TITLE
Output Option 9, SPU-3

QC/Signature
P. Stokoe

DATE
9-29-75

ANALYST
J. Vegers

DATE
9-17-75

NODAL SET
166
SNEAK CIRCUITS NETWORK TREE

CPU
↓
Output RPU STA SEL
↓
J1001/7

CPU
↓
Output Store JTSN
↓
V107/29

RPs
↓
K3
(GPU-4)

↓
GND

PROJECT | TITLE
-------|--------
FB111-A | Output Store JTSN, SPU-4

QC DATE ANALYST DATE NODAL SET
+/- 9.29.75 J. Veyes 9.18.75 170

REV
112-110576

REV
112-110576

Note: When Pylon is present a ground signal is sent to CPU.
SNEAK CIRCUITS NETWORK TREE

CPU

Output Option 9

\( \text{RMS} 276 \)

K16

(SPU-4)

GND

PROJECT

FB111-A

TITLE

Output Option 9, SPU-4

QC

DATE

9-29-75

ANALYST

J. Verges

DATE

9-22-75

NODAL SET

188

SNEAK CIRCUITS NETWORK TREE

CPU

Output Option 9

\( \text{RMS} 276 \)

K16

(SPU-4)

GND

PROJECT

FB111-A

TITLE

Output Option 9, SPU-4

QC

DATE

9-29-75

ANALYST

J. Verges

DATE

9-22-77

NODAL SET

188
SNEAK CIRCUITS NETWORK TREE

Diagram of a network tree with labels and connections:

- CPU
- Output Option 10
- Ki7
- SPu-4
- GND

PROJECT TITLE: Output Option 10, SPu-4

DATE: 9-29-75
ANALYST: Y. Vezin
NODAL SET: 189
NO CONNECTIONS

ENG DERICH

should not have been bid, no connection

CPU

OBJECT

FB-111A

TITLE

ENG DERICH — NO BID

QC

DATE 9-25-75

ANALYST Barnett

DATE 9/19/75

NODAL SET 190

BOEING AEROSPACE COMPANY — HOUSTON, TEXAS C-104
<table>
<thead>
<tr>
<th>PROJECT</th>
<th>TITLE</th>
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</thead>
<tbody>
<tr>
<td>FB-111A</td>
<td>SPU-5 OUTPUT LATCH RESET</td>
</tr>
<tr>
<td></td>
<td>Date 9-29-75, 9-18-75</td>
</tr>
<tr>
<td>Analyst</td>
<td>Date 9-18-75</td>
</tr>
<tr>
<td>Node Set</td>
<td>191</td>
</tr>
</tbody>
</table>

**Diagram:**
```
RPU/CPUL
31008/2

SPU-5
K19, OUTPUT LATCH RESET

x reference NE 278

CPU
31008

S OUTPUT RPU STA SEL 21

RPU 30001

NO X REFERENCES
```

<table>
<thead>
<tr>
<th>PROJECT</th>
<th>TITLE</th>
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<tr>
<td>FB-111A</td>
<td>OUTPUT RPU STA SEL</td>
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<td>Date 9-29-75</td>
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<td>Date 9-18-75</td>
</tr>
<tr>
<td>Node Set</td>
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</tr>
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</table>
SNEAK CIRCUITS NETWORK TREE

PROJECT  TITLE
FB-111A  WPN IDENT LINE 1 TO IRM EJECT

DATE  ANALYST  DATE  NODAL SET
9-29-75  R. D. Nather  9/18/75  199

SNEAK CIRCUITS NETWORK TREE

CPU
31008/12
WPN IDENT LINE 2
3613/70

36061/25
BRU 3A/AS
WPN IDENT LINE 2
36061/25
BRU 3A/AS
(CLR A EJECT - NOT BLD)

WPN BASED INDICATING LIGHT

PROJECT  TITLE
FB-111A  WPN IDENT LINE 2

DATE  ANALYST  DATE  NODAL SET
9-29-75  R. D. Nather  9/18/75  200
PROJECT: FB-111A  
TITLE: WPN IDENT LINE 3

QC:  
DATE: 9-29-75  
ANALYST:  
DATE: 9/18/75  
NODAL SET: 201

NO X REFERENCES

PWR/CPU
31C8/14

BRU 3A/15
3608/26

(NOT BAD - ZRA EJECT)

PROJECT: FB-111A  
TITLE: WPN IDENT LINE 4

QC:  
DATE: 9-29-75  
ANALYST:  
DATE: 9/18/25  
NODAL SET: 202

NO X REFERENCES

PWR/CPU
3108/13

BRU 3A/15
36061/23

(NOT BAD - ZRA EJECT)
CISNio0, K
TREJ REV
Fm
"Cl-
NE-
4h

PWR/CPU (NOT REAL PWR - ALL CPU PINS BID TO PWR)
31008/15

WP N IDENT LINE 5

AMC-5
Sk1
x reference NEI

PWR/CPU
31008/16

SPU-5
k7, output option 1
x reference 393

PROJECT TITLE
FB-111A WPN IDENT LINE 5

DATE ANALYST DATE NODAL OFF.
9-29-75 T. J. Barlow 9/18/75 203

PWR/CPU
31008/16

SPU-5
k7, output option 1
x reference 393

PROJECT TITLE
FB-111A OUTPUT OPTION 1, SPU-5

DATE ANALYST DATE NODAL SET
9-29-75 T. J. Barlow 9/18/75 204
PWR/CPU
1008/23

SPU-5
K15, SET, OUTPUT OPTION 8
x reference NS 1, 393

---

PROJECT | TITLE |
--- | --- |
FB-111A | OUTPUT OPTION 7, SPU-5 |

DATE | ANALYST | DATE | NODAL SET |
--- | --- | --- | --- |
9-29-75 | IM Barrett | 9-18-75 | 209 |

---

PWR/CPU
1008/23

SPU-5
K14, OUTPUT OPTION 7
x reference NS 1, 393

---

PROJECT | TITLE |
--- | --- |
FB-111A | OUTPUT OPTION 8, SPU-5 |

DATE | ANALYST | DATE | NODAL SET |
--- | --- | --- | --- |
9-29-75 | IM Barrett | 9-18-75 | 210 |
PROJECT TILE

FB-111A OUTPUT STORE JTSN — SPU-6

SNEAK CIRCUITS NETWORK TREE

PWR/CPU
31008/29

SPU-6
K3, OUTPUT STORE JTSN
X reference US 424

ANALYST DATE 9/19/75
M. BARNETT

NODAL SET 215

SNEAK CIRCUITS NETWORK TREE

PROJECT TITLE

FB-111A EJECT RACK RPU

ANALYST DATE 9/19/75
M. BARNETT

NODAL SET 216
SNEAK CIRCUITS NETWORK TREE

SNEAK CIRCUITS NETWORK TREE

INPUT CPU
S1008/34
SPU-6
K4, OUTPUT STORE JTEN
X REFERENCE NS 430 (197)

PROJECT | TITLE
---|---
FB-111A | OUTPUT STORE JTEN — SPU-6

DATE | ANALYST | NODAL SET
---|---|---
9-29-75 | M. Carrell | 219

* SNEAK CIRCUITS NETWORK TREE

PROJECT | TITLE
---|---
FB-111A | OUTPUT PYLON JTEN

DATE | ANALYST | NODAL SET
---|---|---
9-29-75 | M. Carrell | 220

* INCLUDES NS 428
SNEAK CIRCUITS NETWORK TREE

PWR/GPU
51008/36

WPN IDENT LINE 1

No X References

BRU 3A/16
36061/24

(IDM eject - not Did)

PROJECT
FB-11A

TITLE
WPN IDENT LINE 1

GC
P. St sleek

DATE
9-29-75

ANALYST
M. Barad

DATE
9-19-75

NODAL SET
221

SNEAK CIRCUITS NETWORK TREE

CPU
51008/37

WPN IDENT LINE 2

PRFPLN
3613/70

BRU 3A/16
36061/24

(IDM eject - not Did)

Maul-12-6

X Reference NS 1

GND
Maul-12-6

*WPN Released Indicating SW

PROJECT
FB-11A

TITLE
WPN IDENT LINE 2

GC
P. St sleek

DATE
9-29-75

ANALYST
M. Barad

DATE
9-19-75

NODAL SET
222
**SNEAK CIRCUITS NETWORK TREE**

![Diagram of SNEAK CIRCUITS NETWORK TREE with nodes and connections]

**PROJECT** FB-111A

**TITLE** OUTPUT OPTION 1 — SPU-6

**DATE** 9-29-75

**ANALYST** D. Stakes

**DATE** 9-19-75

**NODAL SET** 226

---

**PROJECT** FB-111A

**TITLE** OUTPUT OPTION 2 — SPU-6

**DATE** 9-29-75

**ANALYST** D. Stakes

**DATE** 9-19-75

**NODAL SET** 227

---

* Includes Nodal Set 429

---

**POWER/CPM**

1000/41

**SPU-6**

k1, Output Option 1

x Reference NS 396

---

**POWER/CPM**

5/08/42

**SPU-6**

k9, Output Option 2

x Reference NS 396, 397

---

**NS 429**
SNEAK CIRCUITS NETWORK TREE

OUTPUT OPTION 3 — SPU-6

K10, OUTPUT OPTION 3
x reference NS 596

OUTPUT OPTION 4 — SPU-6

K18, SET, OUTPUT OPTION 4
x reference NS 424
PROJECT: FB-111A

TITLE: OUTPUT OPTION 5 — SPU-6

DATE: 9-29-75
ANALYST: T. Dostreto
NODAL SET: 230

SNEAK CIRCUITS NETWORK TREE

SPU-6
K12B, SET, OUTPUT OPT. 5
x reference NS 312, 316

SPU-6
K12A
x reference NS 314

PROJECT: FB-111A

TITLE: OUTPUT OPTION 7 — SPU-6

DATE: 9-29-75
ANALYST: T. Dostreto
NODAL SET: 231

SNEAK CIRCUITS NETWORK TREE

SPU-6
K14, OUTPUT OPTION 7
x reference NS 1,396

DATE ANALYST
31008/47
PWR/CPU
3/30/75

SPU-6
K15, SET, OUTPUT OPTION 8
X Reference No 1, 396

PROJECT        TITLE
FB-111A        OUTPUT OPTION 8 —— SPU-6

DEPARTMENT  DATE  ANALYST  DATE  NODAL SET
P. Design   9-29-75  M. Barnett  9-19-75  232

PWR/CPU
3/30/75

SPU-6
K16, OUTPUT OPTION 9
X Reference No 424

PROJECT        TITLE
FB-111A        OUTPUT OPTION 9 —— SPU-6

DEPARTMENT  DATE  ANALYST  DATE  NODAL SET
P. Design   9-29-75  M. Barnett  9-19-75  233
PWR/CPU

J1008/50

SPU-6

K17, OUTPUT OPTION 10
x reference NS 1,395

PROJECT	TITLE
FB-111A	OUTPUT OPTION 10 — SPU-6

DATE	ANALYST	DATE
9-29-75	I. Brandy	9/19/75

NODAL SET
234

PWR/CPU

J1008/51

NO X REFERENCES

ENG DERICH

OPEN END
(not bid)

PROJECT	TITLE
FB-111A	ENG DERICH — NOT BID

DATE	ANALYST	DATE
9-29-75	M. Barnett	9/19/75

NODAL SET
235
<table>
<thead>
<tr>
<th>CPU</th>
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<tbody>
<tr>
<td>1</td>
<td>OUTPUT STA SEL RELAY</td>
</tr>
<tr>
<td>2</td>
<td>OUTPUT LATCH RESET</td>
</tr>
<tr>
<td>4</td>
<td>OUTPUT STORE JTSN</td>
</tr>
<tr>
<td>6</td>
<td>OUTPUT PLY JTSN</td>
</tr>
<tr>
<td>7</td>
<td>INPUT PLY PRESENT</td>
</tr>
<tr>
<td>8</td>
<td>OUTPUT STA SEL RELAY</td>
</tr>
<tr>
<td>9</td>
<td>OUTPUT STORE JTSN</td>
</tr>
<tr>
<td>10</td>
<td>OUTPUT PLY JTSN</td>
</tr>
<tr>
<td>11</td>
<td>WPN IDENT LINE 1</td>
</tr>
<tr>
<td>12</td>
<td>WPN IDENT LINE 2</td>
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<tr>
<td>13</td>
<td>WPN IDENT LINE 3</td>
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<td>16</td>
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<tr>
<td>17</td>
<td>OUTPUT OPTION 2</td>
</tr>
<tr>
<td>18</td>
<td>OUTPUT OPTION 3</td>
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<tr>
<td>3</td>
<td>OUTPUT RPU STA SEL</td>
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<tr>
<td>5</td>
<td>EJECT RACK RPU</td>
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</table>

**PROJECT:** FB-111A  
**TITLE:** CPU TO FIXPYL7 AND RPU
### Sneak Circuits Network Tree

<table>
<thead>
<tr>
<th>CPU</th>
<th>J1009</th>
<th>Fixpyl8</th>
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<tbody>
<tr>
<td></td>
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<td>39022</td>
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<tr>
<td>22</td>
<td>OUTPUT STA SEL RELAY</td>
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<td>OUTPUT LATCH RESET</td>
<td>1</td>
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<tr>
<td>25</td>
<td>OUTPUT STORE ITSN</td>
<td>9</td>
</tr>
<tr>
<td>27</td>
<td>OUTPUT PLY ITSN</td>
<td>7</td>
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<tr>
<td>28</td>
<td>INPUT PLY PRESENT</td>
<td>13</td>
</tr>
<tr>
<td>29</td>
<td>OUTPUT STA SEL RLY</td>
<td>11</td>
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<tr>
<td>30</td>
<td>OUTPUT STORE ITSN</td>
<td>12</td>
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<td>OUTPUT PLY ITSN</td>
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<td>WPN IDENT LINE 1</td>
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<td>33</td>
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<td>OUTPUT OPTION 1</td>
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<td>OUTPUT OPTION 3</td>
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<td>42</td>
<td>OUTPUT OPTION 9</td>
<td>19</td>
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<tr>
<td>24</td>
<td>OUTPUT RPU STA SEL</td>
<td>11</td>
</tr>
<tr>
<td>26</td>
<td>EJECT RACK RPU</td>
<td>24</td>
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</tbody>
</table>

**Project:** FB-111A  
**Title:** CPU TO FIXPYL8 AND RPU
D2-118576-3

SNEAK CIRCUITS NETWORK TREE

PWIR

"MAU FIRE 2"

J2/5 (RNW)

J6/5 (308)

J6006/11 (Fixed Pyl. 6)

No Data

PROJECT TITLE

"MAU FIRE 2" - Fixed Pylon 6

QC Date 9-30-75 Analyst Date 9-17-75 Nodal Set 280

SNEAK CIRCUITS NETWORK TREE

DWR

"MAU FIRE 1"

J2/6 (RNW)

J6/6 (308)

J6006/14 (Fixed Pyl. 6)

No Data

PROJECT TITLE

"MAU FIRE 1" - Fixed Pylon 6

QC Date 9-30-75 Analyst Date 9-17-75 Nodal Set 281
SNEAK CIRCUITS NETWORK TREE

PWR "MAU Fire 2"

J2/7 (RPU)

J10/15 (200)
J6022/29 (FIXCO PVL 7)

PROJECT
FB111A

TITLE
"MAU Fire 2" - Fixed Pylon 7

DATE
9-30-75

ANALYST
L. Blue

NODAL SET
282

SNEAK CIRCUITS NETWORK TREE

PWR "MAU Fire 1"

J2/8 (RPU)

J10/14 (200)
J6022/26 (FIXCO PVL 7)

PROJECT
FB111A

TITLE
"MAU Fire 1" - Fixed Pylon 7

DATE
9-30-75

ANALYST
L. Blue

NODAL SET
283
Assumed to be command from Remote Program Unit

J201/38 (BRU 3A/4A EJECTOR RACK)

to INFRA-RED MISSILES - NO DATA

See NOTE, NS 286.
"Rack Fire"

PWR

J9001/28

K2
K1

J9002/1

J9061/38 (BRU 3A/45)
to IKEA 400 module

(1) See Note D on NS. 286
SNEAK CIRCUITS NETWORK TREE

PWR/RPU

RACK FIRE

RPU 50003/4

308 26/14

FIX PYL 6 66006/13

FIX PYLON NOT ANALYZED

PROJECT
FB111-A

TITLE
FIX PYLON 6, RACK FIRE

QC
P. Stokes

DATE
9-30-75

ANALYST
P. Valke

DATE
9-23-75

NODAL SET
0284
SNEAK CIRCUITS NETWORK TREE

PROJECT
EB111-A

TITLE
FIX Pylon 7, Rack Fire

DATE
4-30-75

ANALYST
P. 0. K. 4-23-75

NODAL SET
02-96

DATE ANAL
1/6/76

SET
10003/7

NOT ANALYZED

Fix Pylon

3/10/76

Rack Fire

3/10/76

RPU

3/10/76

RPU

3/10/76
SNEAK CIRCUITS NETWORK TREE

PWR/RPU

RACK FIRE

RPU
3003/8

308
310/14

FIX PYL 8
36023/31

FIX PYLON
NOT ANALYZED

PROJECT
FB111-A

TITLE
FIX PYLON 8, RACK FIRE

QC DATE
9-30-75

ANALYST DATE
P. Vahlkn. 9-23-75

NODAL SET
10291

BOEING AEROSPACE COMPANY • HOUSTON, TEXAS C-141
<table>
<thead>
<tr>
<th>PROJECT</th>
<th>TITLE</th>
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<th>DATE</th>
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<tbody>
<tr>
<td>FB111-A</td>
<td>Manual Fire 2</td>
<td></td>
<td>9-30-75</td>
<td>J. Verge</td>
<td>9-23-75</td>
<td>292</td>
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<tr>
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<td>Manual Fire 1</td>
<td></td>
<td>9-30-75</td>
<td>J. Verge</td>
<td>9-23-75</td>
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</tr>
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</table>
Reference Designator is SPU-3 unless otherwise noted

All relay contacts shown have an NC set that is not connected—they have been deleted for clarity.
SNEAK CIRCUITS NETWORK TREE

ESS +28VDC

PWR/ESDC
C65
20A
PRG REL B
30V4A1

NRPU
RPU

J0004/1
ESS BUS

J0004/2
MAIN BUS

J0004/3
MAIN BUS

J0004/4
MAIN BUS

DOES NOT AGREE WITH
POWER SOURCE.
SEE DER # FB-111A-1

PROJECT
FB-111A

TITLE
PROG REL 'B'

QC DATE ANALYST DATE NODAL SET
H. Hoff 10/21/75

SNEAK CIRCUITS NETWORK TREE

ESS +28VDC

PWR/ESDC
C65
20A
PRG REL B
30V4A1

J10/19
309

J6022/28
FIX PLY1

J6022/28
FIX PLY2

J10/19
308

J6022/28
FIX PLY7

J6022/28
FIX PLY8

PROJECT
FB-111A

TITLE
WS 1, 2, 7, 8

QC DATE ANALYST DATE NODAL SET
H. Hoff 10/21/75

THE BOEING CO
C-145
SEE NODAL SET 381
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<tbody>
<tr>
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<td>AMAC-5</td>
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</table>

NODAL SET 299

SEE NODAL SET 375
SEE NODAL SET 370
SEE NODAL SET 365
Essential 28Vdc Power

304A1
20 Amp
CPU B
31001/2

CPU
See Nodal Set 294
See Nodal Set 276
See Nodal Set 278
All J6061 are BBU A/A and I RM EJECT - NOT BAD
All ref dez are SPU-6 unless otherwise stated
SNEAK CIRCUITS NETWORK TREE

PWR/HNDC

28VDC MN BUS

315A1

15A WPN STA 7

20 CB14

FIX PYL 7

J6022/26

OPEN

FIX PYLON

NOT ANALYZED

PROJECT

TITLE

FB111-A

POWER DISTRIBUTION STA 7

QC

DATE

ANALYST

DATE

NODAL SET

4/16/75

10/4/75

P. Val at 9-18-75

0322
SNEAK CIRCUITS NETWORK TREE

PWR/MNDC

28 VDC MN BUS

315A1

15A

WPN STA 2

10

20

CB28

FIX PYL 2

J6022/26

FIX PYLON

NOT ANALYZED

PROJECT

FB111-A

TITLE

POWER DISTRIBUTION STA 2

QC

DATE

10/4/75

ANALYST

Walt 9-18-75

NODAL SET

0323
PWR/4NDC

28VDC MN BUS

315A 1

15A

WPN STA 8

20 CB31

FIX PYL 8

J6022/26

FIX PYLON

NOT ANALYZED
Not wired within the pylons
PWR/MNDC

28VDC MN BUS

315A1

CPU A

20A

20 CB6

CPU

31001/1

CPU NOT BID

PROJECT
FB111-A

TITLE
Power Distribution CPU STA A

QC DATE ANALYST DATE NODAL SET
H 2/6/75 9-16-75 0327

HOU-175

BOEING AEROSPACE COMPANY - HOUSTON, TEXAS C-178
ELECTRICAL ANALYSIS OF B-52/FB-111 AMAC AND RELEASE CIRCUITRY U-ETC(U)
OCT 75
F29601-76-C-0017
UNCLASSIFIED D2-118576-3
PWR/TEST

404A1

LAMP TEST POWER

404A1

SEA

X REFERENCE: NS 334 through NS 341

✓ STOP
SNEAK CIRCUITS NETWORK TREE

POWER/DIM

404A1

31/x

(c) closing switch by-pass capacitor, increasing light brightness

SWITCHED GND

STOP

X REFERENCE : NS 334 → NS 341

PROJECT

FB-111A

SW GND, LAMP DIMMER

QC DATE ANALYST DATE NODAL SET

HOU-205 9/22/76 343

BOEING AEROSPACE COMPANY - HOUSTON, TEXAS C-191
SNEAK CIRCUITS NETWORK TREE

All "I" nodes reference NS-1.

PROJECT
FB-111A

TITLE
NUCLEAR CAUTION PANEL

ANALYST
M. M. Ditch

DATE
9/22/75

NODAL SET
344
*Code Enabling Switch to ARM Nuclear Weapon.*
See Nodal Set 318
See Nodal Set 318
Ground from AMAC to Weapon INTERFACE.

J9013/E
(AMAC-L)

GND
(AMAC-L)
Ground from AMAC to Weapon Interface.

59013/F (AMAC-L)

GND (AMAC-L)
This coil is not used: Ref. nodal set contain contacts that will not be switched.
"AMAC-L SPA STATION SELECT" picks up K12 and switches and into weapon when "OPTION SELECT" swx. is in an "ARM" position and "NUCLEAR CONSENT" swx. is in "ARM & RELEASE" position.
Command from Central Processor & Distributor Unit:
"SAF Class III Cmd. - UP."

J9013's are at SRAM interface

For all K3's, see NS-1
See Nodal Set 318
Command from Central Processor & Distributor Unit:
"SAF Class III Cmd - Down".
J9013's are at SRAM interface.
For all K's, ref. NS. 1.
**SNEAK CIRCUITS NETWORK TREE**

- Not connected in 404A1, Nuclear Weapon Control Panel

---

**PROJECT** FB111A  
**TITLE** Open End - No Function

<table>
<thead>
<tr>
<th>QC</th>
<th>DATE</th>
<th>ANALYST</th>
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<th>NODAL SET</th>
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<tbody>
<tr>
<td>D161</td>
<td>9/24/75</td>
<td>L. J.</td>
<td>9-16-75</td>
<td>355</td>
</tr>
</tbody>
</table>

---

**SNEAK CIRCUITS NETWORK TREE**

- Not connected in 404A1, Nuclear Weapon Control Panel

---

**PROJECT** FB111A  
**TITLE** Open End - No Function

<table>
<thead>
<tr>
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<td>D161</td>
<td>9/27/75</td>
<td>L. J.</td>
<td>9-16-75</td>
<td>356</td>
</tr>
</tbody>
</table>
GROUND FROM AMAC TO WEAPON INTERFACE.
Ground from AMAC to Weapon Interface.
This coil is not used. Ref nodal set contains contacts that will not be switched.
"AMAC-R SPA STATION SELECT" picks up K12 and switches GND into weapon when "OPTION SELECT" swx. is in "ARM" position and "NUCLEAR CONSENT" swx. is in "ARM / RELEASE" position.
Ground from AMAC to weapon interface.
Ground from AMAC to Weapon interface.
This coil is not used. Ret nodal set contains contacts that will not be switched.
"AMAC-3 SPU STATION SELECT" picks up K12 and switches and into weapon when "option select" swk. is in "arm" position and "nuclear consent" swk. is in "arm / release" position.
SNEAK CIRCUITS NETWORK TREE

NOTES:
1. REF DES IS AMAC-3 UNLESS OTHERWISE SHOWN
2. INCLUDES NODAL SET 303
3. 37013 HATES WITH BANG INTERFACE.
GROUND FROM AMAC TO WEAPON INTERFACE.
SNEAK CIRCUITS NETWORK TREE

\[ 59013/F \]
\[ \text{(AMAC-4)} \]

\[ \text{GND} \]
\[ \text{(AMAC-4)} \]

Ground from AMAC to Weapon Interface.
This coil is not used. Ref. nodal set will not be switched.

Nodal set 35

K14 (AMAC=4)
"AMAC-4 SWP STATION SELECT" picks up K12 and switches and into weapon. When "OPTION SELECT" SWX. is in an "ARM" position and "NUCLEAR CONSENT" SWX. is in "ARM I RELEASE" position.
Ground from AMAC to weapon interface.
GROUND FROM AMAC TO WEAPON INTERFACE.
This coil is not used: Ref. nodal set contain coils that will not be switched.
"AMAC-5 SPN STATION SELECT" picks up K12 and switches GND into weapon when "OPTION SELECT" SWK is in an "ARM" position and "NUCLEAR CONSENT" SWK is in "ARM & RELEASE" position.
Ground from AMAC to Weapon Interface.
GROUND FROM AMAC TO WEAPON INTERFACE.

59015/F
(AMAC-6)

GND
(AMAC-6)
This coil is not used. Ref. nodal sets contain coils that will not be switched.

Title: Unused K14 - AMAC-6

Project: FB111A

Boeing Aerospace Company - Houston, Texas C-225
"AMAC-6 SPA STATION SELECT" PULL UP K12
AND SWITCHES AND INTO WEAPON WHEN
"OPTION SELECT" SWX IS IN AN "ARM" POSITION
AND "NUCLEAR CONSENT" SWX IS IN
"ARM & RELEASE" POSITION.

PROJECT

FB111A

AMAC-6 "STATION 6 SELECT"

QC DATE ANALYST DATE NODAL SET
L14/14 9/29/75 9/28/75 379

BOEING AEROSPACE COMPANY - HOUSTON, TEXAS C-226
See Nodal Set 318
Command for this coil originates in SRAM, in CPDU: "Hydraulic Power On Cmd". Out of scope.
Command for this coil originates in SRAM, in CPDU: "Hydraulic Power ON CMD". OUT OF SCOPE.
Command for this coil originates in SRAM, in CPDU: "Hydraulic power on CMD". Out of scope.
Command for this coil originates in SRAM, in CPMU: "Hydraulic Power on CMD". Out of scope.
See NS 295
Typical for MAU-12-4.5+6 tail Arming Solenoid connected to SPU-4,5 and 6 & SK9 respectively.

No contacts are open and have been deleted here.
<table>
<thead>
<tr>
<th>PROJECT</th>
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<tbody>
<tr>
<td>F2-B11A</td>
<td>PYLON JET &amp; CZ CARTRIDGE BREECH SQUIBS</td>
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<th>ANALYST</th>
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<tbody>
<tr>
<td></td>
<td>IN HANRIETT</td>
<td>9/16/75</td>
<td>399</td>
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See NS 276
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<tr>
<td>TITLE</td>
<td>Tail Arming Sation — No Connection</td>
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<tr>
<td>QC</td>
<td>DATE</td>
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<td></td>
<td>ANALYST</td>
</tr>
<tr>
<td></td>
<td>NODAL SET</td>
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</table>

See NS 388
See NS 279
See NS 278
See NS 388
Reference Designator is SPU-6 unless otherwise noted.

All relay contacts shown have an NC set that is not connected—they have been deleted for clarity.
See Nodal Set 321D
See NS 388
KII NOT USED: EMPTY SOCKET WITH HARDWIRED GROUND.
OPEN SRAM INTERFACE

SPI01-1/4
SPIU-L

SPU-L
K8, SET
no x reference, no title

PROJECT
FB-111A

TITLE
K8, SET coil, SPU-L

QC DATE ANALYST DATE NODAL SET
V160T 9/30/75 Im Mooratt 9/22/75 401

BOEING AEROSPACE COMPANY - HOUSTON, TEXAS C-248
K11/9

(SPU-R)

K11 NOT USED: EMPTY SOCKET WITH HARDWIRED GROUND.
KII NOT USED: EMPTY SOCKET WITH HARDWIRED GROUND.
OPEN

3900/5

SPU-R

SPU-R

K17, no remarks

x reference NS 319
OPEN  I/O RAM INTERFACE

SPA-R

k8, set
no x reference, no title
To station 23 SRAM disconnect - not in scope.
SNEAK CIRCUITS NETWORK TREE

SEG N.S. 294

<table>
<thead>
<tr>
<th>OBJECT</th>
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<tr>
<td></td>
<td></td>
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See NS 160
See N.S. 276
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See Nodal Set 172

See Nodal Set 175
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<td>OUTPUT OPTION 2</td>
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See No. 182
See NS 277
SEE N.S. 278
See NS 195
See N.S. 198
See NS 205
See NS 279
To Station 23 SRAM Disconnect - Not in Scope.
SEE N.S. 321 D
See N.S. 217
SEE NS. 220
See NE 227
SEE NODAL SET 395
See N.S. 67

See N.S. 64
See Nodal Set 36
SEE NODAL SET 317

PROJECT
FB-111A

TITLE
NUCLEAR UNLOCK

QC DATE ANALYST DATE NODAL SET

SNEAK CIRCUITS NETWORK TREE

31001/3 CPU

GRD/2406 CPU

PROJECT
FB-111A

TITLE
GROUND

QC DATE ANALYST DATE NODAL SET

Boeing
NODAL SETS 441—459 ARE GROUND WIRES ONLY AND WERE BID IN ERROR.
NO CROSS REFERENCES
ELECTRICAL ANALYSIS OF B-52/FB-111 AMAC AND RELEASE CIRCUITRY

OCT 75

UNCLASSIFIED

D2-118576-3

F/6 15/6

F29601-76-C-0017

NL
SNEAK CIRCUITS NETWORK TREE

Ref. Des.
FIXPYL8 , OPEN

J6022

35

'308'
\( \sqrt{ \text{GRD/2555} } \)

FIXPYL8 , OPEN

J6022

30

'308'
\( \sqrt{ \text{GRD/2556} } \)

FIXPYL7 , OPEN

J6022

30

'308'
\( \sqrt{ \text{GRD/2559} } \)

FXPYL7 , OPEN

J6022

35

'308'
\( \sqrt{ \text{GRD/2559} } \)

NO X REFERENCES

PROJECT
FB-111A

TITLE
GRDS TO FIXPYL 7 & 8

QC
H. MILLER

DATE 9/30/75

ANALYST
T.R. SEWELL

DATE 9/22/75

NODAL SET 465-468

BOEING AEROSPACE COMPANY - HOUSTON, TEXAS C-284
Switch Reset Coil

RBS TONE SWITCH — sends tone out

"WF = 1 or 2 (Tone 1 or 2)"

automatically resets at Bomb Rise
used for scoring on test or training runs.

TONE 2

TONE 1

All ref des: 403A1

No Cross References

Δ Not used in Nuclear Gravity Bomb circuitry
K11 exists as a wired socket with no relay installed.

This circuit is identical for SPU-L and SPU-R.
Ref Doc is SPu-R

No connecting wiring to these wires

SAAM Wiring - not analyzed
No connecting wiring to these wires
No connecting wiring to these wire segments

Unused switch contacts
No connecting wiring to these wires.
No connecting wiring to these wires.

<table>
<thead>
<tr>
<th>PROJECT</th>
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<th>Unused Data</th>
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<tbody>
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<tbody>
<tr>
<td>4</td>
<td>10/2/75</td>
<td>J. Verges</td>
<td>9-29-75</td>
<td>Unused Data</td>
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No connecting wiring to these wires.

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</table>
No connecting wiring to these wires
This appears to be false capability wiring since it goes open end in all of the Markes and the Alektor Wagner.

Panel (Figure)
This circuit is not used for nuclear weapons.