THESIS

DYNAMIC LINKING IN A MICROCOMPUTER ENVIRONMENT

by

Gerald Bertram Elanton

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Dynamic Linking in a Microcomputer Environment.

This thesis presents the detailed design for a dynamic linker suitable for microcomputer operation. The design exhibits the usual property of dynamic linking in that the binding of inter-procedure symbolic references to virtual addresses is deferred until the symbolic reference is first encountered during process execution. The design includes the specifications of dynamic linker modules and data structures. Furthermore, an overview of necessary operating system support is presented along with a detailed discussion of all additional translator output required. Hardware features...
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Dynamic linking without translator support and unlinking of an object (from a process address space) are investigated. A subset of the dynamic linker design (not including the unlinking capability) was implemented on an Intel 8080 microprocessor as a demonstration of the feasibility of the concepts introduced.
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ABSTRACT

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TABLE OF CONTENTS

I. INTRODUCTION -------------------------------------------- 12
II. BACKGROUND ----------------------------------------------- 12
   A. THE TRADITIONAL LINKING LOADER ---------------------- 13
   B. DYNAMIC LINKING -------------------------------------- 14
   C. OPERATING SYSTEM ENVIRONMENT FOR A DYNAMIC LINKER 15
      1. The Logical Levels of an Operating System --- 15
      2. An Introduction to the Address Space Manager ------- 17
   D. TERMINOLOGY ------------------------------------------ 18
III. THE LINKING PROCESS : AN OVERVIEW -------------------- 27
   A. THE WALKTHROUGH ------------------------------------- 21
   B. A SYNOPSIS OF THE WALKTHROUGH -------------------- 25
IV. THE SPECIFICS OF DYNAMIC LINKING ---------------------- 31
   A. FUNCTIONS OF A LINKER ------------------------------- 32
      1. Snapping a link ------------------------------------ 36
         a. Procedure Links --------------------------------- 32
         b. Data Links ------------------------------------- 33
         c. Construction of a List of Snapped Links --------- 37
      2. Building Linkage Tables ----------------------------- 37
      3. Unsnapping Objects --------------------------------- 41
   B. OPERATING SYSTEM SUPPORT ----------------------------- 45
      1. The Address Space Manager -------------------------- 47
      2. The Process Reference Table ------------------------- 48
      3. Object Deletion from a Process Address Space------ 48
## SUBJECT INDEX

<table>
<thead>
<tr>
<th>Section</th>
<th>Pages</th>
</tr>
</thead>
<tbody>
<tr>
<td>4. Process Initialization</td>
<td>45</td>
</tr>
<tr>
<td>C. TRANSLATOR SUPPORT</td>
<td>49</td>
</tr>
<tr>
<td>1. External References</td>
<td>22</td>
</tr>
<tr>
<td>2. Symbolic Name Tables and Templates</td>
<td>51</td>
</tr>
<tr>
<td>3. Entry_Names and Entry_Points</td>
<td>51</td>
</tr>
<tr>
<td>D. DYNAMIC LINKING TABLES</td>
<td>53</td>
</tr>
<tr>
<td>1. The Symbolic Name Table</td>
<td>57</td>
</tr>
<tr>
<td>2. The Linkage Table</td>
<td>55</td>
</tr>
<tr>
<td>a. The Initialized Linkage Table</td>
<td>55</td>
</tr>
<tr>
<td>b. Format of Snapped Links</td>
<td>56</td>
</tr>
<tr>
<td>3. The Linkage Address Table</td>
<td>57</td>
</tr>
<tr>
<td>F. IMPLEMENTATION OF ENTRY_NAMES AND ENTRY_POINTS</td>
<td>61</td>
</tr>
<tr>
<td>7. LINKING WITHOUT TRANSLATOR SUPPORT</td>
<td>62</td>
</tr>
<tr>
<td>A. THE INTERFACE MODULE</td>
<td>62</td>
</tr>
<tr>
<td>1. Linking of Procedures</td>
<td>67</td>
</tr>
<tr>
<td>2. Linking of Data</td>
<td>69</td>
</tr>
<tr>
<td>B. LIMITATIONS OF UNSUPPORTED LINKING</td>
<td>72</td>
</tr>
<tr>
<td>VI. HARDWARE THAT ENHANCES DYNAMIC LINKING</td>
<td>72</td>
</tr>
<tr>
<td>A. HARDWARE FEATURES AFFECTING LINKER LIFETIME</td>
<td>72</td>
</tr>
<tr>
<td>1. Hardware Indirection and Faults on Indirection</td>
<td>77</td>
</tr>
</tbody>
</table>
| 2. Other Features Influencing Linker 
  Implementation                              | 74    |
| E. HARDWARE FEATURES AFFECTING SYSTEM 
  PERFORMANCE                                  | 75    |
| VII. A DEMONSTRATION OF DYNAMIC LINKING     | 77    |
| A. THE MODULES OF THE DYNAMIC LINKER         | 78    |
APPENDIX C - TEST PROGRAMS SOURCE LISTINGS

A. DEMO SOURCE LISTING, TEMPLATE, AND RELOCATION BITS

B. MEDIT SOURCES LISTING AND TEMPLATE

C. MULT SOURCE LISTING, TEMPLATE, AND RELOCATION BITS

D. DIFEY SOURCE LISTING, TEMPLATE, AND RELOCATION BITS

E. SUM SOURCE LISTING, TEMPLATE, AND RELOCATION BITS

F. ARRAY SOURCE LISTING AND TEMPLATE

G. OUTPUT OF DEMO

H. OUTPUT OF SUM

I. LIST OF REFERENCES

INITIAL DISTRIBUTION LIST
LIST OF FIGURES

1. Sequence of Events for Snapping a Link to the Procedure <Target|Entry_Name> .......................... 27
2. Sequence of Events for Subsequent References to <Target|Entry_Name> .................................. 28
3. Sequence of Events for Subsequent References to <Data|Entry_Name> ..................................... 29
4. Translated External Reference ..................................................... 34
5. Linkage Table Entries for a Snapped Procedure Link ............... 35
6. Initialized Linkage Table ............................................. 39
7. Process Reference Table Entry ............................................. 46
8. Symbolic Name Table Entry ................................................ 53
9. Data Segment Symbolic Name Table and Linkage Table .......... 56
10. Data and Procedure Snapped Outgoing Links ................. 59
11. Sequence of Events for Linking Unsupported Objects ........ 64
12. Unsupported Linkage Table ............................................. 68
13. The Subroutines of the Linker Module ........................... 64
14. Dynamic Linking in DEMO .............................................. 79
15. Pseudocode for SUM .............................................. 111
16. Object Code for SUM .................................................. 127
17. Combined Linkage Table for SUM .................................... 127
I. INTRODUCTION

Dynamic linking has been previously assumed to be restricted to those computing systems that were specifically designed to support a dynamic linker. The first goal of this thesis was to determine if specialized hardware, such as found in Multics [11], is essential to realize dynamic linking. And, given that specialized hardware is not necessary, the second goal was to design a linker compatible with existing microcomputer architectures.

The design of a dynamic linker was developed with a basic set of design criteria (Table 1) established as guidelines. (A complete discussion of the implications of these criteria is delayed until the end of this thesis.) The most fundamental criterion which characterizes dynamic linking relates to when an object is bound to a virtual address within a process address space. In the traditional static environment, this binding occurs prior to program execution. In a dynamic linking environment, binding is delayed until an object is first referenced by a process. This capability allows tremendous flexibility in the development of software systems.
TABLE 1 - DESIGN CRITERIA FOR A DYNAMIC LINKEP

1. Delayed Binding - The binding (linking) of an external object to a virtual address (within a process address space) must not take place until the object is first referenced during program execution.

2. Limited Overhead - Subsequent references to an object (i.e., references following the first reference) must not impose excessive overhead with respect to process execution speed and primary storage.

3. Domain Independence - The dynamic linker must be compatible with current secure operating system designs. In a multidomain environment, the dynamic linker must be capable of executing in the domain of the calling subroutine (vice executing in the security kernel1).

4. Syntactic Compatibility - The design must allow external objects to be utilized in the same context as internally defined procedures and data. (This implies that external objects can be used as parameters subject only to the limitations of the language syntax.)

5. Pure Object Code - The dynamic linker must permit the object code of a procedure to remain pure, allowing sharing of procedures in a multiprogramming environment.

6. Hardware Independence - The design must be implementable on a microprocessor which does not possess those hardware features specifically associated with dynamic linking. In Multics [11], the features include:

   a. hardware segmentation
   b. demanding paging
   c. indirect addressing through memory
   d. a linkage fault during indirection

---

1 It has been shown [7] that it is not necessary for the dynamic linker to reside in the security kernel to maintain system security.
II. BACKGROUND

The traditional concept of linking and loading [14] involves one, or possibly two operating system routines that load several distinct objects into memory, combine them into one address space (loading), and finally resolve addressing between objects (linking). The end result is an executable program.

The static and inflexible functions carried out by the linking loader place undesirable limitations on program development. First, a program must be intact (i.e., contain all objects required for proper execution) prior to run time. Second, if a module is changed, the whole program must be relinked. Furthermore, a module may be statically linked to several programs resulting in multiple copies of a module existing within the system. Dynamic linking is proposed as an alternative to static linking that solves these problems.

Dynamic linking [9, 11, 14] offers two other major advantages over static linking. First, dynamic linking allows a programmer to write and test incomplete programs since one may include in a subroutine a reference to an as yet unwritten external object and, as long as the reference is never executed, the program will not experience a run time error. In the field of software development, this feature is advantageous since incomplete modules may still
be tested individually. (It should be noted at this time that once the user has a completely tested product, it may be desired to statically link modules together to avoid the run time overhead associated with dynamic linking.)

The second major advantage of dynamic linking is that modules of a program need not be generated by the same translator. For example, in a dynamic linking environment one may use FORTRAN to do some double precision scientific calculations. If the results were then stored in an external data structure, they could be displayed using a dynamically linked module written in a more suitable language for I/O formatting such as PL/1. Because the modules 'communicate' via the external data structure, and are dynamically linked to each other, they need not be from the same translator. (Note that a dynamic linker does not prohibit such a 'heterogenous' program from executing but may not be sufficient in itself to allow proper execution.)

A. THE TRADITIONAL LINKING LOADER

First of all, the 'linker' and the 'loader' should be considered separate operating system functions. Linking may still be viewed as the combining of several objects into one program; however, the loading process actually consist of two distinct operations. The popular concept of a loader is one of a static operation prior to run time which takes some object code associated with a program and 'loads' this code.
into main memory where it can be executed. This is the second function of a loader. The loader must first determine where each object will be placed in the address space of the process (viz., a program in execution). (This traditional concept views the address space as a linear array of memory locations.) After loading, the linking loader would link distinct objects into a single program by resolving the addressing of data and procedures defined external to individual subroutines. (It is noted that some reverse the order by linking loaders may link before loading).

3. DYNAMIC LINKING

The alternative to the static linking phase of the linking loader is to dynamically link separate objects at run time. This involves objects referred to in the source code of a program by a symbolic name only. The complete operation (including a dynamic linking phase) dictates that the object be located, and added to the address space of a program (i.e., assigned a virtual address). Then the reference to an object's symbolic name is converted into an addressing instruction using the object's virtual address. This implies that a subroutine as it exist at the beginning of run time cannot properly execute since the object code produced from a reference to a symbolic name must be converted into a virtual address in the address space of a process. This address conversion is known as dynamic
In order to support dynamic linking a system must have the ability to enter objects in the address space of a process during run time. Additionally, the operating system must be able to 'load' an object into memory during program execution. As has been noted these two functions traditionally have been considered operations associated with the loader. However, it should be apparent that this 'loading' is actually a function of dynamic memory allocation using techniques such as paging, segmentation, or dynamic relocation. Thus in a dynamic linking environment the loader functions are carried out by the operating system memory management that enters objects in a process address space.

C. OPERATING SYSTEM ENVIRONMENT FOR A DYNAMIC LINKER

1. The Logical Levels of an Operating System

It is useful at this time to propose an abstract operating system as an environment in which a dynamic linker will exist. This operating system consists of four hierarchical levels. (An operating system design along these lines has been shown feasible for microcomputers [16].) The most fundamental level consists of the hardware associated with the target machine. Above this level is a software kernel that includes the most basic software primitives including memory management, file primitives, and
multiprocessing support. Conceptually, the kernel includes those software routines which, in a secure operating system, must be protected from malicious or inadvertent tampering.

In a multiprogramming environment, the kernel provides the capability to multiplex resources (i.e., line printers, disk units, etc.) for various user processes.

The level above the kernel, the supervisor level, consist of those operating system routines which need not exist in the kernel. In general, the supervisor provides common services to all users. The final level is the user level where user programs and data reside. (It has been shown (by Jansen [7]) that the linker should be able to reside in all user levels. Jansen [7] also demonstrated that the dynamic linker need not and, more importantly, should not exist in the kernel.)
2. An Introduction to the Address Space Manager

Before an object can be linked, it must be addressable by a process. In a static environment, this would equate to loading the object in the address space of a process by allocating to it a linear block of memory. Essentially this is what is done in a dynamic environment except the object retains its identity as a distinct segment and is allocated a virtual address \(^2\). (In this thesis, virtual addresses will be considered to consist of a segment number plus some offset from the base of that segment.) The assignment of a virtual address to an object will be done by the address space manager.

The address space manager is invoked by the dynamic linker with a request to make an object known. The address space manager does this by assigning to the object a unique identifier, such as a segment number, that can be used to access the object within the process address space. An entry for the object will then be made by the address space manager in a table to prevent assigning multiple identifiers to the same object. This implies that a search would first be made of this table, which is called the Process Reference Table.

\(^2\) A virtual address is a potentially relocatable address which may be converted into an absolute address by hardware. It may consist of a segment number and offset, or some other relative format in which the base address of the segment is added to an offset to achieve the absolute address. (However this does not imply that segmentation hardware is necessary in a dynamic linking environment.)
Table 3, to determine if the object is already known. If not, the address space manager would have the object assigned a segment number (identifier), create an entry for the object in the process reference table, and return this segment number to the linker.

D. TERMINOLOGY

In order to ensure that the terminology used is understood, the following definitions are offered.

A subroutine will be defined as a basic unit of standalone, executable code (i.e., a procedure). Several subroutines and data objects can be combined to form a program. Stated another way, a program consist of all subroutines and data modules utilized by that program during its execution. A process [1] is a program in execution and is characterized by an execution point (usually defined by a hardware program counter) and an address space. During execution, a subroutine may call an external object that is known to that subroutine only by its symbolic name prior to execution. The reference to an external object within a subroutine will be called an external reference [15]. An external object [4] may consist of either data (external data) or an external procedure (that is itself a subroutine). Each object is a distinct logical entity and

3 In Multics [11], the process reference table is called the known segment table.
will at times also be referred to as a segment [14]. (An effort is made to use the term "object" whenever possible to avoid the implication that a processor featuring hardware segmentation is necessary in a dynamic linking environment.)
III. THE LINKING PROCESS: AN OVERVIEW

Before detailing the dynamic linking process, a brief walkthrough of the steps involved in establishing a link between the subroutine <Caller> and some external procedure <Target|Entry_Name> will be investigated. <Entry_Name> represents one of multiple accesses, or entry points, into <Target>. An entry point into an object can be considered a label that can be referenced by an external object. Associated with each entry point is a unique entry name, and an entry point offset that represents the relative offset of the entry point from the starting location of the object.¹)

Fundamentally, the following events must occur to link <Target|Entry_Name> to <Caller>. The linker must be invoked when a reference to <Target|Entry_Name> is first encountered. The linker must be capable of accessing the symbolic name "Target|Entry_Name" and using that symbolic name to learn the segment number of <Target>. The linker will then establish a link to <Target|Entry_Name> such that subsequent references found in <Caller> will not require invocation of the linker but instead will result in either a call to <Target|Entry_Name>, in the case of an external

¹ The term 'entry point' has evolved as representing either the label 'entry point' or the offset associated with that label [11, 14]. This convention will be continued in this thesis and, where the possibility of ambiguity exist, a comment will be made to ensure clarity.
procedure, or a memory reference to the virtual address of
some external data.

A. THE WALKTHROUGH

When the translator encounters an external reference in
the source code of <Caller>, it will enter the symbolic name
"Target|Entry_Name" in the symbolic name table for <Caller>.
(The symbolic name table of <Caller> contains the symbolic
names of external references and data associated with each
entry point found in <Caller>. Additionally, the symbolic
name table exists at run time.) The object code produced for
the external reference to <Target|Entry_Name> (as found in
<Caller>) consist of a procedure call to a virtual address
in <Caller>'s linkage table. (This virtual address is
constructed at run time using a base register, called the
linkage pointer, and some offset into Caller.link generated
by the translator.) The virtual address called is an entry
in Caller.link set aside for <Target|Entry_Name> and will be
referred to as an outgoing link. The outgoing link has been
initialized to invoke the linker and pass to the linker the
offset (in Caller.sym) of the symbolic name
"Target|Entry_Name". The linker uses this offset along with

---

5 The symbolic name table of an object will be called
object.sym, while object.link will refer to an object's
linkage table. Thus <Caller>'s symbolic name table and
linkage table become Caller.sym and Caller.link
respectively.
the virtual address of the base of Caller.sym (which is stored in Caller.link), to access the symbolic name of the external reference. Once located, the linker will pass the symbolic name "Target" to the address space manager.

The address space manager first determines if an entry for <Target> already exist in the process reference table. If not, the address space manager will locate the object <Target> and have it assigned a segment number in the address space of the executing process. It will also make an entry for <Target> in the process reference table and return to the linker the segment number of <Target>. (It is at this point that <Target> is 'known' to the executing process.)

The linker now knows the segment number of <Target> and must create a linkage table for <Target> (if one has not already been constructed by an external reference to <Target> within another subroutine). A template accessible to the linker has been constructed by the translator for this purpose and is appended (after minor computations) to the end of the combined linkage table 6 (as Target.link). (The building of a linkage table for <Target> allows it to engage in dynamic linking.) Additionally, the starting address of Target.link is entered in a data structure known

6 The combined linkage table contains the linkage tables of each object in a process. (Note that it is not necessary to utilize a combined linkage table in an implementation since each object's linkage table could be allocated its own segment.)
as the Linkage Address Table, making it available for future linking evaluations. (The linkage address table of a process can be considered an array containing the base address of each object's linkage table and is subscripted by the object's segment number.)

A complete virtual address for <Target|Entry_Name> can be constructed by searching Target.sym for "Entry_Name" to discover the entry point (offset) and incoming link offset associated with "Entry_Name". (An incoming link is a section of an object's linkage table set aside to allow the performance of housekeeping functions prior to invoking the object.)

The linker will now alter the outgoing link (in Caller.link) to jump to the incoming link (found in Target.link). The linker then constructs the incoming link to jump to the virtual address of <Target|Entry_Name> after setting the linkage pointer to point to Target.link. (The linkage pointer is a global pointer, e.g., hardware register, which always points to the currently executing subroutine's linkage table. Thus before execution in <Target> can commence, the linkage pointer must be set to point to Target.link. The reason for this will be discussed later.) After the outgoing and incoming links are executed, the process will be executing in <Target>.

When <Target> has finished it will execute a return
instruction. Recall that the only procedure call in the linkage sequence was <Caller>'s call to the outgoing link (in Caller.link) ensuring a return to <Caller> after the completion of <Target>. The final step is to reset the linkage pointer to the virtual (base) address of Caller.link. (This is done by the translated external reference in <Caller>.)

The steps followed for linking external data would be similar except data is not executed. Therefore, the outgoing link need not "invoke" the data (via the incoming link) but instead must allow <Caller> to reference the data. If indirect addressing is available, the outgoing link can be a storage location for the virtual address of the external data and can be referenced via an indirect addressing instruction. (Note that on the first reference, this indirect addressing instruction must be able to invoke the linker in some fashion. In Multics, this is done by generating a fault which invokes the linker as the fault handler.) If indirect addressing is not available (or cannot be used to invoke the linker on first reference to the data), the outgoing link can contain executable instructions which load some pointer with the virtual address of the data and then return the execution point to <Caller>. 

24
B. A SYNOPSIS OF THE WALKTHROUGH

To provide the reader with an abbreviated review of the steps to snap a link, the following synopsis is provided. Additionally, figure 1 is annotated with the number of each "step" to provide added clarity. When the executing procedure (i.e., <Caller>) encounters a translated external reference to <Target|Entry_Name> for the first time, the following sequence of events transpires:

Step 1 - The execution point is transferred to the outgoing link (in Caller.link).

Step 2 - The linker is invoked by the initialized outgoing link. The linker is passed the offset of <Target|Entry_Name>'s entry in Caller.sym as an argument.

Step 3 - The linker references Caller.sym and extracts the symbolic name "Target|Entry_Name" and the offset (in Caller.link) of the (appropriate) outgoing link for <Target|Entry_Name>.

Step 4 - The linker invokes the address space manager with the argument "Target".

Step 5 - The address space manager enters <Target> in the process address space (if necessary) and returns to the linker the segment number of <Target>.

Step 6 - The linker builds a linkage table for <Target> (not shown).

Step 7 - The linker searches Target.sym for "Entry_Name" and extracts the offset of the incoming link (for Entry_Name) in Target.link, and the entry point associated with Entry_Name.

Step 8 - The linker computes the virtual address in <Target> associated with <Target|Entry_Name> and the virtual address of Entry_Name's incoming link.
Step 9 - The linker establishes the link by entering a jump to the incoming link in the outgoing link (in Caller.link); and by loading the incoming link (in Target.link) with an instruction which loads the linkage pointer with the address of Target.link and a jump to the entry_point in <Target>.

Step 10 - The linker invokes <Target> at the entry_point.

Figures 2 and 3 are included to show the execution sequence of a snapped link for procedures and data respectively. It is noted that a link that has already been established does not require the invocation of the linker but rather directly references the external object.
SEQUENCE OF EVENTS FOR SNAPING A LINK TO THE PROCEDURE <TARGET,ENTRY_NAME>

FIGURE 1
SEQUENCE OF EVENTS FOR SUBSEQUENT REFERENCES TO \langle TARGET\|ENTRY_NAME\rangle

FIGURE 2
IV. THE SPECIFICS OF DYNAMIC LINKING

A. FUNCTIONS OF A LINKER

Dynamic linking centers around the ability to alter impure code (linkage tables) during run time. It is this feature which allows invocation of the linker on the first reference (to an object) and yet permits subsequent references to the same object to access that object directly (i.e., without invocation of the linker). Establishing, or snapping [11], a link does not represent all the functions desirable in a linker. Linkage tables must be constructed on the first reference (within a process) to an object, and system limitations may subsequently force the removal, or unlinking, of an object from a process address space.

1. Snapping a Link
   a. Procedure Links

      When snapping a link between procedures, the linker will initially be passed the offset (in Caller.sym) of (the entry for) the symbolic name "Target|Entry_Name". The linker can find Caller.sym via a pointer stored in Caller.link. (Recall that the linkage pointer always indicates the executing procedure's linkage table ensuring the linker can locate Caller.link.) Now the linker knows the

5 It should be noted that linkage tables avoid the undesirable effects normally associated with impure code by being serially reusable and a per process entity (i.e., one linkage table per process for each object).
symbolic name of the object to be linked, but it must
determine a virtual address within the object to be
referenced.

In order to make <Target|Entry_Name> addressable, the linker must determine the segment number
associated with <Target>, and the entry_point associated
with Entry_Name. To determine the segment number of
<Target>, the linker will invoke the address space manager
passing the symbolic name "Target" as an argument. The
address space manager will enter <Target> in the process
address space (if it is not already) and return <Target>'s
segment number to the linker Obtaining the segment number is
trivial since the address space manager will return this
information to the linker when passed the symbolic name
"Target".

Finding the entry_point associated with
Entry_Name requires access to Target.sym. As will be
discussed, a second function of the linker is to construct a
linkage table for <Target> (if one does not already exist as
a result of some previous reference to <Target>). After
Target.link has been constructed, to find Target.sym, the
segment number of <Target> is first used to access (in the
linkage address table) the virtual address of Target.link.
(Recall that the linkage address table is an array of
pointers to the linkage table of each object in a process
A pointer is found in Target.link to Target.sym.

It is proposed that, in an environment allowing multiple entry points into an object, each distinct entry name into an object be stored in the object’s symbolic name table. In addition, the entry point (viz., the offset into the object) and the offset (in object.link) of the incoming link associated with each entry point will also be stored in object.sym. Thus, by searching Target.sym with the argument "Entry_Name", the linker can compute the entry point and incoming link address necessary to snap a link to \(<\text{Target}\mid \text{Entry\_Name}\>.

The first step in the actual snapping of the link is to alter the outgoing link (in Caller.link) from a jump to the linker to a jump to the incoming link (in Target.link). The address jumped to is formed by combining the segment number of Target.link (which is found in the linkage address table) with the offset (as stored in Target.sym) of the incoming link.

The second step is the building of the incoming link. The incoming link consists of two instructions. The first loads the linkage pointer (lp) with the virtual address of Target.link ensuring that the linkage pointer always points to the currently executing procedure’s linkage table. This is necessary to allow a procedure’s translated
code (viz., object code segment) to reference an external object while remaining pure. A reference to an external object is achieved via the outgoing link; the virtual address of the outgoing link is computable at run time by adding a fixed (at translation time) offset to the linkage pointer and allowing the linkage pointer to vary during execution (see figure 4). Stated another way, it is the linkage pointer which allows (pure) translated code to jump to an entity (the outgoing link) which is not bound to a virtual address until run time.

The second instruction in the incoming link is a jump to the virtual address of <Target|Entry_Name> (of the form <segment_number|entry_point>). Note that the incoming link may already exist in its snapped form as a result of some previous reference to <Target|Entry_Name>. To identify this condition, the linker will first check a 'snapped link bit' which is set if the incoming link is snapped. A snapped link is shown in figure 5.

One may observe that the outgoing and incoming links could be merged into one link consisting of a load linkage pointer instruction followed by a jump to <Target|Entry_Name>. This change eliminates incoming links but effectively requires an 'incoming-type' link to be constructed in each outgoing link referencing an object. This approach was not chosen since it requires the
SOURCE CODE

PROCEDURE EXAMPLE:
DECLARE <Target> PROCEDURE EXTERNAL;
/* code */
BEGIN /* example */

CALL <Target|Entry_Name>;

END; /* of example */

OBJECT CODE

/* begin example */

CALL (Ip + offset of <Target|Entry_Name>'s outgoing link)

/* end example */

TRANSLATED EXTERNAL REFERENCE

FIGURE 4
LINKAGE TABLE ENTRIES FOR A SNAPPED PROCEDURE LINK

FIGURE 8

35
construction of an 'incoming link' for each reference to a procedure (vice just one incoming link) and additionally results in multiple load linkage pointer instructions. (Note, however, that in either of these link formats, subsequent references to <Target|Entry_Name> do not result in invocation of the linker.)

b. Data Links

For external data, the steps to snap a link are similar except the linker alters the outgoing link to an instruction which loads a pointer (preferably a register) with the virtual address of the external data, and a return instruction. (As will be shown, it is necessary for data segments to have both linkage tables and symbolic name tables. This permits the linker to use essentially one algorithm to dynamically link both data and procedures.) Thus any subsequent references to the external data (<Data|Entry_Name>) initiated by <Caller> would result in loading a pointer with the virtual address of <Data|Entry_Name> followed by a return to <Caller> and would not result in additional linker calls.

As with procedures, it is desirable to reference multiple, symbolically named locations (viz., entry points)

---

8 As has been previously discussed, it is necessary to use this form of outgoing link if the processor hardware cannot support an indirect addressing instruction to invoke the linker (on first reference) and subsequently access the virtual address of the external data.
in a data structure. This implies that <Data> must undergo a
translation to identify entry names and entry points and
furthermore, must have a symbolic name table in which this
information is stored. It is also necessary, given this
condition, that <Data> have a simplified linkage table
consisting of a linkage table header. (The contents of a
linkage table header will be presented later.)

c. Construction of a List of Snapped Links

For each object in a process address space, it
may be desirable for the linker to construct a linked list
which contains a pointer to each snapped outgoing link
referencing that object. This linked list is basically used
to provide a record of references to an object to permit
unlinking an object from an address space. (Unlinking will
be discussed in more detail later.) A pointer to the start
of this linked list would be stored in the header of the
object's linkage table and new entries to the linked list
would be entered at the head of the list (when snapping an
outgoing link). The linked list could easily be implemented
by storing a list pointer in each snapped outgoing link.

2. Building Linkage Tables

Before <Target> can commence execution, it must have
a linkage table in which snapped links can be stored. This
allows <Target> to engage in dynamic linking (if it is a
procedure). There exist two circumstances under which the
linkage table must be built. The first, and obvious, situation is when an external object is dynamically linked to a process. The second is when a program is initially started executing (viz., during process initialization). However the steps involved in these two cases do not differ, allowing the same module of the linker to be utilized in both instances.

To build a linkage table, the linker will access a template for an external object (or program) that was constructed during translation. The template is an exact duplicate of object.link with the exception of the symbolic name table virtual address. The linker must therefore only add the segment number of <Target> to the symbolic name table offset as found in the template to obtain a complete virtual address (for Target.sym). (This approach assumes Target.sym is a part of the translated code of <Target>.) The remainder of the template is then appended to the combined linkage table. An example of an initialized linkage table (and thus a template) is given in Figure C.

There are two problems related to the implementation of this linker function which require discussion. The first

9 It is not necessary for an implementation to include the combined linkage table since individual linkage tables can be assigned unique segment numbers. In fact, in a multidomain environment [6], it is desired to assign linkage tables to separate segments since this permits the dynamic linker to be domain independent (in accordance with the design criteria of Table 1).
### Initialized Linkage Table

**Figure 6**

<table>
<thead>
<tr>
<th>Linkage Table Size</th>
<th>Symbolic Name Table</th>
<th>Virtual Address</th>
<th>Linked List Pointer</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Table Body</th>
<th>Allocated Memory for an Incoming Link</th>
<th>Incoming Link #1</th>
<th>Outgoing Link #2</th>
<th>Outgoing Link #3</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>PUSH Sym. Name Tbl Offset</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>JUMP LINKER</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>PUSH Sym. Name Tbl Offset</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>JUMP LINKER</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Remaining Entries of Body</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
question involves where the template is located in a process address space. One does not, in general, want the template to be a part of the object code since this will result in an entity (the template) which is used only once becoming an extraneous part of a process. (Note that system limitations may force this shortcoming on an implementation.) A solution in a non-segmented system is to make the template a separate file. (One may not wish to do this in a segmented system if the number of segments represents a limited asset and a file corresponds to a segment since this would require assigning the template its own segment number.) However, in a demand paging environment, the template can be a part of the object code since it will only reside in memory when required and will then be 'paged' out. Because it will never again be referenced, the template will never again be loaded into memory.
This leads to the second problem of ensuring the linker can find the template when it is a part of the object code. There are several solutions to this, the most simple of which is to place a pointer to the template at some known location in the object code. Another solution would entail making the template a separate file. Thus when building a linkage table, the template is brought into a process address space, copied into the combined linkage table, and then deleted from the process address space.

3. Unsnapping Objects

It may be necessary to remove an object from the address space of a program. This situation may occur, for example, when using the Z8000 processor [12] with one memory management unit (MMU). Since this hardware configuration allows a maximum of 64 segments (some of which will be allocated to the operating system), it is entirely possible that a process may require in excess of the maximum number of available segments. It is desirable then to be able to remove an object from the process address space and unsnap all outgoing links referring to that object.

The unsnapping of an outgoing link is a simple procedure. The snapped outgoing link is merely replaced by an entry equivalent to the original, unsnapped outgoing link. More specifically, this unsnapped link consist of code to pass the linker the offset of the external object's
symbolic name in object.sym followed by the invocation of
the linker. (For simplicity, it will be assumed that the
linker is invoked via a jump instruction.) This implies that
a portion of each snapped link must be set aside to store
the offset of the symbolic name for use during
unlinking 10.

The first step in the unlinking process occurs when
the address space manager, after being requested by the
linker to add an object to a process address space, returns
a message to the linker indicating no segment numbers are
available (if this is the case). The linker would then cause
a segment to be deallocated.

If desired, the object's linkage table
(object.link), can be deleted from the combined linkage
table by performing a compaction on the combined linkage
table. (Note that compaction is not necessary since, aside
from resulting in unused memory in the combined linkage
table, if the deleted segment is reentered in the process
address space, a new linkage table will be built and
appended to the combined linkage table.) If a compaction is

10 Note that all information necessary to reset the link
(thus deleting the requirement to store the offset in the
linkage table) is available in the combined linkage table,
the subroutine offset table and the template. However, the
steps necessary to extract this data are rather involved and
the alternative of saving the offset within a snapped link
is suggested unless infrequent unlinking evolutions are
expected.
done, the deleted linkage table contains threads in the linked list of other segments, which must be removed without destroying the linked list they were a part of. One solution to this problem is to implement a doubly linked or circular linked list (by having the last entry of the list point to the linkage address table instead of being set to nil). Now, prior to removing object.link, the linker could find and adjust each thread (of a linked list) with a node in object.link ensuring the integrity of other segments' linked list.

Compaction presents two other problems. First, when object.link is removed, other subroutines' linkage tables may be relocated within the combined linkage table thus receiving new virtual addresses. This requires that the linkage address table values for those linkage tables along with linked list threads pointing into them to be adjusted accordingly. The correction must be done prior to actually compacting (because linked list threads in the deleted linkage table will be lost during compaction) and requires that addresses in the combined linkage table (i.e., subroutine offset table, linked list, and snapped link addresses) be corrected by the size of the removed linkage table. A second problem relates to snapped outgoing links which jump to incoming links in relocated linkage. These must also be adjusted by the size of object.link. Note that
a subroutine's linked list identifies each outgoing link that jumps into its linkage table. Therefore, every procedure segment whose linkage address table value requires correcting must have each entry in its linked list updated.

When unsnapping, the linked list (constructed by the linker) is traversed and each entry in the list is reinitialized. Note that unlinking affects many subroutine linkage tables yet the linkage pointer still points to object.link for the subroutine which originally invoked the linker. This implies that linked list pointers must either be complete virtual addresses or relative to the start of the linked list (i.e., they cannot be relative to the linkage pointer.)

An alternative to a linked list implementation is to have the linker search the combined linkage table for all snapped outgoing links referencing the deleted segment and reset each one found. (This is a less general solution since it requires the linker to know the format of all possible linkage table entries in order to identify those which must be reset.) Once all linkage table entries have been reinitialized, the object's linkage address table entry is set to nil, and the object and its linkage table (if desired) are removed from the process address space.
B. OPERATING SYSTEM SUPPORT

1. The Address Space Manager

As has been noted, before a link to an object can be snapped, the object must first be entered in the address space of a process. A request to enter an object (i.e., make it known) is forwarded from the linker to the address space manager. The address space manager will be passed the symbolic name of the object that is to be made accessible and will first search each entry in the process reference table to determine if an entry already exists for the object. If so, it will return to the linker the segment number of the object.

If the object is not accessible, the address space manager must first call on File Management to locate the object. After the object is located, Memory Management is invoked to assign a segment number to the object. If Memory Management were to indicate that it had no segment numbers left to assign, the address space manager would return to the linker a message to this effect.

It is realized that this represents a very vague description of how an object is located and assigned a segment number. However, since the exact steps involved are highly dependent on the operating environment and are fundamental to most multiprogramming systems, it is felt that adequate information exist elsewhere to allow implementation of these functions without discussing them in this thesis. Note that the file system in use may be extremely sophisticated as in Multics [11], or represent a simple one-to-one mapping of symbolic names to corresponding files.
2. The Process Reference Table

The process reference table contains an entry for each object in the address space of a process. The format for an entry (figure 7) includes the symbolic name of the object alone with the segment number of the object. A third item which may be found in the process reference table is a removal status reflecting the priority of an object for removal when unlinking.

Note that unlike a symbolic name table entry, the symbolic name found in the process reference table does not include entry names. For example, a process may contain external references to <Target|Entry_Name_1> and <Target|Entry_Name_2>, but the process reference table would only contain one entry for <Target>.

| symbolic : segment : removal name : number : priority |

Figure 7 - Process Reference Table Entry

3. Object Deletion from a Process Address Space

In conjunction with the linker, a module of the operating system must exist to delete an object from the address space of a process. When invoked by the linker, this module would use some policy, such as least recently used or
first-in, first-out, to select an object for removal. The module would notify Memory Management that the object's segment number is no longer in use and reset the object's entry in the process reference table to nil. The module would then inform the linker of the segment number of the deleted object. The linker can now unsnippet links to the object.

It is useful to point out policy considerations for selecting an object for removal. To begin, note that each time a link is snapped to an object, the address space manager is called to look up the segment number of the referenced object. It may, therefore, be advantageous to keep track of the number of links to an object to avoid removal of a segment which is referenced many times. One should not, however, strictly delete the object referenced the least number of times since this may well be the last object entered in the address space and, applying the principal of locality, be subject to further use in the near future.

Another important item to be considered before selecting a subroutine for removal is whether it will eventually be returned to by the currently executing procedure (i.e., it has a current activation record). As an example, say procedure A called procedure B which called procedure C. But before C could be linked an unlinking
evolution was required. Certainly one would not want to remove A or B to make virtual memory available for C since these two procedures would be returned to when C completed execution and the linking process has only been defined during a procedure call. Thus, if A or B were unlinked, C would return to a non-existent module which it could not link to or access (since A or B would no longer be in the process address space.)

If the information necessary to determine whether a procedure has a current activation record is not readily available, there is an easily implementable mechanism for determining this. A counter can be assigned to each procedure (in a process address space) that would be incremented or decremented as the procedure is invoked or completes execution. Thus, a procedure whose counter is zero has no current activation records and is available for removal. The counter could be updated by code in the snapped link and could be located in a procedure's linkage table or linkage address table entry. This implies that the linker must be involved in the selection of an object for removal.

4. Process Initialization

Process initialization involves those functions which must be carried out by the operating system prior to commencement of program execution. A brief review of these functions is offered at this time with a more detailed
discussion available in work by Janson [7, 8].

Before a process can commence execution of a program, the program's linkage table (program.link) and linkage address table must be allocated a section of the process address space and both tables must be initialized (or built from a template in the case of program.link). Additionally, the linkage pointer must be set to point to program.link. The operating system must initialize the process reference table with the applicable data for the program to be executed. Once this is accomplished, calls by \langle\text{program}\rangle can be dynamically linked.

C. TRANSLATOR SUPPORT

The process of dynamic linking is only practical if the translator, whether a compiler or assembler, has been designed to support dynamic linking. In a (translator) supported system, the translator must be able to identify external references, build the symbolic name table and linkage table template, and identify entry points and entry names. A translator will be assumed to produce relocatable object code allowing dynamic relocation of object code segments—either by relocation hardware or software.

Together, the translator and the linker must meet two requirements. First, the object code must remain pure during the linking process to allow use of shared procedure segments in a multiprogramming environment (i.e., the pure
object code criterion of Table 1. In addition, the code produced by the translator alone, with the steps followed in the linking process, must not limit features of the source language (i.e., the "syntactic compatibility" criterion).

1. **External References**

A translator must be able to identify external references and convert them into object code which will result in a call to the outgoing link (Figure 4). The call produced by the translator is to an address which can be expressed as the value of the linkage pointer plus some offset. Since the translator constructs the linkage table template, it knows the relative offset for a symbolic name's outgoing link in the linkage table. As has been noted, because the linkage pointer identifies the beginning of the executing procedure's linkage table, the object code for an external reference can be designed to call the outgoing link desired. (The use of the linkage pointer ensures the purity of a procedure's translated code.)

2. **Symbolic Name Tables and Templates**

The translator builds both the symbolic name table and the linkage table template. This should not present any major problem for the translator since all information required to construct these two items is, in general, either easily computable or found in the translator's symbol table. Because the translator builds both, it is not necessary for
entries in either to be of uniform size. The translator, for example, knows the offset (i.e., starting location) of a symbolic name table entry. Therefore, when the translator constructs the linkage table template, each outgoing link can be initialized to pass this offset to the linker (on first reference of an object.)

Notice that a one-to-one correspondence exist between entries in the linkage table body and the symbolic name table. Thus, if the symbolic name table is constructed first, the construction of the template becomes trivial. After the header of the template is built, the symbolic name table is scanned and an outgoing or incoming link is initialized within the template (depending on the type of symbolic name encountered). After each template entry is constructed, the offset of the link from the start of the template can be stored into its respective entry in object.sym.

3. Entry_Names and Entry_Points

The translator should be able to recognize both entry names and their associated entry points and make appropriate linkage table and symbolic name table entries accordingly. The inclusion of entry points in the implementation of a dynamic linker is highly desirable, particularly in a system with a limited virtual memory size. In this environment the number of unlinking evolutions may
be significantly reduced by using entry points to combine small data or procedure objects into larger ones without losing the smaller object's addressability.\footnote{This process is known as binding in Multics [11].}
L. DYNAMIC LINKING TABLES

The following is a discussion of the various tables associated with a dynamic linker. The formats presented do not represent the only structures possible; however, they contain all information necessary for dynamic linking.

1. The Symbolic Name Table

An entry in the symbolic name table (Figure 5) in addition to the symbolic name includes two other items. The first is a descriptor consisting of a type bit to identify the object as procedure or data; an identity bit to classify the symbolic name as an external reference versus entry name; and a size field to pass to the linker the number of characters in the symbolic name.

```
| descriptor | symbolic name | linkage table offset | relative offset |
---------------------------------------------------------------
```

(entry points only)

Descriptor:
```
| data or procedure | entry name | length of the symbolic name |
---------------------------------------------------------------
```

Figure 5 - Symbolic Name Table Entry.
A second item to be included is the offset of a symbolic name's entry in the subroutine's linkare table. For external references, the inclusion of the offset in a symbolic name table entry is not necessary; however, its inclusion does remove the requirement for the linker to save this information when it (the linker) is invoked by an outgoing link. However, for an access 'entry point' into an object, the offset (of the incoming link) must be included in the symbolic name table to ensure the linker knows where, within object.link, to construct the incoming link. The third item found in the symbolic name table is the entry point (offset) associated with each entry name declared within an object. (The entry point is used to construct a virtual address of the form <segment_number>entry_point>. This virtual address is used in the incoming link to invoke the called external procedure.)

It may be desirable to separate the symbolic name table into two sections consisting of external references and entry points. Assuming the entry points follow the external references, a pointer to the beginning of the entry points should be stored at the beginning of the table to allow the dynamic linker to jump directly to the entry point section when required. This feature would permit faster access for both since each would be stored in a smaller data structure. If this table organization is used it would not
be necessary to include an identity bit in the descriptor of an entry. (Note that the symbolic name table is searched for entry points only, since external references are accesses directly via the out\textsuperscript{of} link.)

It is natural to ask where the symbolic name table of an object is located within a process. It is suggested that for procedures, the symbolic name table be appended to the end of a procedure's object code. This will require only one copy of the symbolic name table (which represents a pure data structure) in a shared, multiprogramming environment. However, for external data, the symbolic name table cannot be located at the end of the data since it will limit the ability of the data structure to grow dynamically. A better solution would be to merge the data.sym with data.link and store the two in the combined linkage table. This format allows the data to be based at offset zero and grow dynamically. (The general form of a data symbolic name/linkage table is given in figure 9.)

2. The Linkage Table
   a. The Initialized Linkage Table

The initialized linkage table is shown in figure 6. The header of the linkage table contains three items. The first is the size of the linkage table. This item tells the linker the size of the template when building an object's linkage table and also is used by the linker to adjust
**DATA SEGMENT SYMBOLIC NAME TABLE AND LINKAGE TABLE**

**FIGURE 2**

<table>
<thead>
<tr>
<th>Linkage Table</th>
<th>linkage table/symbolic name table size</th>
<th>linked list pointer</th>
</tr>
</thead>
<tbody>
<tr>
<td>Symbolic Name Table</td>
<td>descriptor</td>
<td>Entry Name</td>
</tr>
<tr>
<td></td>
<td>descriptor</td>
<td>Entry Name</td>
</tr>
</tbody>
</table>

remainder of symbolic name table
linkage table addresses when removing a linkage table (during unlinking). (Recall that linked list threads, linkage address table entries, and jumps within the linkage table must be adjusted by the size of a removed linkage table during compaction of the combined linkage table.) The second and third items found in the header consist of the virtual address of the symbolic name table and a pointer to the head (i.e., a snapped outgoing link to the object) of the linked list used in unlinking.

Each outgoing link in the body of the linkage table template is initialized to two instructions. The first instruction passes the entry’s offset in the symbolic name table to the linker (as an argument). The second is an instruction which results in the invocation of the linker. Logically, the two instructions found in the initialized outgoing link equate to:

```
CALL Linker (symbolic_name_table_offset)
```

The designer can choose from three basic mechanisms that may be used to invoke the linker. First, if the translator knows the virtual address of the linker (such as a fixed or reserved segment number), then the outgoing links in a template can be tailored to invoke the linker directly (e.g., JUMP virtual address of <linker>). The second method is to invoke the linker by a hardware fault which will result in the linker being called as the fault
handler. The translator would therefore, initialize each outgoing link to push the offset of the symbolic name on the machine stack and then induce a hardware fault. The third mechanism is for the linker to enter its own virtual address in each outgoing link as it builds a procedure's linkage table. (This represents the least desirable technique since it requires the linker to know the format of the body of a template and furthermore is much slower since the template must be scanned as the linkage table is built.)

b. Format of Snapped Links

A format for snapped outgoing links to external data and procedures are shown in figure 10. The snapped outgoing link for a procedure consist of a jump to the incoming link in the called procedure's link for an external procedure's linkage table. The snapped incoming link loads the linkage pointer with the virtual address of the called procedure's linkage table (viz., Target.link), and then jumps to the called procedure (as defined by some entry point). For external data, the snapped outgoing link consist of an instruction which loads a register with the virtual address of the data followed by a return instruction. (Recall that this technique is used when the available hardware does not support an indirect addressing approach.)

The two items common to both entries (as shown in figure 10), 'offset' and 'linked list pointer', represent
Data | LOAD ptr, address of data
Entry | RETURN
      | offset | linked list pointer

Procedure | JUMP to virtual address
Entry | offset | linked list pointer
      | offset | linked list pointer

DATA AND PROCEDURE SNAPPED OUTGOING LINKS

FIGURE 16
information to be used during unlinking. The offset (of the symbolic name table entry corresponding to the outgoing link) is used when resetting the entry to its initialized form while the linked list pointer allows the un linker to find each entry in the combined linkage table which references the object being removed.

3. The Linkage Address Table

To facilitate each access to a subroutine's linkage table within the combined linkage table, the linkage address table is used. Entries are subscripted by segment number and contain the offset of an object's linkage table within the combined linkage table. (Note that if linkage tables were allocated individual segments, vice a portion of the combined linkage table, the linkage address table would contain the virtual address of an object's linkage table.)

The problem arises as to where in a process address space the linkage address table should be located. One would like to avoid allocating the linkage address table its own segment and pointer register since these resources within a microprocessor are usually limited. Assuming the linkage address table is initialized at process creation and is a fixed length, a possible solution is to place it at the head of the combined linkage table. If this approach is used, the table's base address would be the segment number of the linkage table (which is stored in the linkage pointer) with
an offset of zero.

F. IMPLEMENTATION OF ENTRY_NAMES AND ENTRY_POINTS

To avoid confusion, some of the fine points related to the implementation of entry names and entry points will be discussed at this time.

First, if an object has multiple entry points declared within it, each entry point must have a unique entry in object.sym and a unique incoming link in object.link. This is logical since each entry point defines a distinct location in an object. Secondly, if a procedure contains external references to several entry points within the same object, each unique reference must have its own entry within the procedure’s symbolic name table and its own outgoing link. (For example, <Target|Entry_Name_1> and <Target|Entry_Name_2> represent distinct references.)

Notice that the start of an object represents an (frequently implied) entry point which must be included in the object’s symbolic name table and have an incoming link. However, one would like not to explicitly include such an ‘entry point’ (e.g. <Target|Target>) in an external reference. Therefore, it is suggested that an implementation default to this implied entry point in the absence of an entry name.
V. LINKING WITHOUT TRANSLATOR SUPPORT

In all probability, the initial implementation of a dynamic linker will not enjoy the translator support which has previously been assured to exist. Yet, within reasonable limitations, one would like to be able to utilize the features of dynamic linking in an unsupported environment. Furthermore, it is desirable to be able to use one dynamic linker for both supported and unsupported procedures, to be able to execute both supported and unsupported modules within a process, and to be able to call an external procedure from a supported module without having to specifically declare the procedure to be supported or unsupported. (This implies the linker must be able to differentiate supported procedures from unsupported ones.) An implementation is proposed which achieves these goals.

A. THE INTERFACE MODULES

In an unsupported subroutine, the linker should be invoked via a data or procedure interface module. Two separate modules are suggested since, besides the fact that their functions differ, the data interface module must return the virtual address of the external data to the point

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13 For the purposes of this thesis, 'supported' will be used when referring to an environment in which the translator supports dynamic linking while 'unsupported' will be used to reference environments which lack this feature.
of call while the procedure interface module merely executes the snapped link. Conceptually, an interface module carries out those functions which, in a supported system, require some translator support. These functions include building the symbolic name table and the linkage table, invoking the linker, and executing a snapped link.

1. Linking of Procedures

To best describe the functions of the procedure interface module, the steps to dynamically link the unsupported procedure \(<\text{Target}>\) to the unsupported procedure \(<\text{Caller}>\) will be traced (figure 11). It will be assured that the procedure interface module is called as follows:

\[
\text{CALL LINK$PROC(Target, parameter_1, parameter_2, \ldots)}
\]

The first function that \(<\text{LINK$PROC}>\) would perform would be to save the value of the interface linkage pointer on a software stack. Because a translator which does not support dynamic linking would not know that the linkage pointer register is not available for general use, in all probability object code produced would utilize the linkage pointer register requiring an interface linkage pointer be established and saved in software. (In a supported system

---

14 This requires that two interface modules (one for procedures and one for data) be implemented due to the fact that most higher level languages have a different syntax for procedures which return arguments to the point of call (versus those which do not).
SEQUENCE OF EVENTS FOR LINKING UNSUPPORTED OBJECTS

FIGURE 11
saving the linkage pointer register is accomplished by the translated code.) This implies that there are two linkage pointers (viz., a hardware linkage pointer and an interface linkage pointer), and both must be initialized to point to the beginning of the linkage table for <program> at process initialization. It should be noted that the last instruction of <LINK$PROC> must reset the interface linkage pointer by popping the saved value off a software stack prior to returning to <Caller>.

<Link$PROC> will then check to see if an entry for <Target> exist in Caller.sym. If not, <LINK$PROC> will enter <Target> in Caller.sym along with the offset of the outgoing link for <Target> in Caller.link. <LINK$PROC> is able to enter this offset because it has constructed an outgoing link for <Target> in the next free location in Caller.link. (The outgoing link for <Target> is of the same format found in a supported system linkage table.) This outgoing link is executed and the linker is invoked. (These steps ensure the use of the same linker for both supported and unsupported linking since the method of linker invocation does not change.)

It should be pointed out that had an entry for <Target> already existed, then <Target> would have already been linked to <Caller> and <LINK$PROC> would only have to execute the snapped link (which it can find since the offset
Once the linker is called, it will first determine if <Target> is a supported or unsupported procedure. The actual mechanism used to perform this check will vary depending on the operating system. One means of performing this check is to tag modules within the file system. An alternative would be to tailor the first byte of a supported module to identify it as such. (One must ensure when using this method that an unsupported module cannot have the same bit pattern for its first byte.) In this thesis it will be assumed that the linker can query the file system to determine whether a module (external object) is supported or not. The ability of the linker to accomplish this check allows an external reference within a supported subroutine to have the same format regardless of whether the external object referenced is supported or not. This prevents having to modify and retranslate modules when an unsupported object is retranslated in a supported environment.

When the linker determines that <Target> is unsupported, it will call on a routine in <LINKSPROC> to allocate a section of the combined linkage table to be used as Target.sym and Target.link. This implies that the next free location in the combined linkage table must be available to <LINKSPROC> in addition to the linker.
constructing linkage tables since <LINK PROC> must build the linkage table for an unsupported subroutine. Target.sym can be located within Target.link (vice Target's object code) since the linker finds Target.sym via a pointer in Target.link (figure 12). Additionally, <LINK PROC> will construct Target's linkage address table entry and will initialize the header of Target.link.

The linker needs to know whether Target is supported or not for one other important reason. Execution of Target is initiated via a jump from Caller to an incoming link in Target.link. The incoming link normally consist of an instruction to set the linkage pointer register to point to Target.link followed by a jump to Target. However, if Target is unsupported, it is the interface linkage pointer vice the linkage pointer register which must be set requiring the linker be able to distinguish between supported and unsupported external procedures and snap incoming links accordingly. Thus the unsupported incoming link will be of the form:

Interface Linkage pointer = Base address of Target.link
Jump to Target

Note that <LINK PROC> is passed not only the symbolic name "Target", but also all of Target's parameters. This implies that <LINK PROC> must be able to pass these parameters to Target in accordance with the
This image contains a table with notes and comments. Here is the plain text representation of the table:

<table>
<thead>
<tr>
<th>Column 1</th>
<th>Column 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Note 1</td>
<td>Note 2</td>
</tr>
<tr>
<td>Note 3</td>
<td>Note 4</td>
</tr>
<tr>
<td>Note 5</td>
<td>Note 6</td>
</tr>
<tr>
<td>Note 7</td>
<td>Note 8</td>
</tr>
</tbody>
</table>

Notes:
- The table is used for linking purposes.
- Each column contains specific details related to the linking process.
- The comments are placed alongside the table entries for clarification.

**Figure 12**

*Unsupported linkage table*
conventions of the translator which compiled <Target>.

2. Linking of Data

The sequence of events to link (the unsupported object) <Data> to <Caller> would be quite similar to those linking <Target>. Assuming <Data> had not yet been referenced by the executing process, the interface module <LINK$DATA> would build an outgoing link for <Data> in Caller.link and enter the symbolic name "Data" in Caller.sym (as <LINK$PROC> does for <Target>).

The linker would then be invoked and, upon determining <Data> to be unsupported, would call <LINK$DATA>. <LINK$DATA> would construct a link table for <Data>; however, the construction of Data.link would be trivial since it consists of only a linked list pointer and a link size entry (figure 8). As will be discussed, unsupported objects cannot have multiple entry points; therefore, <Data> does not require a symbolic name table.

Following the construction of Data.link, the linker would snap the link between <Data> and <Caller>. The snapped link in this situation would differ somewhat in that once snapped, the link will be used by <LINK$DATA> to obtain the virtual address of <Data>. <LINK$DATA> completes the reference to <Data> by returning <Data>'s virtual address to (the point of call) in <Caller>.

69
5. LIMITATIONS OF UNSUPPORTED LINKING

There are four major disadvantages when linking in an unsupported environment. Three of these represent violations of design criteria (as specified in Table 1) while the fourth, the inability to implement multiple entry points, is considered a limitation in the flexibility associated with a dynamic linking environment.

The first disadvantage is that unsupported linking results in excessive overhead for subsequent references to an external object, as required by the limited overload criterion. This is a direct result of the fact that the interface module must be invoked for each external reference to perform those bookkeeping functions (such as manipulating the interface linkage pointer) which in a supported environment are performed by the translated external reference and the snapped link.

A second disadvantage is that an external procedure must be linked before it can be passed to a subroutine as a parameter. This contradicts the delayed binding criterion. Furthermore, to pass an external procedure as a parameter requires a third interface module. A third interface module is called for since <LINK$PROC> can only link and invoke external procedures whereas to pass a procedure as a parameter, it is necessary to have access to the procedure's virtual address. (In the case of an external procedure, it
is sufficient to pass the virtual address of the procedure's outgoing link vice the virtual address of the external procedure itself.) Therefore, the third interface module will snap the link (in violation of the delayed binding criterion) and return the virtual address of the external procedure's outgoing link to the point of call.

The third disadvantage involves a violation of the syntactic compatibility criterion for external data. Note that the utilization of external data is limited to a (PL/I or PL/M) based variable structure since $LINK$ can only return the virtual address of the external data to the point of call.

The final disadvantage is that multiple entry points cannot be implemented in an unsupported object. Since the (translator constructed) symbolic name table is necessary to retain the entry name and entry point associated with an access into an object, an unsupported object can only be referenced at its conventional starting location.
VI. HARDWARE TO ENHANCE DYNAMIC LINKING

Even though care has been taken to develop a dynamic linkers which is not dependent on the availability of certain hardware features, there are hardware capabilities which are desirable in a dynamic linking environment. In general, these features can be divided into two general categories: those which effect the design of the linker; and those which impact on system performance.

It is emphasized that the following discussion is presented with the idea that, if one is going to include dynamic linking in a system and has a choice of processors, one should look for certain hardware features which are desirable in a dynamic linking environment. This section should not be viewed as a list of hardware support necessary for the feasible implementation of a dynamic linker.

A. HARDWARE FEATURES AFFECTING LINKER DESIGN

All the hardware features discussed in this section dictate in some manner how certain functions of a dynamic linker must be implemented. However, the first two features discussed (viz., indirect addressing and a hardware fault on indirection) are necessary to allow a linker to fully meet the design criteria of Table 1.
1. Hardware Indirection and Faults on Indirection

For the most part, it has been assumed the linker was invoked (on the first reference of an object) via the initialized code of the outgoing link. However, the most desirable method of linker invocation requires the processor to provide two hardware features: (1) The ability to reference data and call procedures using indirect addressing through memory; and (2) the ability to generate a hardware fault during indirection.

When a hardware fault on indirection is available, references to external objects are achieved via indirect addressing instructions where the final 'target address' (in the indirection sequence) is stored in the outgoing link of the executing procedure's linkage table. The outgoing link is initialized to cause a fault (on indirection) which results in the invocation of the linker as the fault handler. The linker snaps the outgoing link by altering the initialized fault-inducing code to either the virtual address of the incoming link (for external procedure calls) or the virtual address of the external data. (This represents the method used in Multics [11].)

Without a fault on indirection, it is not apparent how to pass external data as a parameter without first snapping the link to the data. This represents a violation of the delayed binding criterion (of Table 1) because the
binding of a symbolic name to a virtual address has been performed prior to first reference. (Note that even though the external data is passed as a parameter, it may not necessarily be referenced within the procedure.\textsuperscript{15})

2. Other Features Influencing Linker Implementation

There are certain hardware features which do not restrict the implementation of a dynamic linker, but do affect certain aspects of the linker design. Two hardware features which are considered advantageous in a dynamic linking environment will be discussed.

The first feature relates to the number of segments available in a process address space. More specifically, if there are adequate segments (and each segment is of reasonable size), then it may not be necessary to frequently execute the unlinking portion of a dynamic linker. (Note that unlinking is still necessary because segments deleted from an address space should be unlinked.) This is considered advantageous since unlinking is considered one of the more expensive functions to execute. Note that if unlinking is not implemented, segments can always be conserved by combining smaller objects into a single segment and referencing each object via an entry point.

\textsuperscript{15} One is free to judge how much of a limitation the absence of these two hardware features presents. However, the author does not consider it very prohibitive.
The object code produced by the translator is subject to the hardware features available. In a dynamic linking environment, some hardware features tend to simplify the object code produced for an external reference. For example, if hardware registers are automatically saved by the procedure CALL and RETURN conventions, then it is not necessary for the object code (during an external procedure call) to explicitly save and reset the linkage pointer. Possess an indirect addressing CALL instruction but can only perform

B. HARDWARE FEATURES AFFECTING SYSTEM PERFORMANCE

There exist hardware capabilities which enhance system performance in a dynamic linking environment. These capabilities do not directly affect the design of the linker; but, because of the requirements dynamic linking places on the operating system (such as dynamic relocatability of code), the inclusion of certain hardware features serves to improve overall system performance.

In a dynamic linking environment, subroutines are not bound to virtual addresses (in a process address space) until run time. Therefore, they must reside on secondary storage in a relocatable form and be dynamically relocated during process execution. Thus, the more efficiently code can be relocated, the better system performance (viz., execution speed) will be. This implies that hardware
relocatability of code is desirable.

A second hardware capability which enhances system performance is hardware segmentation. Even though the linker design is not dependent on the support of segmentation hardware, many of the attributes associated with procedure and data objects (which are logical entities, or segments), are in fact intrinsic to segmentation. These attributes include object (unique) identifiers (viz., segment numbers) and object virtual addresses (viz., an object segment number + offset). It is therefore reasonable to conclude that segmentation hardware is desirable (but not essential) in a dynamic linking environment.
VII. A DEMONSTRATION OF DYNAMIC LINKING

In order to support the design concepts of this thesis, and, in a sense, prove the feasibility of microcomputer dynamic linking, a subset of the dynamic linker design (not including unlinking) was implemented on an Intel 8086 based system. The 8280 microprocessor [12] was selected because of its lack of hardware support, a fact which supported the contention that the linker design is hardware independent.

The implementation consisted of five modules: (1) a process initialization module, (2) the dynamic linker module, (3) the address space manager, (4) a display linkage table routine, and (5) a package of system library routines. Three of these modules (process initialization, the dynamic linker, and the address space manager) will be discussed in detail. The display linkage table routine was included in the implementation strictly to add clarity to the demonstration and will not be discussed in detail. (Source listings for the display linkage table routine and the system library routines are provided in appendix (E) for the interested reader.)

The implementation of the dynamic linker ran on the CP/M operating system [21]. The hardware support included two eight inch floppy disk drives and 55K of main memory. Modules were written in FL/M-82 [22] and compiled under the
Isis-II operating system [13].

(It should be noted at this time that because no translator which supported dynamic linking was available, test programs were hand compiled to produce the necessary object code, symbolic name tables, and linkage table templates.)

A. THE MODULES OF THE DYNAMIC LINKER

The three major modules of the linker were the process initialization module, the (dynamic) linker module, and the address space manager. Briefly, these modules perform the following functions:

Process Initialization is passed the argument 'program name' and performs the following:

1. Extracts the name of the program to be executed from the command line.

2. Causes the linker module and the address space manager to be initialized.

3. Causes the address space manager to (1) enter the program in the process address space and (2) load the program into memory.

4. Causes the linker module to build a linkage table for the program.

5. Builds the interrupt handler. The interrupt handler is invoked by initialized outgoing links and, in turn, invokes the linker module.

6. Starts the program in execution.
7. If the display toggle was set (in the command line), causes the process reference table and combined linkage table to be displayed following completion of program execution.

The linker module is invoked (by the fault handler) with the arguments 'linkage pointer' and 'symbolic name offset' (in the symbolic name table) and performs the following:

1. Extracts the character string name associated with the external reference from the calling procedure's symbolic name table.

2. Invokes the address space manager passing as an argument the symbolic name of the external object (to be linked).

3. Builds a linkage table for the external object (if necessary).

4. Extracts the data associated with the entry name field (of the external reference) from the external object's symbolic name table.

5. Snaps the outgoing and (if required) incoming links.

6. Causes the snapped outgoing link to be executed by returning the address of the outgoing link to the interrupt handler. The interrupt handler then jumps to the outgoing link.

The Address Space Manager consists of two submodules, ASMMakeAccessible is invoked with the argument 'symbolic name' (of an object) and performs the following:

1. Determines if the object is already in the process address space.
2. If not, loads the object into memory (performs a relocation if the object is executable code) and makes an entry for the object in the process reference table.

3. Returns to the point of call the unique identifier and base address (viz., 8-bit 'virtual address') of the object.

ASM-removeSez is invoked with the argument 'symbolic name' and performs the following: 1. Removes an object from a process address space by deleting the object's entry in the process reference table.

The implementation of each of these modules will now be reviewed in detail. The discussion will include implementation details dictated by the 8088 hardware and CP/M operating system support utilized.

1. **Process Initialization**
   The linker implementation was call 'Exec' and was invoked by the CP/M command line
   
   `A>Exec program_name <cr>`

   The first function of process initialization was to scan the command line to determine the name of the program (viz., program_name) to be executed. This was performed by the READCOMANDLINE subroutine which read the CP/M buffer to extract the program name. Additionally, if the last character of the command line was 's' (which is optional), the display toggle was set telling process initialization to display the process reference table and continue linking.
table following the completion of program execution.

Additionally, since a program is executable code, READCOMANDLINE assumes for the program a CP/M filetype of \texttt{COA}.\footnote{CP/M utilizes a filetype field to distinguish the various types of files on disk storage. The filetypes utilized by the linker implementation were (1) \texttt{COM} – a file of executable code; (2) \texttt{ETA} – an input file; (3) \texttt{LTA} – a linkage table template file; and (4) \texttt{RIB} – a file of relocation hints for a COM file.}

Process initialization then calls on the subroutines \texttt{INITIALIZEASM} and \texttt{INITIALIZELINKER} which initialize the address space manager and linker modules respectively. (These two subroutines are a part of their respective modules and will be discussed in detail with the parent module.)

Having initialized the address space manager and linker module, process initialization then enters the program in the process address space and builds it a linkage table. The program is entered in the address space by calling \texttt{ASMMAKEACCESSABLE} (passing \texttt{program_name} as an argument). \texttt{ASMMAKEACCESSABLE} returns to process initialization the unique identifier and base address assigned to the program. (It should be noted that because the 8080 does not provide hardware segmentation, it was necessary to utilize a unique identifier and base address in...
place of the object segment number.) Process initialization then calls on an entry point into the linker module (viz., the subroutine LINKAGES\$TABLES\$ROUTINES) which builds a linkage table for the program.

The next function of process initialization is to build the interrupt vector. It was decided to invoke the linker (when snapping a link) via a software fault. This technique allowed initialized out-going links to be independent of the linker address by saving the out-going link jump (via a software fault) to a predetermined location which then invoked the linker. (The software fault used was an \$EXCP FST 4 instruction which saves the current execution point on the stack and jumps to the interrupt vector at location \$C000.)

The interrupt vector first removes the return address placed on the stack by the FST 4 instruction. This address represents the address at the end of the out-going link: when the link is snapped, it is desired to jump to the beginning of the out-going link (to reference the external object). The next instruction of the interrupt vector calls the linker module passing to it the linkage pointer (the \$EXCP B and C register pair) and the offset (in the symbolic name table) of the entry for the object to be linked. (The symbolic name table offset is loaded in the L
and E register pair by the initialized outgoing link. When
the linker module has completed execution it returns the
address of the outgoing link to the interrupt vector (in the
hardware E and I register pair). The interrupt vector then
processes any arguments initially passed (by the caller) to the
external object into the D and E register pair and jumps to
the outgoing link. (The D and E register pair is used to
pass arguments or pointers to a list of arguments between
external objects.)

Finally, process initialization loads the initial
value of the linkage pointer into the E and C register pair
and invokes the program to be executed. These two functions
are performed by the subroutine EXECUTE.

2. The Linker Module

The linker module was initially written in a high
level pseudocode (Appendix A1) and then translated into
PL/M-86. This permitted an orderly approach to the
implementation of the dynamic linker module. The linker
module consisted of five major subroutines and a control
routine. The logical relation between linker subroutines is
given in figure 13.

As has been noted the linker module (i.e., the
control routine LINKER) was invoked by the interrupt vector.
LINKER first calls on ACCESSSYMBOLICNAME@DATA passing as
arguments the linkage pointer and the symbolic name offset. ACCESS$SYMBOLIC$NAMES$DATA utilizes the linkage pointer to address the linkage table of the calling procedure (which will be referred to as <Caller>) and extracts the address of Caller.sym. The entry of the external reference (viz., <Target|Entry#1>) is then computed by adding the symbolic name offset to the address of Caller.sym.

ACCESS$SYMBOLIC$NAMES$DATA can now extract (from the symbolic name table) the symbolic name "Target", the entry name "Entry#1", the offset of <Target|Entry#1>'s outgoing link (in Caller.link), and <Target>'s type (i.e., procedure or data). ACCESS$SYMBOLIC$NAMES$DATA will compute the address of <Target|Entry#1>'s outgoing link (by adding the outgoing link offset to the base of Caller.link). Next it will set the CP/M filetype for <Target> (viz., 'COM' for procedures and 'DTA' for data) in the symbolic name buffer. The symbolic name buffer stores the filename and filetype of the object being linked in a standardized format. The standardized format is of the form 'FILENAME.FILETYPE'. Thus if <Target> was a procedure, the symbolic name buffer would contain the entry 'TARGET.COM'.

LINKER can now call on the address space manager (ASM$MAP$ACCESSABLE) to learn the segment number (i.e., the unique identifier and base address) of <Target>. Once LINKIF
knows <Target>‘s segment number data, it will invoke the subroutine LINKAGE$TABLE$ROUTINES.

LINKAGE$TABLE$ROUTINES determines if a linkage table already exist for <Target> by checking the valid entry bit of the linkage address table entry for <Target>. (Recall that the unique identifier of an object is used as a subscript into the linkage address table to access the base address of the object’s linkage table.) If Target.link does not exist, LINKAGE$TABLE$ROUTINES will invoke BUILD$OBJECT$LINK to construct a linkage table for <Target> and will update <Target>‘s entry in the linkage address table. Otherwise, LINKAGE$TABLE$ROUTINES merely returns a pointer (the parameter NEW$LINK$PTR) to point to Target.link.

BUILD$OBJECT$LINK first causes the address space manager to enter <Target>‘s linkage table template in the process address space. It does this by appending to the program name (<Target>) the CP/M filetype of ‘TMP’. (For example, if <Target> were a procedure, the executable code would exist in the file TARGET.COM while <Target>‘s template is in the file TARGET.TMP.) Once the template is loaded into memory, BUILD$OBJECT$LINK first computes the address of Target.sym.
Recall that for a procedure, the symbolic name table is appended to the end of the object code. Thus, the address of the symbolic name table for procedures is computed by adding the offset of the symbolic name table (found in the template) to the base address of the object. For data, the symbolic name table is a part of the linkage table and its address is computed by adding the symbolic name table offset to the data object's linkage table base address.

BUILD$OBJECT$LINK then enters the (computed) symbolic name table address, the linkage table size, and the body of the linkage table in the combined linkage table as Target.link. (The combined linkage table was a statically allocated 1K block of memory.) BUILD$OBJECT$LINK then removes the template from the process address space by invoking ASC$REMOTE$SEG (an address space manager routine).

Now that Target.link exist, the linker module can find Target.sym (via a pointer in Target.link's header) and

17 The decision to build linkage tables in this manner was driven by an effort to simulate the mechanisms which would occur if hardware segmentation were available. To create Target.link in a segmented system, it would be necessary to take a copy of the (pure and sharable) template. However, in this implementation, since the disk copy of a template remains pure, the process copy (as introduced by the address space manager) could have just as easily served as the linkage table without recopying it into the combined linkage table. (Note that this approach would eliminate the need for a statically allocated combined linkage table.)
access the data associated with entry name. The routine
ACCESS$ENTRY$NAME$DATA does this by searching Target.sym
with the argument 'Entry_#1'. Recall that the symbolic name
table entry for an entry name includes the incoming link
offset and the entry point (of Entry_#1 into <Target>). Thus
by adding the incoming link offset to the base address of
Target.link, the incoming link address can be computed.
Additionally, the entry point (offset) plus the base address
of <Target> is the target address referenced by the symbolic
name "Target!Entry_#1".

All the information necessary to snap the link is
now available and LINKER calls on the subroutine
SNAP$TARGETLINKS to perform this function. The final
subroutine of the linker module is INITIALIZE$LINKER which
is invoked by process initialization. INITIALIZE$LINKER
initializes various pointers (used by the linker module) and
the valid entry bits of the linkage address table. It
returns to process initialization the address of LINKER (for
use in the interrupt vector), the address of the linkage
address table (which is passed as a parameter to the display
linkage table routine), and the base address of the combined
linkage table (which is used in EXECUTE to initialize the
linkage pointer).
3. The Address Space Manager Module

Because the CP/M operating system lacked any memory management executive, it was necessary for the address space manager to perform functions which would usually be provided by the operating system. Thus the address space manager had to be able to load objects into free memory and relocate executable code. These functions were carried out by the subroutines LOADOBJECT and RELOCATE respectively. The implementation of the two subroutines was extremely primitive providing only the minimum support necessary to allow the implementation of the remainder of the address space manager (and will not be discussed in any further detail).

Like the dynamic linker module, the address space manager was first written in pseudocode (appendix (A)) and translated into PL/M-80. It centers around the management of the process reference table which is implemented as an array of structures of the form:

```plaintext
Process_Reference_Table : ARRAY of STRUCTURES of
Valid_bit : BOOLEAN;
Name : ARRAY of CHARACTERS;
Base_address : ADDRESS;
END;
```

The valid_bit field was set to 'valid' if the entry
represented an object in the process address space. The name field contained the object name in standardized form (e.g., CALLED.COM) while the base_address is the location (in memory) where the object was loaded. Note also that an object's unique identifier represents an implied process reference table field and corresponded to the subscript of the object's entry in the process reference table.

When ASM$MAKE$ACCESSABLE is invoked, it is passed the object name (in standard form) as an argument. ASM$MAKE$ACCESSABLE first searches the process reference table to determine if <Target> already has an entry (implying <Target> is already in the process address space). If not, ICAD$OBJECT is invoked to load <Target> into memory returning the base address of <Target> to the point of call. ASM$MAKE$ACCESSABLE then enters <Target> in the process reference table in the first free entry. The final function of ASM$MAKE$ACCESSABLE is to return the base address and unique identifier (viz., the process reference table subscript) of <Target> to the point of call.

The subroutine ASM$REMOVE$SEG is passed an object name (in standard form) and deletes the object from the process address space by setting the object's valid_bit in the process reference table to 'invalid'.
Two other subroutines included in the address space manager were DISPLAY$PRT which displayed the process reference table (and is not necessary in a dynamic linker implementation) and INITIALIZE$ASM. INITIALIZE$ASM is invoked by process initialization (as is DISPLAY$PRT) and initializes the valid_bits of the process reference table to 'invalid'. Additionally it statically sets the size of the process reference table (which was arbitrarily set to 16 entries) and initializes a free memory pointer for the LOAD$OBJECT subroutine.
B. THE TEST PROGRAMS

Two test programs were run on the dynamic linker. The first, DEMO, computed and displayed (in hexadecimal form) the multiplication and addition tables (with appropriate headers for the numbers from 0 to 15. The second test program, SUM, added the elements of an external data array and displayed the result in hexadecimal form. DEMO demonstrated all the capabilities desired of dynamically linked objects. SUM was included to provide a simple example that will be explained in detail.

1. Test Program Construction

Before discussing either test program further, it is useful to explain the mechanics used in their construction. First, because a translator which supported dynamic linking was not available, it was necessary to hand assemble those portions of the test programs unique to dynamic linking. These included translated external references, symbolic name tables, and linkage table templates. All test program source listings and program test results are included in appendix (C).

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18 The test programs, including templates, symbolic name tables, and relocation bits (for executable code) were written in Z8000 assembly code and assembled using the Digital Research Z8000 Assembler [17].
a. The Assembled Symbolic Name Table

The symbolic name table of an object can be found (in the source listings) at the end of either the object code (for procedures) or in the linkage table template (for data). Each entry in the symbolic name table consists of four fields. For clarity, each field was preceded by a label. Entries were of the following form:

DESCn : DB byte_1 19
LINKn : DB low_byte, sign_byte
ENTRYn : DB low_byte, high_byte
NAMEn : DB 'OBJECT_NAME:ENTRY_NAME' or 'ENTRY_NAME'

DESCn represents the entry descriptor (of the nth symbolic name table entry). The most significant bit of byte_1 indicated the object type (viz., 0 for procedures and 1 for data). The five least significant bits of byte_1 contained the number of characters in the name field. The remaining two bits of DESCn were unused.

LINKn is the offset of the entry's outgoing or incoming link in the parent object's linkage table. 20 The ENTRYn field is an entry point offset in the parent object.

---

19 DB is an assembler pseudo-operator that tells the assembler that the rest of the line represents data. Data not surrounded by single quotes is translated as a numerical value while data in quotes is an ASCII character string.

20 In the 6802, two byte values are stored in memory with the low byte in the lower numbered memory location. Thus the number 1023H would appear as 2FH, 1FH when used in a DB field.
associated with some entry name. For an external reference, low_byte and high_byte of this field were arbitrarily set to zero.

The NAME field held the symbolic name associated with the entry. This field contained either an entry name (e.g., ENTRY_#1), or the name of an external reference (e.g., OBJECT_NAME:ENTRY_NAME). For the NAME field of an external reference, the "ENTRY_NAME" portion is optional. When left out, it implies that the entry name to be used is the same as the object name. For example, the procedure MULT has an entry point by the same name but appears as 'MULT' in DEMC's symbolic name table (vice 'MULT:MULT').

b. The Assembled Template

The linkage table template was constructed as assembled code. Templates were of the form:

```
SIZE : DB low_byte, high_byte
SNT : DB low_byte, high_byte
BODY : EB ox, ox, ox, ox, ox, ox (in-oring link)
PUSH D (cutting link)
LXI D, symbolic_name_table_offset
PST 4
```

The SIZE field contains the number of bytes in the template. SNT represents the offset (i.e., number of bytes) of the symbolic name table from the beginning of either a procedure segment or a data segment's template.
The BODY of a template contains two types of entries. For an incoming link, the template merely reserves six bytes (initialized to 0) in the combined linkage table in which the snapped incoming link will eventually be placed. An outgoing link consists of three assembly code instructions. The first instruction (PUSH D) saves the argument register (viz., the D and E register pair) prior to loading that register with the symbolic name table offset of the external object to be linked. The third outgoing link instruction (RST 4) causes a software fault resulting in the invocation of the linker via the interrupt vector.

c. Other Problems in Test Program Construction

Because the 68HC microprocessor does not have an indirect addressing CALL instruction, the transfer of control to an outgoing link (by the execution procedure) deserves explanation. Recall that it is desired to perform the following:

CALL (Lp + outgoing_link_offset)

To achieve this in 8080 code, the following sequence of instructions was used:

```
PUSH B
LXI H, return_address
PUSH H
LVI H, outgoing_link_offset
LDS B
PCPL
return_address : PCF P
```
The first instruction (PUSH B) saves the linkage pointer. The next two instructions save the return address (which is normally done automatically by a CALL instruction). The H and I register pair is then loaded with the outgoing_link_offset and added to the E and C register pair (viz., the linkage pointer) by the DAD B instruction. DAD B adds the E and C registers to the H and I registers and leaves the result in the H and I registers. The value 'Lp + outgoing_link_offset' is now jumped to by the PCHI instruction (which transfers control to the address stored in the H and I registers). The final instruction (POE B) restores the linkage pointer upon return from the external procedure.

Very briefly, a relocation bits file was constructed by hand and was of the following form:

SIZE : DB low_byte, high_byte
LO16E : DB binary_number_1, binary_number_2

The SIZE field represents the number of bytes in the relocation bits file. The remainder (of the file) consisted of two binary numbers preceded by a label such as LO16E (where 016E corresponds to an address in the procedure object code listing). A '0' in a binary number corresponds to a non-relocatable byte of object code. A '1' identifies the byte as the first of a two byte relocatable address.

96
2. **The Test Program DEMO**

The address space of DEMO included four objects: (1) the procedure segment DEMO(priority); (2) the procedure segment MULT(ly) which included the entry point 'MULT'; (3) the procedure segment DISPLAY which included the entry points 'HEX_VALUE' and 'BUFFER'; (4) and the data segment HEADER which included the entry points 'HEADER' and 'TITLE'.

As has been noted, DEMO computed and displayed the (hexadecimal) multiplication and addition tables for the values 0 through 15. The construction of each table was performed by the internal (to DEMO) procedure Build_table which is passed a subroutine as a parameter (viz., ADD, an internal procedure, and MULT, an external procedure). ADD and MULT are passed (by Build_table) a number that is added-multiplied by 0 through 15. The result of the computation is displayed by invoking the external procedure DISPLAY.HEX_VALUE. Thus to build a hexadecimal table Build_table simply invokes either ADD or MULT sixteen times passing as a parameter the values from 2 to 15.

Before building a table, DEMO displays an appropriate heading. It does this by dynamically linking to the data segment HEADER, inserting the appropriate title (viz., MULTIPLICATION or ADDITION) at the entry point HEADER.TITLE, and then displaying HEADER by passing it as an
argument to the external procedure DISPLY.BUFFER.

The dynamic linking which takes place during the execution of DEMO is given in figure 14. DEMO includes examples of all the various capabilities (of external objects) desired in a dynamic linking environment including:

(1) The ability to dynamically link and execute external procedures—DEMO dynamically links to and invokes DISPLY.

(2) The ability to reference external data—DEMO links to and references HEADER.

(3) The ability to pass external objects as arguments—HEADER and MULT are passed to DISPLY and Build_table respectively.

(4) The ability of an external object to engage in dynamic linking—MULT dynamically links to DISPLY.HEX_VALUE.

(5) The implementation of entry points in objects—DISPLY and HEADER both are referenced via entry points.

3. The Test Program SUM

The procedure SUM was included to allow a complete and comprehensive discussion of the concepts presented in this thesis. SUM itself is rather simple. It dynamically links to the external data segment ARRAY and sums the (data) bytes of ARRAY. The results are displayed by dynamically linking to DISPLY.HEX_VALUE (passing the sum of ARRAY's bytes as an argument). DISPLY.BUFFEP is also invoked to display appropriate messages along with the computation result.

98
DYNAMIC LINKING IN DEMO

FIGURE 14
A pseudocode listing of SUM is given in figure 15 while figure 16 presents a representative assembly code translation of SUM. The assembly code used is not associated with any particular microprocessor, but is considered within the capabilities of most microprocessor instruction sets. The only instruction used which may cause confusion is LDPARAM (viz., load parameter register). This instruction is simply a register load but the mnemonic LDPARAM is offered to signify the passing of arguments to an external procedure. (Note that the dynamic linker demonstration implementation uses the D and E register pair for this purpose.)

The combined linkage table for SUM is shown in figure 17. (The figure does not include ARRAY.link or a link for DISPLY.BUFFER). The linkage table for SUM includes an incoming link (entry #1) which would be used if SUM were referenced as an external object. Entry #2 is the outgoing link from SUM to ARRAY while Entry #3 represents the outgoing link from SUM to DISPLY.HEX_VALUE.

When the two outgoing links of SUM are snapped, the unlinking data is included in the snapped link and includes the symbolic name table offset of APRAV and DISPLY.HEX_VALUE (in SUM.sym) respectively and the appropriate linked list pointers. Unlinking linked lists are implemented as circular linked list. Thus the linked list for DISPLY starting with
PROCEDURE Sum;

/* Sum adds the bytes of the external data structure 'Array' and then calls on the external procedure 'Display' to output the result. */

DECLARE Sum ENTRY POINT;
    Array DATA EXTERNAL;
    Display PROCEDURE EXTERNAL;
    result : BYTE;
    array_pointer : POINTER;
    data_array BASED at array_pointer STRUCTURE of number_of_bytes : BYTE;
    data : ARRAY of BYTES;
END;

i : BYTE;

/* end of declarations */

array_pointer = address of array;
result = 0;

FOR i = 1 to data_array.number_of_bytes;
    result = result + data_array.data(i);
ENDFOR;

CALL display.buffer ('The sum of the data array is ', ' & '); CALL display.hex_buffer (result);

/* generate a carriage return and line feed */

CALL display.buffer (CP, IF, ' & '); CALL display.buffer ('End of Sum', ' & ');

END Sum,

PSEUDOCODE FOR SUM

FIGURE 15

101
OBJECT CODE

/* comments */
CALL (ip + out לנו link_offset_2)
RPC ip
PCF ip
IPRFN (ip + out_frames_offset_2)
CR ip
/ * data declarations */

string_1 = "Sum of the data array is ", delimiter
string_2 = ASCII carriage return, ASCII line feed, delimiter
string_3 = "End of sum", delimiter

/* symbolic data table */

// descritter_1, [ incoming link_offset ] , [ entry_point ], sum
[descritter_1], [out_frames_offset_1], [ void ], [array]
[descritter_2], [out_frames_offset_1], [ void ], [array]
[descritter_3], [out_frames_offset_2], [ void ], [display.buffer]
[descritter_4], [out_frames_offset_2], [ void ], [display.hex_value]
### LINKAGE TABLE

<table>
<thead>
<tr>
<th>Offset</th>
<th>Before execution</th>
<th>After execution</th>
</tr>
</thead>
<tbody>
<tr>
<td>0-3</td>
<td>sum Ip</td>
<td>sum Ip</td>
</tr>
<tr>
<td>4-7</td>
<td>nil</td>
<td>array Ip</td>
</tr>
<tr>
<td>8-11</td>
<td>nil</td>
<td>disp Ip</td>
</tr>
<tr>
<td>12-15</td>
<td>nil</td>
<td>nil</td>
</tr>
</tbody>
</table>

#### Sun Linkage Table 1

<table>
<thead>
<tr>
<th>Offset</th>
<th>Function</th>
<th>Address</th>
</tr>
</thead>
<tbody>
<tr>
<td>0-3</td>
<td>linked e till size</td>
<td>header</td>
</tr>
<tr>
<td>4-7</td>
<td>syn norm till addr</td>
<td>linked list ptr</td>
</tr>
<tr>
<td>8-11</td>
<td>linked list ptr</td>
<td>unsnipped incoming</td>
</tr>
<tr>
<td>12-15</td>
<td>entry #1</td>
<td>link to Sun</td>
</tr>
<tr>
<td>16-19</td>
<td>linked e till offset</td>
<td>load ptr reg, addr</td>
</tr>
<tr>
<td>20-23</td>
<td>disp #1</td>
<td>disp #2</td>
</tr>
<tr>
<td>24-27</td>
<td>disp #3</td>
<td>disp #4</td>
</tr>
<tr>
<td>28-31</td>
<td>disp #5</td>
<td>disp #6</td>
</tr>
<tr>
<td>32-35</td>
<td>disp #7</td>
<td>disp #8</td>
</tr>
<tr>
<td>36-39</td>
<td>disp #9</td>
<td>disp #10</td>
</tr>
<tr>
<td>40-43</td>
<td>disp #11</td>
<td>disp #12</td>
</tr>
</tbody>
</table>

#### Sun Linkage Table 2

<table>
<thead>
<tr>
<th>Offset</th>
<th>Function</th>
<th>Address</th>
</tr>
</thead>
<tbody>
<tr>
<td>0-3</td>
<td>linked e till size</td>
<td>header</td>
</tr>
<tr>
<td>4-7</td>
<td>syn norm till addr</td>
<td>linked list ptr</td>
</tr>
<tr>
<td>8-11</td>
<td>linked list ptr</td>
<td>unsnipped incoming</td>
</tr>
<tr>
<td>12-15</td>
<td>entry #1</td>
<td>link to Sun</td>
</tr>
<tr>
<td>16-19</td>
<td>linked e till offset</td>
<td>load ptr reg, addr</td>
</tr>
<tr>
<td>20-23</td>
<td>disp #1</td>
<td>disp #2</td>
</tr>
<tr>
<td>24-27</td>
<td>disp #3</td>
<td>disp #4</td>
</tr>
<tr>
<td>28-31</td>
<td>disp #5</td>
<td>disp #6</td>
</tr>
<tr>
<td>32-35</td>
<td>disp #7</td>
<td>disp #8</td>
</tr>
<tr>
<td>36-39</td>
<td>disp #9</td>
<td>disp #10</td>
</tr>
<tr>
<td>40-43</td>
<td>disp #11</td>
<td>disp #12</td>
</tr>
</tbody>
</table>

### Combined LINKAGE TABLE for SUN

#### Figure 17
the header entry (in DISPIY.link) goes from offset 17F to 00H (the snapped outgoing link from SUM to DISPIY.HEY_VALUE). The linked list pointer at 22H (in SUM.link) points to DISPIY's linkage address table entry which in turn points to DISPIY.link (viz., DISPIY.link's header which contains the first node of DISPIY's linked list).

The assembly code for SUM, ARRAY, and DISPIY is included in appendix (C) along with the output generated by SUM, the process reference table, and the combined linkage table also. The process reference table and combined linkage table are annotated to provide additional clarification.

4. Observations on the Implementation
   a. Size of the Dynamic Linker Implementation

   The dynamic linker including the display linkage table and display process reference table routines was 6340 bytes in length. This includes 1K bytes of memory statically allocated to the combined linkage table and 15F bytes reserved for the hardware stack. (It should be noted that additional memory was allocated to the PL/M-80 stack segment to prevent stack overflow during test program execution. This was necessary since the PL/M-80 stack is allocated based on the needs of the dynamic linker and does not take into account stack operations done by other procedures in a process.) It is emphasized that no effort was made to
optimize the object code. Instead, the dynamic linker was written to be as clear and obvious as possible.

The dynamic linker was also compiled without the display linkage table and display process reference table routines (which were included for the purposes of the demonstration only). This edition of the linker was 6272 bytes in length. It is estimated that a complete (i.e., including unlinking) and optimized implementation of the dynamic linker should require about 7282 bytes of object code. It is noted that error conditions were not checked for by the dynamic linker. However, since there are essentially only two error conditions which could occur, it is felt that the size estimate for a dynamic linker is still valid. The error conditions which may occur are (1) a reference is made to a non-existent entry point (References to non-existent files are flagged by the library routines.), and (2) The statically allocated 1K combined linkage table is overflowed. Such problems as running out of free memory or process reference table entries are handled by the un linker.

b. Overhead Associated with Snapped Links

One of the major arguments against dynamic linking is the issue of overhead associated with snapped links. Before debating this must be true, it is observed that the cost of dynamic linking associated with snapping a link (i.e., the first reference of an external object) is on the
order of the overhead required to statically link the same object.

With respect to snapped procedure links, the overhead (associated with the linker implementation) lies in two areas. First, the linkage pointer must be updated to always indicate the executing procedure's linkage table. Thus the linkage pointer must be saved and restored for each external procedure reference, which requires an additional two instructions. Additionally the linkage pointer is set to point to the (dynamically linked) external procedure by the snapped incoming link, which requires a third instruction. Secondly, the execution point goes from the calling procedure to the external procedure via the snapped outgoing and incoming links. This requires two jump instructions not needed for internal procedure calls thereby bringing the total overhead to five instructions. It is noted that the extensive code necessary in invoke an external object's outgoing link is considered a limitation of the FOE (because of the lack of an FOE indirect call instruction) and is not considered overhead induced by dynamic linking.

Recall that to reference external data (via the outgoing link) a call to the outgoing link is performed, the virtual address of the data is loaded in a pointer, and a return instruction (to the calling procedure) is executed. Since internal data is essentially referenced by loading a
pointer with the address of the (internal) data, the overhead associated with dynamic linking (for data) is limited to a CALL and RETURN instruction.
VIII. CONCLUSIONS

Based on the research supported in this thesis it is reasonable to assert that dynamic linking is feasible in a microcomputer environment. However, given that the linker design is implementable on microprocessors, it can be asserted that dynamic linking does not require the support of specialized hardware and thus can be feasibly implemented on most general purpose computers (including minicomputers and main frames). The overhead is within reason and can be far outweighed by the derived benefits. It has been implied [9, 13, 14] that dynamic linking requires the support of specialized hardware. It is felt that the major contribution of this thesis is to dispell that notion.
EXECUTIVE Linker;

/*
Explanation of variables and constants:

En_buffer - The entry name buffer is a string variable where the entry name associated with an external reference is stored once the entry name has been extracted from the calling procedure's symbolic name table.

Fixed_Sn_offset - The fixed symbolic name offset is a constant which represents the number of bytes in that portion of a symbolic name table entry that does not vary in size (i.e., the descriptor, link offset, and entry point).

Free_link_table - The free linkage table variable is the next free location in the combined linkage table where new (object) linkage tables can be constructed.

In_link_address - The incoming link address is the virtual address of the incoming link for the <external Procedure|entry_name> being linked.

Incoming_link - The incoming link structure represents the format of an incoming link. Incoming link is based at the incoming link address.

Linkage_ptr - The linkage pointer.

Linkage_array - Linkage_array is a linkage table structure based at the Linkage pointer.

New_link_ptr - The new linkage pointer is assigned the value of the linkage pointer of the external object being linked.

New_link_table - The new linkage table is a linkage table structure (of the external object being linked) based at the new linkage pointer.

Object_segment_number - Object segment number is the segment number assigned to the external object being linked.

109
Object_type - Object type represents whether the external object being linked is a procedure or data.

Out_link_address - The outgoing link address is the virtual address of the outgoing link assigned to the <external_object|entry_name> being linked.

Outgoing_link - The outgoing link based at the outgoing link address.

Sn_buffer - The symbolic name buffer is a string variable where the symbolic name of the external reference is stored once the symbolic name has been extracted from the calling procedure's symbolic name table.

Sn_address - The symbolic name address is a pointer into a symbolic name table.

Sn_item - A symbolic name item is a structure based at the symbolic name address and represents an entry in a symbolic name table.

Sn_offset - The symbolic name offset is the parameter passed the linker and is the offset into the calling procedure's symbolic name table of the external reference to be linked.

Sn_size - The symbolic name size is the number of character in an <external_reference|entry_name> as found in a symbolic name table entry.

Sn_size_mask - The symbolic name size mask is used to extract the size of a symbolic name from a descriptor in the symbolic name table of the calling procedure.

Sn_type_mask - The symbolic type mask extracts the type of an external reference (i.e., procedure or data) from the descriptor.

Target_address - The target address is the ultimate virtual address in an external object which the calling procedure seeks to reference.
Template seg number - The template segment number is the segment number assigned to the linkage table template when it is entered in a process address space.

Template - Template is a linkage table template structure based at template segment number.

Type data - Type data is a constant which is used to identify external data objects.

Type procedure - Type identifies external procedure objects.

/* end of variable explanations */

/* explanation of declaration types */

ADDRESS - a virtual address.
BYTE - the contents of a virtual address.
CHARACTER - an ASCII character.
INTEGER - a variable.
POINTER - an address variable which points to a user defined data structure.
STRUCTURE - a Pascal record.

/* end of explanation of declaration types */
The following is a list of variable and constant declarations used in the linker.

DECLARE

En_buffer : STRUCTURE of
  size : INTEGER;
  name : ARRAY of CHARACTERS;
END;

Fixed_Sn_offset : INTEGER CONSTANT;
Free_link_table : ADDRESS;

In_link_address : POINTER;
Incoming_link : STRUCTURE BASED at In_link_address of
  Link_snapped_bit : BYTE;
  Load_Lp : INTEGER;
  Jump_inst : INTEGER;
END;

Linkage_ptr : POINTER;
Linkage_array : STRUCTURE BASED at Linkage_ptr of
  Size : INTEGER;
  Snt_address : ADDRESS;
  Body : ARRAY of BYTE;
END;

Linkage_address_table : ARRAY of ADDRESS;

New_link_ptr : POINTER;
New_link_table : STRUCTURE BASED at New_link_ptr of
  Size : INTEGER;
  Snt_address : ADDRESS;
  Body : ARRAY of BYTE;
END;

Object_seg_number : ADDRESS;
Object_type : BYTE;

Out_link_address : POINTER;
Outgoing_link : ARRAY of INTEGER
  BASED at Out_link_address;

Sn_buffer : STRUCTURE of
  size : INTEGER;
  name : ARRAY of CHARACTERS;
END;
Sn_address : POINTER;
Sn_item : STRUCTURE BASED at Sn_address of
descriptor : BYTE;
name : ARRAY of CHARACTERS;
link_offset : INTEGER;
entry_point : INTEGER;
END;

Sn_offset : INTEGER;
Sn_size : INTEGER;
Sn_size_mask : BYTE CONSTANT;
Sn_type_mask : BYTE CONSTANT;

Target_address : ADDRESS;

Template_seg_number : POINTER;
Template : STRUCTURE BASED at Template_seg_number of
Size : INTEGER;
Snt_offset : INTEGER;
Body : ARRAY of BYTE;
END;

Type_data : BYTE CONSTANT;
Type_procedure : BYTE CONSTANT;

END of DECLARATIONS;
BEGIN

/* Save processor registers if necessary */

CALL Access_Symbolic_Name_Data;
    PARAMETER_LIST : Sn_offset, Sn_buffer, En_buffer,
    Linkage_pointer;
    RETURN_LIST : Sn_address, En_buffer, Sn_buffer,
    Object_type, Out_link_address;

/* ASM_Make_Accessible calls on the Address Space Manager to add the object found in Sn_buffer.name to the process address space and return the segment number assigned to that object. */

CALL ASM_Make_Accessible;
    PARAMETER_LIST : Sn_buffer.name;
    RETURN_LIST : Object_seq_number;

CALL Linkage_Table_Routines;
    PARAMETER_LIST : Object_seq_number, Link_address_table,
    Free_link_table, New_link_ptr;
    RETURN_LIST : New_link_ptr, Link_address_table,
    Free_link_table;

CALL Access_Entry_Name_Data;
    PARAMETER_LIST : Sn_address, En_buffer, New_link_ptr,
    Object_type, Object_seq_number;
    RETURN_LIST : Target_address, In_link_address;

CALL Snap_the_Links;
    PARAMETER_LIST : In_link_address, Out_link_address,
    New_link_ptr, Object_type,
    Target_address;
    RETURN_LIST : None;

/* restore processor registers if necessary */

JUMP to Out_link_address;

END Linter;
Access_Symbolic_Name_Data performs the following functions:

1. Obtains the address of the symbolic name of the external reference being linked.
2. Loads the symbolic name of the external reference in the symbolic name buffer (Sn_Buffer).
3. Loads the entry name of the external reference in the entry name buffer (En_buffer).
4. Computes the outgoing link address and determines whether the external object is a procedure or data.

PROCEDURE Access_Symbolic_Name_Data;

PARAMETER_LIST : Sn_offset, Sn_buffer, En_buffer,
                Linkage_pointer;

DECLARE i, temp : INTEGER;
Sn_address = Linkage_array.Snt_address + Sn_offset;
Sn_size = Sn_item.size AND Sn_size_mask;

/* Load the symbolic name into Sn_buffer.name. */
i = 1;
DO WHILE (Sn_item.name(i) <> ':') AND (i <> Sn_size);
   Sn_buffer.name(i) = Sn_item.name(i);
i = i + 1;
ENDDO;

/* Load symbolic name size into Sn_buffer.size. */
IF i = Sn_size THEN Sn_buffer.size = i;
ELSE Sn_buffer.size = (i-1);
/* Load the entry name buffer with the entry name. */

IF i = Sn_size THEN BEGIN
  /* No entry name specified, default to the symbolic name as the entry name. */
  En_buffer.size = Sn_size;
  FOR i = 1 to Sn_size by 1 DO
    En_buffer.name(i) = Sn_item.name(i);
END THEN;

ELSE BEGIN /* entry name specified */
  temp = i;
  i = i + 1;
  /* load size of entry name in En_buffer.size */
  En_Buffer.size = Sn_size - i;
  /* load entry name into entry name buffer */
  DO WHILE (i <= Sn_size)
    En_Buffer.name(i - temp) = Sn_item.name(i);
    i = i + 1;
  ENDWHILE;
END ELSE;

/* Compute the address of the outgoing link and determine the type (procedure or data) of the external object. */

Out_link_address = Linkage_pointer + Sn_item.link_offset;
Object_type = Sn_item.descriptor AND Sn_type_?ask;

RETURN_LIST : Sn_address, En_buffer, Sn_buffer,
Object_type, Out_link_address;

END Access_Symbolic_name_Data;
Linkage_Table_Routines performs the following functions:

1. Determines if a linkage table already exists for the external reference being linked.
   a. If not, Linkage_Table_Routines initializes the Linkage Address Table value for the object and then calls on Build_Object.link.
   b. If so, Linkage_Table_Routines sets a return parameter (New_link_ptr) equal to the linkage pointer value for the new object's linkage table.

PROCEDURE Linkage_Table_Routines;
PARAMETER_LIST : Object_seg_number, Link_address_table, Free_link_table, New_link_ptr;
IF Link_address_table (Object_seg_number) = nil THEN
  /* This is the first time the object has been referenced by the process and the linker must build a linkage table for the object. */
BEGIN
  Link_address_table (Object_seg_number) = Free_link_table;
  CALL Build_Object.link;
  CALL Build_Object.link;
  PARAMETER_LIST : Object_type, Free_link_table, Sn_buffer, Object_seg_number;
  RETURN_LIST : New_link_ptr, Free_link_table;
END IF;
ELSE
  /* The object already has a linkage table. */
  New_link_ptr = Link_address_table (Object_seg_number);
RETURN_LIST : New_link_ptr, Link_address_table, Free_link_table;
END Linkage_Table_Routines;
Build_Object.link performs the following functions:

1. Causes the Address Space Manager to load the external object’s linkage table template into the process address space.

2. Initializes a return parameter (New_link_ptr) to the value of the object’s linkage pointer.

3. Appends Object.link to the end of the combined linkage table.

4. Deletes the linkage table template from the process address space.

******************************************************************************

PROCEDURE Build_Object.link;
PARAMETER_LIST: Object_type, Free_link_table, Sn_buffer, Object_seg_number;

DECLARE i : INTEGER;

/ * The following two steps cause Sn_buffer.name to be loaded with the filename \symbolic name.template\ and then invokes the Address Space Manager to have the template loaded into the process address space (with ASM.Make Accessable returning the segment number assigned to the template. */

APPEND ‘template’ to Sn_buffer.name;
CALL ASM.Make Accessable;
PARAMETER_LIST: Sn_buffer.name;
RETURN_LIST : Template_seg_number;

New_link_ptr = Free_link_table;

IF Object_type = Type_procedure THEN BEGIN

/ * If the object is a procedure, then its symbolic name table is in the object code segment. */

New_link_table.Snt_address = Template.Snt_offset + Object_seg_number;

ENDTHEN;
ELSE BEGIN

if the object is data, then its symbolic name

table is in its template. /*

New_link_table.Snt_address = New_link_ptr +
Template.Snt_offset;
ENDELSE;

New_link_table.Size = Template.Size;

FOR i = 0 to (Template.Size - 2) by 1 DO

New_link_table.Body (i) = Template.Body (i);
ENDFOR;

Free_link_table = Free_link_table + 1 +
Template.Size;

CALL ASM_Remove_Seg;

PARAMETER_LIST : Snt_buffer.name;
RETURN_LIST : None;

RETURN_LIST : New_link_ptr, Free_link_table;
END Build_Object.link;
AccessEntry Name Data performs the following functions:

1. Computes the target address in the external object to be utilized in the linkage process.
2. Computes the incoming link address (if applicable).

```
PROCEDURE AccessEntry_Name_Data;

PARAMETER_LIST : Sn_address, En_buffer, New_link_ptr,
                 Object_type, Object_seg_number;

/* Get_Next_Sn_item causes Sn_address to point to the
   next entry in the external object's Symbolic Name
   Table. */
PROCEDURE Get_Next_Sn_item (Sn_address);
   Sn_address = Sn_address + Fixed_Sn_offset
                + (Sn_item.descriptor AND Sn_size_mask);
   RETURN Sn_address;
END Get_Next_Sn_item;

/* Begin Access_Entry_Name_Data. */
Sn_address = New_link_table.Snt_address;
DO WHILE Sn_item.name <> En_buffer.name;
   CALL Get_Next_Sn_item;
   Target_address = Object_seg_number + Sn_item.entry_point;
   IF Object_type = Type_procedure THEN
      In_link_address = New_link_ptr - Sn_item.link_offset;
   END IF;
END WHILE;
RETURN_LIST : Target_address, In_link_address;
END AccessEntry_Name_Data;
```
Snap_the_Links performs the following functions:

1. Snaps the outgoing link and incoming link for a procedure object.
2. Snaps the outgoing link for a data object.

procedure Snap_the_Links;

parameter_list : In_link_address, Out_link_address,
                 New_link_ptr, Object_type,
                 Target_address;

if Object_type = Type_procedure then begin
  /* Snap a link for an external procedure. */
  Outgoing_link (0) = 'Jump to' In_link_address;
  if Incoming_link.link_snapped_bit is unsnapped then begin
    Incoming_link.Load_Id = 'Load Ip' New_link_ptr;
    Incoming_link.Jump_inst = 'Jump to' Target_address;
  end then;
  end then;
else begin
  /* Snap a link for external data. */
  Outgoing_link (0) = 'Load pointer' Target_address;
  Outgoing_link (1) = 'Return instruction';
end else;

end Snap_the_Links;
EXECUTIVE Address Space Manager;

/*
Explanation of variables:

PRT_size - The size of the process reference table.

Seg_number - The segment number assigned to a newly loaded object by the procedure load_Object.

PRT - The process reference table.

/* end of variable explanation */

/* The following is a list of variable and constant declaration used in the Address Space Manager. */

DECLARE

PRT_size : INTEGER;
Seg_Number : ADDRESS;

PRT : ARRAY of STRUCTURES of
    Valid_bit : BOOLEAN;
    Name : ARRAY of CHARACTERS;
    Seg_number : ADDRESS;
END

END of DECLARATIONS;
ASM_Make_Accessable performs the following functions:

1. Determines if the object passed as an argument is already in the process reference table (i.e., is already in the process address space).

2. If not, loads the object into memory at the next available memory location and updates the Process Reference Table (PRT).

3. Returns to the point of call the segment number assigned to the object.

PROCEDURE ASM_Make_Accessable;

PARAMETER_LIST : Object_name;

DECLARE i : INTEGER;
  found : BOOLEAN;

i = 1;
found = false;

/* check to see if Object_name is in the PRT */

DO WHILE NOT found AND i <= PRT_size;
  IF PPT(i).valid_bit = valid THEN BEGIN
    IF PPT(i).name = Object_name THEN found = true;
    ELSE i = i + 1;
  END;
ENDWHILE;
IF NOT found THEN BEGIN

/* find a free PRT entry */

i = 1;
DO WHILE PRT(i).valid_bit = valid;
   i = i + 1;
ENDWHILE;

CALL Load_object;
PARAMETER_LIST : Object_name;
RETURN_LIST : Seg_Number;

PRT(i).name = Object_Name;
PRT(i).seg_number = Seg_Number;
PRT(i).valid_bit = valid;

ENDTHEN;

RETURN_LIST : PRT(i).seg_number;

END ASM_Make_Accessable;
ASM_Remove_Seg performs the following functions:
1. Removes an object from a process address space.

PROCEDURE ASM_Remove_Seg;
PARAMETER_LIST : Object_name;

DECLARE i : INTEGER;
found : BOOLEAN;

/* find Object_name in the process reference table (PRT) */
i = 1;
found = false;
DO WHILE NOT found AND i <= PRT_size;
   IF PRT(i).name = Object_name
      THEN found = true;
      ELSE i = i + 1;
   ENDWHILE;

/* remove the object from the PRT */
PRT(i).valid_bit = invalid;
RETURN_LIST : none;
END ASM_Remove_Seg;
PL/M-80 COMPILER  PROCESS INITIALIZATION

ISIS-II PL/M-80 V3.1 COMPIILATION OF MODULE EXEC
OBJECT MODULE PLACED IN: F1:EXEC.OBJ
COMPILED INVOKED BY: PLM80 :F1:EXEC.SRC PAGELENGTH(36) TITLE('PROCESS INITIALIZATION')

1

EXEC : DO;

/* DATE LAST EDITED : 29 JULY 1980 */

/**** PROCESS INITIALIZATION ****/

/* LITERALS */

2 1

DECLARE LIT LITERALLY 'LITERALLY',
POINTER LIT 'ADDRESS';
INTEGER LIT 'ADDRESS';
TRUE LIT 'E1H';
FALSE LIT 'E0H';
SPACE LIT '20H';

POPSH LIT 'E1H';
POP$D LIT 'E1H';
PCHL LIT 'E9H';
CALL$INST LIT 'ECDH';
RETURN$INST LIT 'EC9H';
LXI$B LIT '01H';

SET LIT '01H';
NOT$SET LIT '00H';

/**** PROGRAM VARIABLES ****/
31 DECLARE RET$VALUE$PTR POINTER,
   OBJECT STRUCTURE (
     UNIQUE$ID BYTE,
     BASE$ADDRESS ADDRESS),
   LINKER$VALUES$PTR POINTER,
   LINKER$VALUES STRUCTURE (
     LINKER$ADDRESS ADDRESS,
     LINK$ADDR$TALE$BASE ADDRESS,
     LINK$TABLE$ADDRESS ADDRESS),
   PROGRAM$POINTER POINTER,
   PROGRAM STRUCTURE (
     NAME (12) BYTE,
     SIZE BYTE),
   DISPLAY$TOGGLE BYTE,
   TYPE$PROCEDURE BYTE INITIAL ($EF);
PL/M-80 COMPILER  PROCESS INITIALIZATION

8  2  END INITIALIZE$ASM;

/** ASMS$MAKE$ACCESSABLE ENTERS AN OBJECT IN THE ADDRESS SPACE
   OF THE EXECUTING PROCESS. /**

9  1  ASMS$MAKE$ACCESSABLE : PROCEDURE (OBJ$NAME$PTR, RETURN$VALUE$PTR)
   EXTERNAL;

10 2  DECLARE OBJ$NAME$PTR POINTER,
    RETURN$VALUE$PTR POINTER;
    END ASMS$MAKE$ACCESSABLE;

/** DISPLAY$PRT DISPLAYS THE PROCESS REFERENCE TABLE ON THE CRT /**

12 1  DISPLAY$PRT : PROCEDURE EXTERNAL;
    END DISPLAY$PRT;

/** OUTPUT$THE$LINK$TABLE DISPLAYS THE COMBINED LINKAGE TABLE ON
   THE CRT. /**

14 1  OUTPUT$THE$LINK$TABLE : PROCEDURE (LINK$ADDRESS$TABLE$BASE) EXTERNAL;
15 2  DECLARE LINK$ADDRESS$TABLE$BASE ADDRESS;
16 2  END OUTPUT$THE$LINK$TABLE;

/** INITIALIZE$LINKEP Initializes the Linker and Returns the Address
   of the Control Module "Linker" and the Base Address of the
   Linkage Address Table. /**

17 1  INITIALIZE$LINKEP : PROCEDURE (RETURN$VALUES$POINTER) EXTERNAL;
18 2  DECLARE RETURN$VALUES$POINTER POINTER;
19 2  END INITIALIZE$LINKEP;

/** LINKAGE$TABLE$ROUTINES BUILDS A LINKAGE TABLE FOR AN OBJECT
   AND ENTERS THE OBJECT'S LINKAGE TABLE ADDRESS IN THE LINKAGE
   ADDRESS TABLE. /**

81
82
PI/M-60 COMPILER  PROCESS INITIALIZATION

20  1  LINKAGE$TABLE$Routines : PROCEDURE (OBJECT$SEG$NUMBER,  
       OBJECT$BASE$ADDRESS,            
       OBJECT$TYPE,                     
       POINTER$TO$SYMBOLIC$NAME)        
       EXTERNAL;                         

21  2  DECLARE OBJECT$SEG$NUMBER BYTE,     
       OBJECT$BASE$ADDRESS ADDRESS,      
       OBJECT$TYPE BYTE,                 
       POINTER$TO$SYMBOLIC$NAME POINTER; 

22  2  END LINKAGE$TABLE$Routines;

/**** BOOT RETURNS CONTROL TO THE OPERATING SYSTEM ****/

23  1  BOOT : PROCEDURE EXTERNAL;
24  2  END BOOT;

/**** CRIF OUTPUTS A CARRIAGE RETURN AND LINE FEED ON THE CPT. ****/

25  1  CRLF : PROCEDURE EXTERNAL;
26  2  END CRLF;

/****************************************************************************/
/****  PROCESS INITIALIZATION ROUTINES  ****/
/****************************************************************************/
/****************************************************************************/
/****************************************************************************/
*  READ$COMMAND$LINE READS THE NAME OF THE PROGRAM TO BE  *
*  EXECUTED AND SETS THE DISPLAY TOGGLE.  *
PL/M-80 COMPILER       PROCESS INITIALIZATION

**

*************************************************************/

27 1 READ$COMMAND$LINE : PROCEDURE (NAME$POINTER);

28 2 DECLARE NAME$POINTER POINTER,

   OBJECT BASED NAME$POINTER STRUCTURE ( 
       NAME (12) BYTE, 
       SIZE BYTE), 

   I BYTE, 
   INPUT$POINTER POINTER, 
   INPUT$BUFFER BASED INPUT$POINTER (12) BYTE;

29 2 I = 0;

/*** THE CP/M OPERATING SYSTEM STORES THE COMMAND LINE IN A BUFFER 

   STARTING AT $0H. THE BYTE AT $0H CONTAINS THE BUFFER SIZE 

   WHILE STARTING AT $2H IS THE ACTUAL COMMAND LINE. THIS, TO 

   RUN A PROGRAM, THE FOLLOWING COMMAND LINE IS INPUTED:

   A$ EXEC PROGRAM $ 

   WHERE 'A$' IS THE CP/M PROMPT; EXEC IS THE DYNAMIC LINKER 

   ROUTINE; 'PROGRAM' IS THE PROGRAM NAME; AND '$' INDICATES 

   WHETHER THE LINKAGE TABLE AND PROCESS REFERENCE TABLE ARE TO 

   BE DISPLAYED OR NOT. IN THIS CASE, THE COMMAND LINE IS:

   'PROGRAM $'.  ***/

38 2 INPUT$POINTER = $2H;

/*** COPY THE NAME OF THE PROGRAM TO BE EXECUTED INTO THE NAME 

   BUFFER.  ***/
PL/M-80 COMPILER

PROCESS INITIALIZATION

31 2  DO WHILE INPUT$BUFFER (I) <> SPACE;
32 3  OBJECT$NAME(I) = INPUT$BUFFER (I);
33 3  I = I + 1;
34 3  END;

/*** SET THE SIZE OF THE OBJECT NAME /***/
35 2  OBJECT$SIZE = I;

/*** SET THE OBJECT TYPE TO EXECUTABLE CODE (TYPE "COM"). /***/
36 2  OBJECT$NAME (I) = 'C';
37 2  OBJECT$NAME (I + 1) = 'C';
38 2  OBJECT$NAME (I + 2) = 'O';
39 2  OBJECT$NAME (I + 3) = 'M';

/*** NOW SEE IF THE DISPLAY TOGGLE SHOULD BE SET /***/
40 2  IF INPUT$BUFFER (I + 1) = '$' THEN DISPLAY$TOGGLE = SET;
42 2  ELSE DISPLAY$TOGGLE = NOT$SET;
43 2  END READ$COMMAND$LINE;

/***************************************************************************/
*/ EXECUTE IS THE KEY TO INVOKING THE PROGRAM TO BE EXECUTED */
*/ AND SETTING THE LINKAGE POINTER. IT LOADS THE LINKAGE */
*/ POINTER IN THE B & C REGISTER PAIR (THE DESIGNATED LINKAGE */
*/ POINTER REGISTER) AND THEN INVOKES THE PROGRAM TO BE */
*/ EXECUTED. IT DOES THIS BY INITIALIZING AN ARRAY WITH THE */
*/ MACHINE INSTRUCTIONS REQUIRED AND THEN EXECUTING THE */
*/ ARRAY. */
/******************************************************************************/
EXECUTE : PROCEDURE (LINKAGE$POINTER, OBJECT$ADDRESS);

DECLARE LINKAGE$POINTER POINTER,
OBJECT$ADDRESS ADDRESS,
EXECUTE$ARRAY$BASE ADDRESS,
EXECUTE$ARRAY STRUCTURE (BYTE1 BYTE,
BYTE2$3 ADDRESS,
BYTE4 BYTE,
BYTE5$6 ADDRESS,
BYTE7 BYTE);

/* SET EXECUTE$ARRAY$BASE TO POINT TO EXECUTE$ARRAY */

EXECUTE$ARRAY$BASE = .EXECUTE$ARRAY$BYTE1;

EXECUTE$ARRAY$BYTE1 = LIX$P;
EXECUTE$ARRAY$BYTE2$3 = LINKAGE$POINTER;
EXECUTE$ARRAY$BYTE4 = CALL$INST;
EXECUTE$ARRAY$BYTE5$6 = OBJECT$ADDRESS;
EXECUTE$ARRAY$BYTE7 = RETURN$INST;

/* NOW EXECUTE EXECUTE$ARRAY */

CALL EXECUTE$ARRAY$BASE;

FND EXECUTF;

*******************************************************************************

* BUILD$INTERRUPT$VECTOR $INITIALIZES$ THE $INTERRUPT$ $VECTOR$ *
* TO CALL THE $LINKER$ AND THEN $JUMP$ $TO$ $THE$ $OUTGOING$ $LINK$. *
*******************************************************************************
PI/M-80 COMPILER  PROCESS INITIALIZATION

54 1 BUILD$INTERRUPT$VECTOR : PROCEDURE (LINKER$ADDRESS);

55 2 DECLARE INTERRUPT$BASE POINTER,
      INTERRUPT$VECTOR BASED INTERRUPT$BASE STRUCTURE (  
      BYTE1 BYTE,
      BYTE2 BYTE,
      BYTE3$4 ADDRESS,
      BYTE5 BYTE,
      BYTE6 BYTE),

      LINKER$ADDRESS ADDRESS;

      /*** THE INTERRUPT VECTOR INVOKES THE LINKER VIA AN INTERRUPT  
      GENERATED BY THE INITIALIZED OUTGOING LINK. THE INSTRUCTION  
      IN THE OUTGOING LINK WHICH CALLS THE INTERRUPT VECTOR IS A  
      RESET 4 INSTRUCTION (RST 4). THIS INSTRUCTION SAVES THE  
      RETURN ADDRESS ON THE STACK AND JUMPS TO LOCATION 20H. THE  
      INTERRUPT VECTOR REMOVES THE RETURN ADDRESS FROM THE STACK  
      (VIA THE POP$H INSTRUCTION) AND CALL THE LINKER. WHEN THE  
      LINKER HAS FINISHED EXECUTING IT RETURNS THE BASE ADDRESS OF  
      THE (SNAPPED) OUTGOING LINK TO THE INTERRUPT VECTOR. THE  
      INTERRUPT VECTOR RESTORES THE PARAMETER REGISTER (POP$D) AND  
      JUMPS TO THE OUTGOING LINK (PCHL). ***/  

56 2 INTERRUPT$BASE = $20H;

57 2 INTERRUPT$VECTOR.BYTE1 = POP$H;
58 2 INTERRUPT$VECTOR.BYTE2 = CALL$INST;
59 2 INTERRUPT$VECTOR.BYTE3$4 = LINKER$ADDRESS;
60 2 INTERRUPT$VECTOR.BYTE5 = POP$D;
61 2 INTERRUPT$VECTOR.BYTE6 = PCHL;
62 2 END BUILD$INTERRUPT$VECTOR;
PL/10 COMPILER PROCESS INITIALIZATION

/****************************************************************/
/**** MAIN CODE *****/
/****************************************************************/

63 1 CALL CRFlF;
64 1 CALL DISPLAY (.("DYNAMIC LINKER VERSION 1.0", "S"));
65 1 CALL CRFlF;
66 1 CALL CRFlF;
67 1 LINKER$VALUES$PTR = .LINKER$VALUES.LINKER$ADDRESS;
68 1 PROGRAM$POINTER = .PROGRAM.NAME(Q);
69 1 RET$VALUE$PTR = .OBJECT.UNIQUE$ID;
70 1 CALL READ$COMMAND$LINE (PROGRAM$POINTER);
71 1 CALL INITIALIZE$ASM;
72 1 CALL INITIALIZE$LINKER (LINKER$VALUES$PTR);
73 1 CALL ASM$MAKE$ACCESSABLE (PROGRAM$POINTER, RET$VALUE$PTR);
74 1 CALL LINKAGE$TABLE$ROUTINES (OBJECT$UNIQUE$ID,
       OBJECT.BASE$ADDRESS,
       TYPE$PROCEDURE,
       PROGRAM$POINTER);
75 1 CALL BUILD$INTERRUPT$VECTOR (LINKER$VALUES.LINKER$ADDRESS);
76 1 CALL EXECUTE (LINKER$VALUES.LINK$TABLE$ADDRESS, OBJECT.BASE$ADDRESS);
77 1 IF DISPLAY$TOGGLE = SET THEN DO;
78 2 CALL DISPLAY$FPT;
79 2 CALL CRFlF;
PL/M-80 COMPILER  PROCESS INITIALIZATION

81 2       CALL OUTPUT$THE$LINK$TABLE (LINK$VALUES, LINK$ATTR$TABLE$BASE);
82 2       END;
83 1       CALL BOOT;
84 1       END EXEC;

MODULE INFORMATION:

   CODE AREA SIZE   = 0152H   434D
   VARIABLE AREA SIZE = 0034H   52D
   MAXIMUM STACK SIZE = 0006H    6D
315 LINES READ
0 PROGRAM ERROR(S)

END OF PL/M-80 COMPILATION
PL/M-80 COMPILER   LINKER MODULE

ISIS-II PL/M-80 V3.1 COMPIlATION OF MODULE DLKR
OBJECT MODULE PLACED IN :F1:DLKR.OBJ
COMPILER INVOKED BY: PLM80 :F1:DLKR.SRC PAGELENGTH(30) TITLE('LINKER MODULE')

1   DLKR:DO;
    /* DATE LAST EDITED : 4 AUGUST 1980 */

2  1 DECLARE LIT LITERALLY 'LITERALLY',
     TRUE LIT '01H',
     FALSE LIT '00H',
     SPACE LIT '20H',
     BOOLEAN LIT 'BYTE',
     FUNCTION LIT 'PROCEDURE',
     POINTER LIT 'ADDRESS',
     INTEGER LIT 'ADDRESS',
     ENTRY LIT 'PUBLIC',
     INST LIT '01H',
     LOAD LIT '00H',
     LOAD$POINTER LIT '11H',
     JUMP LIT '0C3H',
     RETURN$INST LIT '0C9H',
     UNSNAPPED LIT '00H',
     VALID LIT '01H',
     INVALID LIT '00H';

    /***** VARIABLE DECLARATIONS ****/

3  1 DECLARE
PL/M-60 COMPILER  LINKER MODULE

EN$BUFFER STRUCTURE (  
    NAME (16) BYTE,  
    SIZE BYTE),  
EN$BUFFER$PTR POINTER,  

FIXED$SN$OFFSET BYTE INITIAL (05H),  
FREE$LINK$TABLE ADDRESS,  

IN$LINK$ADDRESS POINTER,  
INCOMING$LINK BASED IN$LINK$ADDRESS STRUCTURE (  
    LOAD$LP (3) BYTE,  
    JUMP$INST (3) BYTE),  

LINKAGE$TABLE (1024) BYTE,  

LINKAGE$POINTER POINTER,  
LINKAGE$ARRAY BASED LINKAGE$POINTER STRUCTURE (  
    SIZE INTEGER,  
    SMT$ADDRESS ADDRESS,  
    BODY (1) BYTE),  

LINKAGE$ADDRESS$TABLE (16) STRUCTURE (  
    VALID$BIT BYTE,  
    BASE$ADDR ADDRESS),  

NEW$LINK$PTR POINTER,  
NEW$LINK$TABLE BASED NEW$LINK$PTR STRUCTURE (  
    SIZE INTEGER,  
    SMT$ADDRESS ADDRESS,  
    BODY (1) BYTE),  

OBJECT STRUCTURE (  
    UNIQUE$ID BYTE,  
    BASE$ADDRESS ADDRESS),  
OBJECT$ID$POINTER POINTER,
OBJECT$TYPE BYTE,
OUT$LINK$ADDRESS POINTER,
OUTGOING$LINK BASED OUT$LINK$ADDRESS (4) BYTE,
SN$BUFFER STRUCTURE ( NAME (12) BYTE,
 SIZE BYTE),
SN$BUFFER$POINTER POINTER,
SN$ADDRESS POINTER,
SN$ITEM BASED SN$ADDRESS STRUCTURE ( DESCRIPTOR BYTE,
 LINK$OFFSET INTEGER,
 ENTRY$POINT INTEGER,
 NAME (1) BYTE),
SN$OFFSET INTEGER,
SN$SIZE BYTE,
SN$SIZE$MASK BYTE INITIAL (1FH),
SN$TYPES$MASK BYTE INITIAL (00H),
TARGET$ADDRESS ADDRESS,
TEMPLATE$BASE$ADDRESS POINTER,
TEMPLATE BASED TEMPLATE$BASE$ADDRESS STRUCTURE ( SIZE INTEGER,
 SNT$OFFSET INTEGER,
 BODY (1) BYTE),
TYPE$DATA BYTE INITIAL (01H),
TYPE$PROCEDURE BYTE INITIAL (00H);
PL/M-80 COMPILER   LINKER MODULE

/**** END OF VARIABLE DECLARATIONS ****/

/*********************************************************************************
* EXTERNALLY DEFINED SYSTEM PROCEDURE DECLARATIONS *
*********************************************************************************/

/**** DISPLAY OUTPUTS AN ASCII CHARACTER STRING TO THE CRT. ****/
DISPLAY: PROCEDURE (STRING$ADDRESS) EXTERNAL;
DECLARE STRING$ADDRESS POINTER;
END DISPLAY;

/**** OUTPUT$ADDR DISPLAYS A 2-BYTE VALUE ON THE CRT. ****/
OUTPUT$ADDR: PROCEDURE (DEVICE, VALUE) EXTERNAL;
DECLARE VALUE ADDRESS,
DEVICE BYTE;
END OUTPUT$ADDR;

/**** DISPLAY$CHAR OUTPUTS AN ASCII CHARACTER TO THE CRT. ****/
DISPLAY$CHAR: PROCEDURE (CHARACTER) EXTERNAL;
DECLARE CHARACTER BYTE;
END DISPLAY$CHAR;

/**** CRLF GENERATES A CARRIAGE RETURN AND LINE FEED ON THE CRT. ****/
CRLF: PROCEDURE EXTERNAL;
END CRLF;

/**** END OF EXTERNAL SYSTEM DECLARATIONS. ****/
PL/M-86 COMPILER  LINKER MODULE

/*** ADDRESS SPACE MANAGER EXTERNAL ROUTINE DECLARATIONS ***/

15  1
ASM$MAKE$ACCESSIBLE : PROCEDURE (OEP$NAME$PTR, RETURN$VALUE$PTR)
    EXTERNAL;

16  2
DECLARE OBJ$NAME$PTR POINTER,
    RETURN$VALUE$PTR POINTER;
END ASM$MAKE$ACCESSIBLE;

18  1
ASM$REMOVE$SEG : PROCEDURE (OBJ$NAME$PTR) EXTERNAL;

19  2
DECLARE OBJ$NAME$PTR POINTER;

20  2
END ASM$REMOVE$SEG;

/*** END OF ADDRESS SPACE MANAGER EXTERNAL DECLARATIONS ***/

******************************************************************************/

/*
THE DYNAMIC LINKER
******************************************************************************/

******************************************************************************/

*/
* ACCESS$SYMBOLIC$NAME$DATA PERFORMS THE FOLLOWING FUNCTIONS:
* *
* 1. OBTAINS THE ADDRESS OF THE SYMBOLIC NAME OF THE EXTERNAL
   REFERENCE BEING LINKED.
* *
* 2. LOADS THE SYMBOLIC NAME OF THE EXTERNAL REFERENCE IN
   THE SYMBOLIC NAME BUFFER (SN$BUFFER).
* *
* 3. LOADS THE ENTRY NAME IN THE EXTERNAL REFERENCE IN THE  
*/
ENTRY NAME BUFFER (EN$BUFFER).

4. COMPUTES THE OUTGOING LINK ADDRESS AND DETERMINES WHETHER THE EXTERNAL OBJECT IS A PROCEDURE OR DATA.

*******************************************************************************/

ACCESS$SYMBOLIC$NAME$DATA : PROCEDURE (LINKAGE$POINTER, SN$OFFSET);

DECLARE LINKAGE$POINTER POINTER,
SN$OFFSET INTEGER,
(I, TEMP) BYTE;

SN$ADDRESS = LINKAGE$ARRAY$SN$ADDRESS + SN$OFFSET;
SN$SIZE = SN$ITEM$DESCRIPTION AND SN$SIZE$MASK;

/*** LOAD THE SYMBOLIC NAME INTO SN$BUFFER$NAME /***/

I = 0;
DO WHILE (SN$ITEM$NAME(I) <> '.' AND (I < SN$SIZE);

SN$BUFFER$NAME(I) = SN$ITEM$NAME(I);
I = I + 1;
END; /* OF THE WHILE CLAUSE */

/*** LOAD THE SYMBOLIC NAME SIZE INTO SN$BUFFER$SIZE /***/

SN$BUFFER$SIZE = I;

/*** LOAD THE ENTRY NAME BUFFER WITH THE ENTRY NAME /***/

IF I = SN$SIZE THEN DO;
PL/M-80 COMPILER

LINKER MODULE

/* NO ENTRY NAME SPECIFIED, DEFAULT TO THE SYMBOLIC
   NAME AS THE ENTRY POINT */

EN$BUFFER.SIZE = SN$SIZE;
DO I = 0 TO (SN$SIZE - 1);
   EN$BUFFER.NAME(I) = SN$ITEM.NAME(I);
END;

END; /* OF THE THEN CLAUSE */

ELSE DO;

TEMP = I;
I = I + 1;
/* LOAD SIZE OF ENTRY NAME INTO EN$BUFFER.SIZE */

EN$BUFFER.SIZE = SN$SIZE - I;
/* LOAD ENTRY NAME INTO ENTRY NAME BUFFER */

DO WHILE (I < SN$SIZE);
   EN$BUFFER.NAME(I - TEMP - 1) = SN$ITEM.NAME(I);
   I = I + 1;
END;

END; /* OF THE ELSE CLAUSE */

/**** COMPUTE THE ADDRESS OF THE OUTGOING LINK AND DETERMINE
   THE TYPE (PROCEDURE OR DATA) OF THE EXTERNAL OBJECT. */

OUT$LINK$ADDRESS = LINKAGE$POINTER + SN$ITEM.LINK$OFFSET;

IF (SN$ITEM.DESCRIPTOR AND SN$TYPE$MASK) = 0 THEN
   OBJECT$TYPE = TYPE$PROCEDURE;
PL/M-80 COMPILER   LINKER MODULE

50 2 ELSE OBJECT$TYPE = TYPE$DATA;

/**** THE CP/M OPERATING SYSTEM UTILIZES A FILETYPE OF 'COM' FOR
PROCEDURES AND 'DATA' FOR DATA. NOW THAT WE KNOW THE OBJECT
TYPE WE WILL INSERT THE FILETYPE IN SN$BUFFER.NAME
ACCORDINGLY. ****/

51 2 IF OBJECT$TYPE = TYPE$PROCEDURE THEN DO;

53 3 I = SN$BUFFER.SIZE;

54 3 SN$BUFFER.NAME(I) = 'C';
55 3 SN$BUFFER.NAME(I + 1) = 'O';
56 3 SN$BUFFER.NAME(I + 2) = 'M';
57 3 SN$BUFFER.NAME(I + 3) = 'M';
58 3 END;
59 2 ELSE DO;

/> THE OBJECT IS TYPE DATA */

60 3 I = SN$BUFFER.SIZE;

61 3 SN$BUFFER.NAME(I) = 'D';
62 3 SN$BUFFER.NAME(I + 1) = 'T';
63 3 SN$BUFFER.NAME(I + 2) = 'A';
64 3 SN$BUFFER.NAME(I + 3) = 'A';
65 3 END;
66 2 END ACCESS$SYMBOLIC$NAME$DATA;

/*****************************/

* BUILD$OBJECT$LINK PERFORMS THE FOLLOWING FUNCTIONS: *

*
PL/1800 COMPILER    LINKER MODULE

* 1. CAUSES THE ADDRESS SPACE MANAGER TO LOAD THE EXTERNAL
   OBJECT'S LINKAGE TABLE TEMPLATE INTO THE PROCESS
   ADDRESS SPACE.

* 2. INITIALIZES A TEMPORARY VARIABLE (NEW$LINK$PTR) TO
   THE VALUE OF THE OBJECT'S LINKAGE POINTER.

* 3. APPENDS OBJECT_LINK TO THE END OF THE COMBINED
   LINKAGE TABLE.

* 4. DELETES THE LINKAGE TABLE TEMPLATE FROM THE PROCESS
   ADDRESS SPACE.

*******************************************************************************/

67 1 BUILD$OBJECT$LINK : PROCEDURE (BASE$ADDRESS, OBJECT$TYPE, 
                  SN$POINTER);

68 2 DECLARE RETURN$VALUE$PTR POINTER,
      RETURN$VALUE STRUCTURE ( 
          UNIQUE$ID BYTE, 
          BASE$ADDR ADDRESS), 
      BASE$ADDRESS ADDRESS, 
      OBJECT$TYPE BYTE, 
      SN$POINTER POINTER, 
      SYMBOLIC$NAME BASED SN$POINTER STRUCTURE ( 
          NAME (12) BYTE, 
          SIZE BYTE), 
      I BYTE;

/**** INITIALIZE RETURN$VALUE$PTR ****/

69 2 RETURN$VALUE$PTR = .RETURN$VALUE.UNIQUE$ID;
PL/M-86 COMPILER       LINKER MODULE

/*** APPEND A FILE TYPE OF TEMPLATE TO THE SYMBOLIC NAME ***/

70 2
I = SYMBOLIC$NAME.SIZE;

71 2
SYMBOLIC$NAME.NAME(I := I + 1) = 'T';

72 2
SYMBOLIC$NAME.NAME(I := I + 1) = 'M';

73 2
SYMBOLIC$NAME.NAME(I := I + 1) = 'P';

74 2
CALL ASH$MAKE$ACCESSIBLE (SN$POINTER, RETURN$VALUE$PTR);

/*** SET THE TEMPLATE BASE ADDRESS = FASE$ADDR OF THE TEMPLATE
    AND, NEW$LINK$PTR = FREE$LINK$TABLE ***/

75 2
TEMPLATE$BASE$ADDRESS = RETURN$VALUE$BASE$ADDR;

76 2
NEW$LINK$PTR = FREE$LINK$TABLE;

77 2
IF OBJECT$TYPE = TYPE$PROCEDURE THEN DO;

    /* IF THE OBJECT IS A PROCEDURE, THEN ITS SYMBOLIC NAME
        TABLE IS IN THE OBJECT CODE SEGMENT. */

79 3
NEW$LINK$TABLE.SNT$ADDRESS = TEMPLATE.SNT$OFFSET +
BASE$ADDRESS;

80 3
END; /* OF THE THEN CLAUSE */

81 2
ELSE DO;

    /* THE OBJECT IS DATA AND ITS SYMBOLIC NAME TABLE IS IN
        THE TEMPLATE */

82 3
NEW$LINK$TABLE.SNT$ADDRESS = NEW$LINK$PTR + TEMPLATE.SNT$OFFSET;

83 3
END; /* OF THE ELSE CLAUSE */
PL/M-80 COMPILER

LINKER MODULE

/** THE REST OF THE LINKAGE TABLE **/

NEW$TABLE$SIZE = TEMPLATE$SIZE;

DO I = 0 TO (TEMPLATE$SIZE - 5);
    NEW$TABLE$BODY(I) = TEMPLATE$BODY(I);
END;

FREE$TABLE = NEW$TABLE$PTR + NEW$TABLE$SIZE + 1;

CALL ASM$REMOVE$SEG (SN$POINTER);

END BUILD$OBJECT$LINK;

***************

*** LINKAGE$TABLE$ROUTINES PERFORMS THE FOLLOWING FUNCTIONS:
***
*** 1. DETERMINES IF A LINKAGE TABLE ALREADY EXIST FOR THE
***    EXTERNAL REFERENCE BEING LINKED.
***
***    A. IF NOT, LINKAGE$TABLE$ROUTINES INITIALIZES THE
***       LINKAGE ADDRESS TABLE ENTRY FOR THE OBJECT AND THEN
***       CALLS ON BUILT$OBJECT$LINK.
***
***    B. IF SO, LINKAGE$TABLE$ROUTINES SETS A TEMPORARY
***       VARIABLE (NEW$TABLE$PTR) EQUAL TO THE LINKAGE POINTER
***       VALUE FOR THE NEW OBJECT'S LINKAGE TABLE.
***
***************

LINKAGE$TABLE$ROUTINES: PROCEDURE (OBJECT$ADDR$NUMBER, PAGE$ADDR$,
                                      OBJECT$TYPE, SN$POINTER)
/** LINKAGE$TABLE$ROUTINES IS **/
PL/M-80 COMPILER

LINKER MODULE

92 2
DECLARE OBJECT$SEG$NUMBER BYTE,
BASE$ADDR ADDRESS,
OBJECT$TYPE BYTE,
SN$POINTER POINTER;

93 2
IF LINKAGE$ADDRESS$TABLE (OBJECT$SEG$NUMBER).VALID$EXIT <> VALID
THEN DO;

/**** THIS IS THE FIRST TIME THE OBJECT HAS BEEN REFERENCED
BY THE PROCESS AND THE LINKER MUST BUILD A LINKAGE TABLE
FOR THE OBJECT. ****/

95 3
LINKAGE$ADDRESS$TABLE (OBJECT$SEG$NUMBER).BASE$ADDR =
FREE$LINK$TABLE;
96 3
LINKAGE$ADDRESS$TABLE (OBJECT$SEG$NUMBER).VALID$EXIT = VALID;
97 3
CALL PUild$OBJECT$LINK (BASE$ADDR, OBJECT$TYPE, SN$POINTER);
98 3
END; /* OF THE THEN CLAUSE */
ELSE

/**** THE OBJECT ALREADY HAS A LINKAGE TABLE ****/

99 2
NEW$LINK$PTR =
LINKAGE$ADDRESS$TABLE (OBJECT$SEG$NUMBER).BASE$ADDR;
100 2
END LINKAGE$TABLE$ROUTINES;

/********************
** ACCESS$ENTRY$NAME$DATA PERFORMS THE FOLLOWING FUNCTIONS:
**
** 1. COMPUTES THE ADDRESS (TARGET$ADDRESS) IN THE EXTERNAL
**    OBJECT TO BE UTILIZED IN THE LINKING PROCESS.
**
101 1 ACCESS$ENTRY$NAME$DATA : PROCEDURE;
102 2 DECLARE I BYTE,
       FOUND BOOLEAN;
103 2 GET$NEXT$SN$ITEM : PROCEDURE;
104 3   SN$ADDRESS = SN$ADDRESS + FIXED$SN$OFFSET +
       (SN$ITEM.DESCRIPTOR AND SN$SIZE$MASK);
105 3   END GET$NEXT$SN$ITEM;
106 2   NAMES$MATCH : FUNCTION BOOLEAN;
107 3   DECLARE I BYTE,
       RESULT BOOLEAN;
108 3   RESULT = TRUE;
109 3   I = 0;
110 3   DO WHILE I < EN$BUFFER$SIZE AND RESULT = TRUE;
111 4     IF EN$BUFFER$NAME(I) <> SN$ITEM$NAME(I) THEN
112 4       RESULT = FALSE;
113 4     ELSE I = I + 1;
114 4   END; /* OF THE WHILE LOOP */
PL/M-80 COMPILER  LINKER MODULE

115 3 RETURN RESULT;
116 3 END NAMES$MATCH;

/**** BEGIN ACCESS$ENTRY$NAME$DATA ****/

117 2 FOUND = FALSE;
118 2 SN$ADDRESS = NEW$LINK$TABLE$SNT$ADDRESS;
119 2 DO WHILE NOT FOUND;
120 3 IF NAMES$MATCH THEN FOUND = TRUE;
122 3 ELSE CALL GET$NEXT$SN$ITEM;
123 3 END;
124 2 TARGET$ADDRESS = OBJECT.BASE$ADDRESS + SN$ITEM.ENTRY$POINT;
125 2 IF OBJECT$TYPE = TYPE$PROCEDURE THEN
126 2 IN$LINK$ADDRESS = NEW$LINK$PTR + SN$ITEM.LINK$OFFSET;
127 2 END ACCESS$ENTRY$NAME$DATA;

/**************************************************************/

*/ SNAP$THE$LINKS PERFORMS THE FOLLOWING FUNCTIONS:
*/
*/
*/ 1. SNAPS THE OUTGOING AND INCOMING LINKS FOR A PROCEDURE
*/ OBJECT.
*/
*/ 2. SNAPS THE OUTGOING LINK FOR A DATA OBJECT.
*/
**************************************************************/
SNAP$THE$LINKS : PROCEDURE;

IF OBJECT$TYPE = TYPE$PROCEDURE THEN DO;
  /* SNAP A LINK FOR AN EXTERNAL PROCEDURE */
  OUTGOING$LINK (0) = JUMP$TO;
  OUTGOING$LINK (1) = LOW (IN$LINK$ADDRESS);
  OUTGOING$LINK (2) = HIGH (IN$LINK$ADDRESS);

  IF INCOMING$LINK$LOAD$LP (0) = UNSNAPPED THEN DO;
    INCOMING$LINK$LOAD$LP (0) = LOAD$LP$INST;
    INCOMING$LINK$LOAD$LP (1) = LOW (NEW$LINK$PTR);
    INCOMING$LINK$LOAD$LP (2) = HIGH (NEW$LINK$PTR);
  END; /* OF THE IF INCOMING$LINK IS UNSNAPPED CLAUSE */

END; /* OF THE THEN CLAUSE */

ELSE DO;
  /* SNAP A DATA LINK */
  OUTGOING$LINK (0) = LOAD$POINTER;
  OUTGOING$LINK (1) = LOW (TARGET$ADDRESS);
  OUTGOING$LINK (2) = HIGH (TARGET$ADDRESS);
  OUTGOING$LINK (3) = RETURN$INST;

END; /* OF THE ELSE CLAUSE */

END SNAP$THE$LINKS;
PL/M-60 COMPILER       LINKER MODULE

******************************************************************************
* LINKER IS THE CONTROL MODULE CALLED TO PERFORM THE *
* LINKING PROCESS.                                      *
******************************************************************************

151 1 LINKER : PROCEDURE (LINK$PTR, SYM$NAME$OFFSET) ADDRESS;
152 2 DECLARE LINK$PTR POINTER,
       SYM$NAME$OFFSET INTEGER;
       /**** FIRST INITIALIZE THE LINKAGE POINTER AND SYMBOLIC NAME *
       OFFSET.  ***/
153 2 LINKAGE$POINTER = LINK$PTR;
154 2 SN$OFFSET = SYM$NAME$OFFSET;
155 2 CALL ACCESS$SYMBOLIC$NAME$data (LINKAGE$POINTER, SN$OFFSET);
156 2 CALL ASM$MAKE$ACCESSABLE (SN$BUFFER$POINTER, OBJECT$ID$POINTER);
157 2 CALL LINKAGE$TABLE$ROUTINES (OBJECT$UNIQUE$ID, OBJECT$ENTRY$ADDRESS,
       OBJECT$TYPE, SN$BUFFER$POINTER);
158 2 CALL ACCESS$ENTRY$NAME$data;
159 2 CALL SNAP$THE$LINKS;
161 2 RETURN OUT$LINK$ADDRESS;
161 2 END LINKER;
PL/M-80 COMPILER LINKER MODULE

/* INITIALIZES LINKER PERFORMS THE FOLLOWING FUNCTIONS:
* 1. Initializes the Linker.
*/

INITIALIZESLINKER: PROCEDURE (RET$VAL$PTR) PUBLIC;

DECLARE RET$VAL$PTR POINTER,
   RET$VALUE BASED RET$VAL$PTR STRUCTURE (LINKER$ADDRESS ADDRESS,
       LINK$ADDRESS$TABLE$BASE ADDRESS,
       LINK$ADDRESS$TABLE$ADDRESS ADDRESS),
       I BYTE;

OBJECT$ID$POINTER = .OBJECT.UNIQUE$ID;

DO I = 0 TO 15;
   LINKAGE$ADDRESS$TABLE (I).VALID$BIT = INVALID;
END;

FREE$LINK$TABLE = .LINKAGE$TABLE (∅);
SN$BUFFER$POINTER = .SN$BUFFER$NAME (∅);
EN$BUFFER$PTR = .EN$BUFFER$NAME (∅);

/**** WE RETURN TO PROCESS INITIALIZATION THE ADDRESS OF
   THE SUBROUTINE "LINKER", THE ADDRESS OF THE
   LINKAGE ADDRESS TABLE AND THE LINKAGE TABLE. ****/

RET$VALUE.LINKER$ADDRESS = .LINKER;
RET$VALUE.LINK$ADDRESS$BASE =
   .LINKAGE$ADDRESS$TABLE (∅).VALID$BIT;
RET$VALUE.LINK$ADDRESS$ADDRESS = .LINKAGE$TABLE (∅);
PL/M-80 COMPILER       LINKER MODULE

174  2       END INITIALIZE$LINKER;

175  1       END DLKR;

MODULE INFORMATION:

   CODE AREA SIZE   = 0519H   1305D
   VARIABLE AREA SIZE = 0492H   1172D
   MAXIMUM STACK SIZE = 0006H    6D
   595 LINES READ
   0 PROGRAM ERROR(S)

END OF PL/M-80-compilation
PL/M-80 COMPILER  ADDRESS SPACE MANAGER

ISIS-II PL/M-80 V3.1 COMPIILATION OF MODULE ASM
OBJECT MODULE PLACED IN :F1:ASM.OBJ
COMPILER INVOKED BY:  PLM80 :F1:ASM.SRC PAGELENGTH(38) TITLE('ADDRESS SPACE MANAGER')

1

ASM : DO;
/* DATE LAST EDITED : 4 AUGUST 1980 */

2 1 DECLARF LIT LITERALLY 'LITERALLY',
   TRUE LIT '01H',
   FALSE LIT '00H',
   SPACE LIT '00H',
   FORMFEED LIT '0CH',
   VALID LIT '01H',
   INVALID LIT '00H',
   POINTER LIT 'ADDRESS',
   INTEGER LIT 'ADDRESS',
   FUNCTION LIT 'PROCEDURE',
   BOOLEAN LIT 'BYTE';

3 1 DECLARE

   PRT$SIZE INTEGER,
   PRT (16) STRUCTURE ( 
       VALID$BIT BOOLEAN,
       NAME (12) BYTE,
       BASE$ADDR ADDRESS,
       FREE$MEMORY ADDRESS;

/*********************************************************************/
/*  EXTERNALLY DEFINED SYSTEM PROCEDURE DECLARATIONS */
PL/M-80 COMPILED  ADDRESS SPACE MANAGER

/*
   ***********************************************************************
   
   
   /*** OPEN$FILE OPENS A FILE ON DISK.  ***/
   
   OPEN$FILE : PROCEDURE (PTR$TO$FILENAME) EXTERNAL;
   DECLARE PTR$TO$FILENAME POINTER;
   END OPEN$FILE;

   /*** CLOSE$FILE CLOSES A FILE ON DISK.  ***/
   
   CLOSE$FILE : PROCEDURE EXTERNAL;
   END CLOSE$FILE;

   /*** READ$DISK READS 128 BYTES FROM A FILE ON DISK INTO A BUFFER
   STARTING AT LOCATION BUFFER$ADDR.  ***/
   
   READ$DISK : FUNCTION (BUFFER$ADDR) BOOLEAN EXTERNAL;
   DECLARE BUFFER$ADDR ADDRESS;
   END READ$DISK;

   /*** DISPLAY$CHAR OUTPUTS AN ASCII CHARACTER TO THE CRT.  ***/
   
   DISPLAY$CHAR : PROCEDURE (CHARACTER) EXTERNAL;
   DECLARE CHARACTER BYTE;
   END DISPLAY$CHAR;

   /*** DISPLAY OUTPUTS AN ASCII CHARACTER STRIGHT TO THE CRT.  ***/
   
   DISPLAY : PROCEDURE (STRING$ADDRESS) EXTERNAL;
   DECLARE STRING$ADDRESS ADDRESS;
   END DISPLAY;

   /*** OUTPUT$ADDR DISPLAYS A 2-BYTE VALUE ON THE CRT.  ***/
*/
PI/M-80 COMPILER
ADDRESS SPACE MANAGER

OUTPUT$ADDR : PROCEDURE (DEVICE, VALUE) EXTERNAL;
DECLARE VALUE ADDRESS,
DEVICE BYTE;
END OUTPUT$ADDR;

/*** CRLF GENERATES A CARRIAGE RETURN AND LINE FEED ON THE CRT. ***/

CRLF : PROCEDURE EXTERNAL;
END CRLF;

/*** END OF EXTERNAL SYSTEM DECLARATIONS. ***/

THE ADDRESS SPACE MANAGER */

/*** LOAD$OBJECT AND RELOCATE ARE INTERFACE ROUTINES
BETWEEN THE ADDRESS SPACE MANAGER AND THE CP/M OPERATING
SYSTEM. ***/

RELOCATE PERFORMS THE FOLLOWING FUNCTIONS:
1. CHANGES ALL RELATIVE ADDRESSES IN A PROCEDURE TO
ABSOLUTE ADDRESSES.

RELOCATE : PROCEDURE (OBJ$NAME$PTR, BASE$ADDRESS);
DECLARE OBJ$NAME$PTR POINTER,
OBJECT$NAME$BASED OBJ$NAME$PTR (12) BYTE,
TEMP$NAME$BUFFER (12) BYTE,
TEMP$NAME$PTR POINTER,
BASE$ADDR$ADDRESS ADDRESS,
FILE$POINTER$POINTER,
RELATIVEADDR BASED FILE$POINTER ADDRESS,
RELOC$BUFF$PTR POINTER,
RELOC$BUFFER (128) BYTE,
ADDRESS$VALUE BASED RELOC$BUFF$PTR ADDRESS,
NUM$OF$RELOC$BYTES INTEGER,
1 BYTE;

/* ................................................ . */

/*** LOAD$RELOC$BUFFER LOADS 128 BYTES OF RELOCATION
BITS INTO THE RELOCATION BUFFER. ***/

LOAD$RELOC$BUFFER : PROCEDURE;
DECLARE DUMMY BYTE;
DUMMY = READ$DISK (RELOC$BUFF$PTR);
END LOAD$RELOC$BUFFER;

/* ................................................ . */

/*** RELOC$BYTES RELOCATES EIGHT BYTES IN THE EXECUTABLE
OBJECT FILE. ***/

RELOC$BYTES : PROCEDURE (SUBSCRIPT);
DECLARE SUBSCRIPT BYTE,
BYTES$MASK BYTE,
LOOP BYTE;
BYTESMASK = 00H;
DO LOOP = 1 TO 8;

/* IF THE RELOCATION BIT IS 1, THEN RELOCATE */

IF (RELOC$BUFFER (SUBSCRIPT) AND BYTESMASK) <> 0 THEN
  RELATIVEADDR = RELATIVEADDR + BASEADDRESS - 100H;

/* NOW SHIFT THE BYTE$MASK BIT TO THE RIGHT AND INCREMENT THE FILE$POINTER. */

BYTESMASK = SHR (BYTESMASK, 1);
FILE$POINTER = FILE$POINTER + 1;

END; /* OF THE LOOP */

END RELOC$BYTES;

/* . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . */

/** BEGIN RELOCATION /**

/*** SET FILE$POINTER = THE BASE ADDRESS OF THE OBJECT FILE AND RELOC$BUFF$PTR TO POINT TO THE RELOCATION BUFFER. ALSO SET TEMPS$NAME$PTR TO POINT TO THE TEMPS$NAME$BUFFER. THE TEMPORARY NAME BUFFER WILL CONTAIN THE OBJECT NAME AND IS USED TO PREVENT SETTING THE OBJECT TYPE TO 'RIB' BY SETTING THE TYPE IN THE TEMPORARY BUFFER TO 'RIB' (RELOCATION BITS). ***/

FILE$POINTER = BASEADDRESS;
RELOC$BUFF$PTR = .RELOC$BUFFER(0);
PL/M-80 COMPILER    ADDRESS SPACE MANAGER

41 2 TEMP$NAME$PTR = .TEMP$NAME$BUFFER(0);
    /* NOW SET TEMP$NAME$BUFFER TO OBJECT$NAME */

42 2 DO I = 0 TO 11;
43 3 TEMP$NAME$BUFFER (I) = OBJECT$NAME (I);
44 3 END;

    /*** SET UP AND OPEN THE RELOCATION BITS FILE ***/

45 2 I = 0;
46 2 DO WHILE TEMP$NAME$BUFFER(I) <> ' . ';
47 3 I = I + 1;
48 3 END;

    /* SET FILE TYPE TO 'RLB' */

49 2 TEMP$NAME$BUFFER(I := I + 1) = 'R';
50 2 TEMP$NAME$BUFFER(I := I + 1) = 'I';
51 2 TEMP$NAME$BUFFER(I := I + 1) = 'F';

52 2 CALL OPEN$FILE (TEMP$NAME$PTR);

    /*** START THE RELOCATION ***/

53 2 I = 2;    /* INITIALIZE THE SUBSCIRPT TO 2 BECAUSE THE FIRST
54 2 CALL LOAD$RELOC$BUFFER;    /* INITIALIZE THE TWO BYTES OF THE RELOCATION BITS FILE CONTAIN
    ** EXTRACT THE SIZE OF THE RELOCATION BITS FILE ***/
PL/M-80 COMPILER
ADDRESS SPACE MANAGER

55 2
NUM$OF$RELOC$BYTES = ADDRESS$VALUE;
/
** RELOCATE 8 BYTES IN THE OBJECT FILE **/

56 2
DO WHILE I <= NUM$OF$RELOC$BYTES;

57 3
CALL RELOC$BYTES (I);
I = I + 1;

59 3
IF I = 128 THEN DO;
/* IF THE NUM$OF$RELOC$BYTES IS 128, THEN RELOCATION IS COMPLETE AND NO FURTHER COMPUTATIONS IS NECESSARY. */

61 4
IF NUM$OF$RELOC$BYTES <> 127 THEN
62 4
DO;
63 5
NUM$OF$RELOC$BYTES = NUM$OF$RELOC$BYTES - 126;
64 5
CALL LOAD$RELOC$BUFFER;
65 5
I = 0;
66 5
END; /* OF THE IF NUM$OF$RELOC$BYTES <> 127 CLAUSE */

67 4
END; /* OF THE IF I = 128 CLAUSE */

68 3
END; /* OF THE WHILE CLAUSE */
/
** THE RELOCATION IS COMPLETE--CLOSE THE RELOCATION BITS FILE. **/

69 2
CALL CLOSE$FILE;

70 2
END RELOCATE;
/
/* */
/* LOAD$OBJECT PERFORMS THE FOLLOWING FUNCTIONS: */
LOAD$OBJECT : FUNCTION (O$NAME$PTR) ADDRESS;

DECLARE O$NAME$PTR POINTER,
O$NAME$BASED O$NAME$PTR (12) BYTE,
BASE$ADDRESS ADDRESS,
1 BYTE;

/**** OPEN THE OBJECT FILE ****/

CALL O$FILE (O$NAME$PTR);

/**** SET BASE$ADDRESS = THE BASE LOCATION OF THE OBJECT AND LOAD THE OBJECT INTO MEMORY. ****/

BASE$ADDRESS = FREE$MEMORY;

DO WHILE READ$DISK (FREE$MEMORY) = TRUE;

/**** INCREMENT FREE$MEMORY AND LOAD ANOTHER 128 BYTES ****/

FREE$MEMORY = FREE$MEMORY + 128;

END;  /* OF THE WHILE CLAUSE */
PLM-80 COMPILER ADDRESS SPACE MANAGER

/**** NOW CLOSE THE OBJECT FILE ****/

78 2 CALL CLOSE$FILE;

/**** IF THE OBJECT WAS EXECUTABLE CODE, THEN PERFORM A RELOCATION ****/

79 2 I = 0;
80 2 DO WHILE OBJECT$NAME(I) <> ";
81 3 I = I + 1;
82 3 END;

83 2 IF OBJECT$NAME(I := I + 1) = 'C' AND
OBJECT$NAME(I := I + 1) = 'O' AND
OBJECT$NAME(I := I + 1) = 'M'

THEN CALL RELOCATE (OBJ$NAME$PTR, BASE$ADDRESS);

/**** RETURN-LIST : BASE$ADDRESS, FREE MEMORY ****/

85 2 RETURN BASE$ADDRESS;

86 2 END LOAD$OBJECT;

/*****************************/

* * COMPARE PERFORMS THE FOLLOWING FUNCTIONS:
* *
* 1. DETERMINES IF THE OBJECT NAME PASSED AS AN ACTUAL
* PARAMETER IS EQUAL TO PRT(PRT$INDEX).NAME.
*
*/

87 1 COMPARE : FUNCTION (OBJ$NAME$PTR, PRT$INDEX) BOOLEAN;
DECLARE OBJ$NAME$PTR POINTER,
OBJ$NAME BASED OBJ$NAME$PTR (12) BYTE,
PRT$INDEX BYTE,
CHECK$RESULT BOOLEAN,
(J,I) BYTE;

J = 0;
CHECK$RESULT = TRUE;

/*** PERFORM A BYTE BY BYTE COMPARISON OF OBJ$NAME AND
PRT(PRT$INDEX).NAME TO DETERMINE WHETHER THEY MATCH.
DO NOT LOOK PAST THE FILE TYPE FOR THE COMPARISON. ***/

DO WHILE CHECK$RESULT AND OBJ$NAME(J) <> '.');

IF OBJ$NAME(J) <> PRT(PRT$INDEX).NAME(J)
THEN CHECK$RESULT = FALSE;

J = J + 1;

END; /* OF THE WHILE CLAUSE */

/*** IF THE OBJECT$NAME WAS A MATCH, THEN CHECK FOR A
MATCH OF THE OBJECT TYPE. ***/

IF CHECK$RESULT THEN

DO I = (J + 1) TO (J + 3);

IF OBJ$NAME(I) <> PRT(PRT$INDEX).NAME(I) THEN
CHECK$RESULT = FALSE;

END;

RETURN CHECK$RESULT;

END COMPARE;
/************MAKE$ACCESSIBLE PERFORMS THE FOLLOWING FUNCTIONS:************

* 1. DETERMINES IF THE OBJECT IS ALREADY IN THE PROCESS
   REFERENCE TABLE (I.E., THE OBJECT HAS ALREADY BEEN
   MADE ACCESSIBLE).

* 2. IF NOT, LOADS THE OBJECT INTO MEMORY AND ENTERS IT IN
   THE PROCESS REFERENCE TABLE.

* 3. RETURNS TO THE POINT OF CALL A POINTER TO THE UNIQUE
   ID AND BASE ADDRESS OF THE OBJECT.

******************************************************************************/

103 1 ASM$MAKE$ACCESSIBLE : PROCEDURE (OBJ$NAME$PTR, RETURN$VALUE$PTR)
     PUBLIC;

104 2 DECLARE OBJ$NAME$PTR POINTER,
     OBJECT$NAME$ BASED OBJ$NAME$PTR (12) BYTE,
     RETURN$VALUE$PTR POINTER,
     RETURN$VALUE$ BASED RETURN$VALUE$PTR STRUCTURE (   
     UNIQUE$ID BYTE,   
     BASE$ADDR ADDRESS),
     FOUND BOOLEAN,
     OBJECT$SUBSCRIPT BYTE,
     I BYTE,
     J BYTE;

105 2 I = 0;
106 2 FOUND = FALSE;
PL/M-80 COMPILER ADDRESS SPACE MANAGER

/*** CHECK TO SEE IF OBJECT$NAME IS IN THE PRT. ***/

107  2  DO WHILE NOT FOUND AND I < PRT$SIZE;

108  3  IF PRT(I).VALID$BIT = VALID THEN

109  3  IF COMPARE (OBJ$NAME$PTR,I) THEN DO;

111  4  FOUND = TRUE;

112  4  OBJECT$SUBSCRIPT = I;

113  4  END;

114  3  I = I + 1;

115  3  END;  /* OF THE WHILE CLAUSE */

116  2  IF NOT FOUND THEN

117  2  DO;

118  3  I = 0;

119  3  /* FIND A FREE PRT ENTRY */

120  4  DO WHILE PRT(I).VALID$BIT;

121  4  I = I + 1;

122  4  END;

123  3  OBJECT$SUBSCRIPT = I;

124  3  /* LOAD THE OBJECT INTO THE ADDRESS SPACE AND SET UP A PRT ENTRY FOR THE OBJECT. */

125  3  PRT(OBJECT$SUBSCRIPT).VALID$BIT = VALID;

126  4  DO J = 0 TO 11;

127  4  PRT(OBJECT$SUBSCRIPT).NAME(J) = OBJECT$NAME(J);

128  4  END;
PI/μ-80 COMPILER        ADDRESS SPACE MANAGER

127  3                      PRT(OBJECT$SUBSCRIPT).BASE$ADDR = LOAD$OBJECT(OBJ$NAME$PTR);
128  3                      END; /* OF THE IF NOT FOUND CLAUSE */

/**** NOW SET UP THE RETURN VALUE STRUCTURE ****/

129  2                      RETURN$VALUE.UNIQUE$ID = OBJECT$SUBSCRIPT;
130  2                      RETURN$VALUE.BASE$ADDR = PRT(OBJECT$SUBSCRIPT).BASE$ADDR;
131  2                      END ASM$MAKE$ACCESSIBLE;

/*********************************************************
*/
*/ ASM$REMOVE$SEG PERFORMS THE FOLLOWING FUNCTIONS: */
*/
*/ 1. REMOVES AN OBJECT FROM A PROCESS ADDRESS SPACE BY */
*/    DELETING IT FROM THE PRT. */
*/*********************************************************/

132  1                      ASM$REMOVE$SEG : PROCEDURE (OBJ$NAME$PTR) PUBLIC;
133  2                      DECLARE OBJ$NAME$PTR POINTER,
                           OBJECT$NAME BASED OBJ$NAME$PTR (12) BYTE,
                           FOUND BOOLEAN,
                           OBJECT$SUBSCRIPT BYTE,
                           J BYTE,
                           I BYTE;

134  2                      I = 0;
135  2                      FOUND = FALSE;

/**** FIND THE OBJECT IN THE PRT ****/
PL/M-62 COMPILER ADDRESS SPACE MANAGER

136 2
137 3
138 3
140 4
141 4
142 4
143 3
144 3
145 2
146 2

DO WHILE NOT FOUND AND I < PRT$SIZE;

IF PRT(I).VALID$BIT = VALID THEN
    IF COMPARE(OBJ$NAME$PTR, I) THEN GO;
    FOUND = TRUE;
    OBJECT$SUBSCRIPT = I;
    END;

    I = I + 1;
    END; /* OF THE WHILE CLAUSE */

    /*** REMOVE THE OBJECT ***/

    PRT(OBJECT$SUBSCRIPT).VALID$BIT = INVALID;

    END ASM$REMOVE$SEG;

/*----------------------------------------------------------------------
 * INITIALIZE$ASM PERFORMS THE FOLLOWING FUNCTIONS:
 * * 1. INITIALIZES THE ADDRESS SPACE MANAGER DURING PROCESS INITIALIZATION.
 *----------------------------------------------------------------------*/

147 1
148 2
149 2
150 3
151 3
152 2

INITIALIZE$ASM : PROCEDURE PUBLIC;

DECLARE I BYTE;

DO I = 0 TO 15;
    PRT(I).VALID$BIT = INVALID;
    END;

FREE$MEMORY = .MEMORY;
PL/M-80 Compiler  
ADDRESS SPACE MANAGER

153 2  
PRT$SIZE = 16;

154 2  
END INITIALIZE$ASM;

/****  END OF ADDRESS SPACE MANAGER  ****/

/*****************************/

/**** THE FOLLOWING PROCEDURE DISPLAYS THE PROCESS REFERENCE TABLE AND 
IS NOT NECESSARY FOR THE PROPER EXECUTION OF THE ADDRESS SPACE 
MANAGER OR THE DYNAMIC LINKER--IT IS STRICTLY FOR THE PURPOSE 
OF THE DEMONSTRATION.  ****/

/*****************************/

/*****************************/

*  
* DISPLAY$PRT PERFORMS THE FOLLOWING FUNCTIONS:  
*  
* 1. DISPLAYS TPE PROCESS REFERENCE TABLE ON THE CRT.  
*  
*************************************/

155 1  
DISPLAY$PRT : PROCEDURE PUBLIC;

156 2  
DECLARE (I,J,K) BYTE;

/**** OUTPUT THE HEADING "PROCESS REFERENCE TABLE".  ****/

157 2  
CALL DISPLAY$CHAR (FORM$FEED);
PL/M-60 COMPILER

ADDRESS SPACE MANAGER

158 2
CALL CRIF;
159 2
CALL CRIF;
160 2
CALL CRIF;
161 2
CALL DISPLAY(.,'THE PROCESS REFERENCE TABLE',"$"));
162 2
CALL CRIF;
163 2
CALL DISPLAY(.,"-
164 2
CALL CRIF;
165 2
CALL CRIF;

/*** STEP THROUGH THE PROCESS REFERENCE TABLE AN ENTRY AT A
TIME. IF THE VALID$BIT IS VALID, THEN DISPLAY THE
ENTRY. ELSE DISPLAY 'NO ENTRY'. ***/

166 2
DO I = 1 TO PRT$SIZE;

/* FIRST DISPLAY THE PRT SUBSCRIPT (I) */

167 3
CALL DISPLAY(.," ");
168 3
IF I < 10 THEN CALL DISPLAY$CHAR(SPACE);
170 3
CALL OUTPUT$ADDR(?,DOBLE(1));
171 3
CALL DISPLAY(.,":","$"));

/* NOW DISPLAY THE PRT ENTRY ITSELF */

172 3
IF PRT(I - 1).VALID$BIT = INVALID THEN
173 3
CALL DISPLAY(.,'NO ENTRY',"$"));
174 3
ELSE DO;
175 4
CALL DISPLAY(.,'OBJECT NAME - ","$"));

/* DISPLAY THE OBJECT NAME */

176 4
J = 0;
177 4
DO WHILE PRT(I - 1).NAME(J) <> ";

/* DISPLAY THE FILE NAME */
PL/M-60 COMPILER
ADDRESS SPACE MANAGER

176 5 CALL DISPLAY$CHAR(PRT(I - 1).NAME(J));
179 5 J = J + 1;
180 5 END;
181 4 DO K = J TO (J + 3);

/* DISPLAY THE FILE TYPE */
182 5 CALL DISPLAY$CHAR(PRT(I - 1).NAME(K));
183 5 END;
184 4 CALL CRLF;
185 4 CALL DISPLAY(' ( " BASE ADDRESS - " , "$")');
186 4 CALL OUTPUT$ADDR(0, PRT(I - 1).BASE$ADDR);
187 4 END; /* OF THE ELSE CLAUSE */
188 3 CALL CRLF;
189 3 END; /* OF THE DO I = 0 TO PRT$SIZE LOOP */
190 2 END DISPLAY$PRT;
191 1 END ASM;

MODULE INFORMATION:

CODE AREA SIZE = 0628H 1576D
VARIABLE AREA SIZE = 01AEH 430D
MAXIMUM STACK SIZE = 000AH 10D
579 LINES READ
0 PROGRAM ERROR(S)
PL/M-80 COMPILER DISPLAY LINKAGE TABLE

ISIS-II PL/M-80 V3.1 COMPILED OF MODULE DISLT
OBJECT MODULE PLACED IN :F1:DISLT.OBJ
COMPILER INVOKED BY: PLMEC :F1:DISLT.SRC PAGELENGTH(38) TITLE(‘DISPLAY LINKAGE TABLE’)

1 DISLT : DO;

/* DATE LAST EDITED : 4 AUGUST 1984 */

/* THIS ROUTINE DISPLAYS THE LINKAGE ADDRESS TABLE AND LINKAGE TABLE ON THE CRT. */

2 1 DECLARE LIT LITERALLY ‘LITERALLY’,
POINTER LIT ‘ADDRESS’,
INTEGER LIT ‘ADDRESS’,
BOOLEAN LIT ‘BYTE’,
TRUE LIT ‘01H’,
FALSE LIT ‘00H’,
SPACE LIT ‘20H’,
FORMSFEED LIT ‘0CH’,
LITTLE$P LIT ‘70H’,
BAR LIT ‘7CH’,
PUSH$L LIT ‘05H’,
LOAD$LP LIT ‘01H’,
LOAD$PTR LIT ‘11H’,
JUMPS TO LIT ‘03H’,
INCOMING LIT ‘0EH’,
OUTGOING LIT ‘01H’,
VALID LIT ‘01H’,
INVALID LIT ‘00H’;

3 1 DECLARE COUNTER BYTE INITIAL (01H);
PLM-80 COMPILER     DISPLAY LINKAGE TABLE

/*******************************************************************************/
/*
* EXTERNALLY DEFINED SYSTEM PROCEDURE DECLARATIONS
*/
*******************************************************************************/

/*** DISPLAY OUTPUTS AN ASCII CHARACTER STRING TO THE CRT. ***/

DISPLAY : PROCEDURE (STRING$ADDRESS) EXTERNAL;
        DECLARE STRING$ADDRESS POINTER;
        END DISPLAY;

/*** OUTPUT$ADDR DISPLAYS A 2-BYTE VALUE ON THE CRT. ***/

OUTPUT$ADDR : PROCEDURE (DEVICE, VALUE) EXTERNAL;
        DECLARE DEVICE BYTE,
                VALUE ADDRESS;
        END OUTPUT$ADDR;

/*** DISPLAY$CHAR OUTPUTS AN ASCII CHARACTER TO THE CRT ***/

DISPLAY$CHAR : PROCEDURE (CHARACTER) EXTERNAL;
        DECLARE CHARACTER BYTE;
        END DISPLAY$CHAR;

/*** CRLF GENERATES A CARRIAGE RETURN AND LINE FEED ON THE CRT. ***/

CRLF : PROCEDURE EXTERNAL;
        END CRLF;

/*** END OF EXTERNAL SYSTEM DECLARATIONS ***/
*******************************************************************************/
PI/μ-80 COMPILER  DISPLAY LINKAGE TABLE

/**** USER Routines ****/

/*******************************************************************************/

/**** DISPLAY$HEX outputs a byte value in hexadecimal form to the CRT ****/

15 1 DISPLAY$HEX : PROCEDURE (VALUE);
16 2 DECLARE VALUE BYTE,
     TEMP$VAL BYTE;
17 2 TEMP$VAL = SHR((VALUE AND $FFH), 4);
16 2 IF TEMP$VAL < 10 THEN CALL DISPLAY$CHAR(TEMP$VAL + 36H);
20 2 ELSE CALL DISPLAY$CHAR(TEMP$VAL + 37H);
21 2 VALUE = VALUE AND $FF;
22 2 IF VALUE < 10 THEN CALL DISPLAY$CHAR(VALUE + 36H);
24 2 ELSE CALL DISPLAY$CHAR(VALUE + 37H);
25 2 CALL DISPLAY$CHAR ("H");
26 2 END DISPLAY$HEX;

/**** LINE$OF$Dots and LINE$OF$Dashes displays a line of dots or dashes on the CRT. ****/

27 1 LINE$OF$Dots : PROCEDURE;
28 2 CALL CRLF;
29 2 CALL DISPLAY (., BAR, ., .........., BAR, ";");
30 2 CALL CRLF;
31 2 END LINE$OF$Dots;
PL/M-80 COMPILER DISPLAY LINKAGE TABLE

32 1 LINE$OF$DASHES : PROCEDURE;
33 2 CALL CRIF;
34 2 CALL DISPLAY (., FAR, "-------------", FAR, ";");
35 2 CALL CRIF;
36 2 END LINE$OF$DASHES;

/*** PRINT$ADDRESS DISPLAYS 'ADDRESS' FOLLOWED BY VALUE /***

37 1 PRINT$ADDRESS : PROCEDURE (VALUE);
38 2 DECLARE VALUE ADDRESS;
39 2 CALL DISPLAY (., (ADDRESS - '.', ";");
40 2 CALL OUTPUT$ADDR (., VALUE);
41 2 CALL DISPLAY$CHAR (']));
42 2 END PRINT$ADDRESS;

/*** PRINT$VALUE PRINTS AN INTEGER ON THE CRT AND FILLs IN THE
   NUMBER OF NECESSARY SPACES TO KEEP THE OUTPUT UNIFORM. /***

43 1 PRINT$VALUE : PROCEDURE (DEVICE, NUMBER);
44 2 DECLARE NUMBER ADDRESS,
   DEVICE BYTE;
45 2 CALL OUTPUT$ADDR (DEVICE, NUMBER);
46 2 IF NUMBER < 10 THEN CALL DISPLAY(., ";", ";");
   ELSE IF NUMBER < 100 THEN CALL DISPLAY (. (SPACE, SPACE, SPACE, ";");
48 2 ELSE IF NUMBER < 1000 THEN CALL DISPLAY (. (SPACE, SPACE, ";");
50 2 ELSE IF NUMBER < 10000 THEN CALL DISPLAY$CHAR (SPACE);
PL/M-60 Compiler  Display Linkage Table

END PRINT$VALUE;

/*** DISPLAY$PROC$LINK OUTPUTS A SNAPPED PROCEDURE OUTGOING LINK
  TO THE CRT. ***/

55 1  DISPLAY$PROC$LINK : PROCEDURE (OUT$LINK$ADDR);

56 2  DECLARE OUT$LINK$ADDR POINTER,
        SNAPPED$LINK BASED OUT$LINK$ADDR STRUCTURE (
            JUMP$INST BYTE,
            INS$LINK$ADDR ADDRESS,
            FILLER ADDRESS);

57 2  CALL DISPLAY (.(',' BAR,' JUMP TO ','$'));
58 2  CALL PRINT$VALUE (0, SNAPPED$LINK, INS$LINK$ADDR);
59 2  CALL DISPLAY (.('SPACE, BAR, ' SNAPPED PROCEDURE LINK', '$'));
60 2  CALL PRINT$ADDRESS (OUT$LINK$ADDR);
61 2  CALL LINE$OF$DASHES;

62 2  END DISPLAY$PROC$LINK;

/*** DISPLAY$DATA$LINK OUTPUTS A SNAPPED OUTGOING DATA LINK TO THE
  CRT. ***/

63 1  DISPLAY$DATA$LINK : PROCEDURE (OUT$LINK$ADDR);

64 2  DECLARE OUT$LINK$ADDR POINTER,
        SNAPPED$LINK BASED OUT$LINK$ADDR STRUCTURE (
            LOAD$PTR$INST BYTE,
            DATA$ADDRESS ADDRESS,
            RETURN$INST BYTE);
CALL DISPLAY ("", BAR, " LOAD PIR ", ");
CALL PRINT$VALUE (0, SNAPPED$LINK.LINK$ADDR);
CALL DISPLAY (. (SPACE, BAR, " SNAPPED DATA LINK ", ");
CALL PRINT$ADDRESS (OUT$LINK$ADDR);
CALL LINE$OF$DOTS;
CALL DISPLAY (. (", FAR, " RETURN ", FAR, ");
CALL LINE$OF$DASHES;

END DISPLAY$DATA$LINK;

/*** DISPLAY$INCOMING$LINK OUTPUTS A SNAPPED INCOMING LINK TO THE CPT. /***/

DISPLAY$INCOMING$LINK : PROCEDURE (IN$LINK$ADDR);

DECLARE INS$LINK$ADDR POINTER,
SNAPPED$LINK BASED INS$LINK$ADDR STRUCTURE ( LOAD$IP$INST BYTE,
LINK$PTR ADDRESS,
JUMP$INST BYTE,
TARGET$ADDR ADDRESS);

CALL DISPLAY (. (", BAR, ", LOAD IP ", ");
CALL PRINT$VALUE (0, SNAPPED$LINK.LINK$PTR);
CALL DISPLAY (. (SPACE, BAR, " INCOMING LINK ", ");
CALL PRINT$ADDRESS (IN$LINK$ADDR);
CALL LINE$OF$DOTS;
CALL DISPLAY (. (", BAR, " JUMP TO ", ");
CALL PRINT$VALUE (0, SNAPPED$LINK.TARGET$ADDR);
CALL DISPLAY (. (SPACE, BAR, ");
CALL LINE$OF$DASHES;
PL/M-60 COMPLIER

DISPLAY LINKAGE TABLE

84   2   END DISPLAY$INCOMING$LINK;

/** DISPLAY$UNSNAPPED$LINK DISPLAYS AN UNSNAPPED LINK OF THE CRT. */
85   1   DISPLAY$UNSNAPPED$LINK : PROCEDURE (LINK$TYPE);
86   2   DECLARE LINK$TYPE BYTE;
87   2   CALL DISPLAY(.(' ', BAR, ' UNSNAPPED ', BAR, ' $'));
88   2   CALL CR LF;
89   2   IF LINK$TYPE = INCOMING THEN
90   2       CALL DISPLAY(.(' ', BAR, ' INCOMING LINK ', BAR, ' $'));
91   2   ELSE CALL DISPLAY(.(' ', BAR, ' OUTGOING LINK ', BAR, ' $'));
92   2   CALL LINE$OF$DASHES;
93   2   END DISPLAY$UNSNAPPED$LINK;

/** DISPLAY$SYM$NAME$TABLE DISPLAYS A DATA SYMPOIC NAME TABLE (WHICH WOULD BE STORED IN THE LINKAGE TABLE). */
94   1   DISPLAY$SYM$NAME$TABLE : PROCEDURE (START$OF$TABLE, END$OF$TABLE);
95   2   DECLARE START$OF$TABLE ADDRESS,
56     END$OF$TABLE ADDRESS,
57     SNT$PTR POINTER,
58     SNT BASED SNT$PTR STRUCTURE ( DESCRIPTOR BYTE,
59             LINK$OFFSET INTEGER,
60             ENTRY$POINT INTEGER,
61             NAME (1) BYTE),

1 BYTE;
PL/M-68 COMPILFR DISPLAY LINKAGE TABLE

96 2 SNT$PTR = START$CF$TABLE;
97 2 CALL CRLF;
98 2 CALL DISPLAY(., 'DATA SYMPOIC NAME TABLE', 'S');
99 2 CALL PRINT$ADDRESS (START$OF$TABLE);
100 2 CALL CRLF;
101 2 CALL CRLF;
102 2 DO WHILE SNT$PTR < END$OF$TABLE;
103 3 CALL DISPLAY(., DESCRIPTOR, 'S');
104 3 CALL DISPLAY$HEX (SNT.DESCRIPTOR);
105 3 CALL CRLF;
106 3 CALL DISPLAY(., LINK OFFSET, 'S');
107 3 CALL PRINT$value (0, SNT.LINK$OFFSET);
108 3 CALL CRLF;
109 3 CALL DISPLAY(., ENTRY POINT, 'S');
110 3 CALL PRINT$value (0, SNT.ENTRY$POINT);
111 3 CALL CRLF;
112 3 CALL DISPLAY(., NAME, 'S');
113 3 LO I = 0 TO ((SNT.DESCRIPTOR AND 1FF) - 1);
114 4 CALL DISPLAY$CHAR (SNT.NAME (I));
115 4 END;
116 3 SNT$PTR = SNT$PTR + 5 + (SNT.DESCRIPTOR AND 1FF);
117 3 CALL CRLF;
118 3 CALL CRLF;
119 3 END; /* OF THE WHILE CLAUSE */
PL/M-80 COMPILER    DISPLAY LINKAGE TABLE

120   2    CALL CRIF;
121   2    END DISPLAY$SYM$NAME$TABLE;

/**** DISPLAY$A$LINKAGE$TABLE OUTPUTS A LINKAGE TABLE TO THE CRT ****/
122   1    DISPLAY$A$LINKAGE$TABLE : PROCEDURE (LINKAGE$TABLE$BASE);
123   2    DECLARE LINKAGE$TABLE$BASE POINTER,
               TABLE BASED LINKAGE$TABLE$BASE STRUCTURE ( SIZE INTEGER,
               SMT$ADDRESS ADDRESS,
               BODY (1) BYTE),
               LINK$BODY$PTR POINTER,
               CHECK$BYTE BASED LINK$BODY$PTR BYTE;
124   2    CALL CRIF;
125   2    LINK$BODY$PTR = LINKAGE$TABLE$BASE + 4;
126   2    CALL DISPLAY(.,( LINKAGE TABLE ,"$"));
127   2    CALL OUTPUT$ADDR ($, COUNTER);
128   2    CALL DISPLAY(.,(L,LITTLESP ,"$"));
129   2    CALL PRINT$VALUE ($, LINKAGE$TABLE$BASE);
130   2    CALL DISPLAY$CHAR (');
131   2    CALL CRIF;
132   2    CALL LINE$OF$DASHES;
133   2    CALL DISPLAY(.,( PAR, SIZE - ,"$"));
134   2    CALL PRINT$VALUE ($, TABLE$SIZE);
135   2    CALL DISPLAY(.,( PAR, '"'));
136   2    CALL LINE$OF$DOTS;
PL/M-80 COMPILER  DISPLAY LINKAGE TABLE

137  2  CALL DISPLAY (.(' ', 'FAR', 'SNT - ', '$'));
138  2  CALL PRINT$VALUE (0, TABLE$SNT$ADDRESS);
139  2  CALL DISPLAY (.(' ', 'FAR', '$'));
140  2  CALL LINESOF$DASHES;

*/** DISPLAY THE BODY OF THE LINKAGE TABLE ***/

141  2  DO WHILE LINK$BODY$PTR < (LINKAGE$TABLE$BASE + TABLE$SIZE);
142  3  IF CHECK$BYTE = 0 THEN DO;
143  4  CALL DISPLAY$UNSNAPPED$LINK (INCOMING);
144  4  LINK$BODY$PTR = LINK$BODY$PTR + 6;
145  4  END;
146  4  ELSE
147  3  IF CHECK$BYTE = JUMP$TO THEN DO;
148  4  CALL DISPLAY$PROC$LINK (LINK$BODY$PTR);
149  4  LINK$BODY$PTR = LINK$BODY$PTR + 5;
150  4  END;
151  4  ELSE
152  3  IF CHECK$BYTE = LOAD$LP THEN DO;
153  4  CALL DISPLAY$INCOMING$LINK (LINK$BODY$PTR);
154  4  LINK$BODY$PTR = LINK$BODY$PTR + 6;
155  4  END;
156  4  IF CHECK$BYTE = LOAD$PTR THEN DO;
157  4  CALL DISPLAY$PASSTHROUGH$LINK (LINK$BODY$PTR);
158  4  LINK$BODY$PTR = LINK$BODY$PTR + 5;
159  4  END;
160  4  ELSE
161  3  IF CHECK$BYTE = PUSH$D THEN DO;
162  4  CALL DISPLAY$UNSNAPPED$LINK (OUTGOING);
163  4  LINK$BODY$PTR = LINK$BODY$PTR + 5;
164  4  END;
165  4  ELSE
166  3  IF TABLE$.SNT$ADDRESS = LINK$BODY$PTR THEN DO;
167  4  CALL DISPLAY$SYMBOL$NAME$TABLE (LINK$BODY$PTR,
PL/M-80 Compiler

DISPLAY LINKAGE TABLE

170 4
171 4
172 4
173 2
174 2
175 1
176 2
177 2
178 2
179 2
180 2
181 2
182 2
183 2
184 2
185 3

LINK$TABLE$BASE + TABLE$SIZE);
LINK$BODY$PTR = LINK$TABLE$BASE + TABLE$SIZE;
END;
END; /* OF THE WHILE LOOP */
CALL CRIF;
END DISPLAY$A$LINK$TABLE;

/* OUTPUT$THE$LINK$TABLE DISPLAYS THE COMBINED LINKAGE TABLE ON
THE CRT. IT DOES THIS BY SCANNING THE LINKAGE ADDRESS TABLE
AND OUTPUTING THE LINKAGE TABLE OF EACH VALID LINKAGE ADDRESS
TABLE ENTRY. */

OUTPUT$THE$LINK$TABLE : PROCEDURE (LINK$ADDR$TABLE$BASE) PUBLIC;

DECLARE LINK$ADDR$TABLE$BASE POINTER,
LINK$ADDR$TABLE BASED LINK$ADDR$TABLE$BASE (16) STRUCTURE
VALID$BIT BYTE,
BASE$ADDR ADDRESS),
I BYTE;

CALL DISPLAY$C$EAP (FORM$E$EET);
CALL CRIF;
CALL DISPLAY(' THE COMBINED LINKAGE TABLE', '$');
CALL CRIF;
CALL DISPLAY('----------------------', '$');
CALL CRIF;
CALL CRIF;

DO I = 0 TO 15;

IF LINK$ADDR$TABLE (I).VALID$BIT = VALID THEN DO;
PL/M-80 COMPILER  DISPLAY LINKAGE TABLE

187  4     CALL DISPLAY$A$LINKAGE$TABLE (LINK$ADDR$TABLE (I).BASE$ADDR);
188  4     COUNTER = COUNTER + 1;
189  4     END;

190  3     END;
191  2     END OUTPUT$THE$LINK$TABLE;
192  1     END DISLT;

MODULE INFORMATION:

    CODE AREA SIZE = 0668H  1640D
    VARIABLE AREA SIZE = 001DH  29D
    MAXIMUM STACK SIZE = 0000H  0D
    387 LINES READ
    0 PROGRAM ERROR(S)

END OF PL/M-80 COMPIILATION
PL/M-80 COMPILER SYSTEM ROUTINES

ISIS-II PL/M-80 V3.1 COMPILED OF MODULE COMMON
OBJECT MODUKE PLACED IN :FL:COMMON.OBJ
COMPIlER INVOKED BY: PLM dangerous TITLE('SYSTEM ROUTINES')

1 COMMON : DO;

2 DECLARE LIT LITERALLY 'LITERALLY',
   DCI LIT 'DECLARE',
   PROC LIT 'PROCEDURE',
   ADDR LIT 'ADDRESS',
   EXT LIT 'EXTERNAL',
   SPACE LIT '20H',
   TRUE LIT '01H',
   FALSE LIT '00H';

3 DCI CHAR BYTE PUBLIC,
   DECIMAL$BUFF (5) ADDR INITIAL (10000,1000,100,10,1),
   FILE$BLK$ADDR ADDR INITIAL (5CH),
   FILE$CONT$BLK BASED FILE$BLK$ADDR (33) BYTE;

4 MON1 : PROC (A,B) EXT;
5 DCL A BYTE,
   B ADDR;
6 END MON1;

7 MON2 : PROC (A,B) BYTE EXT;
8 DCL A BYTE,
   B ADDR;
9 END MON2;

10 BOOT: PROC EXTERNAL;
11 END BOOT;
/* PREADCHAR reads a character from the console and returns the ASCII value for this character to the point of call. It also assigns the ASCII value of the character to the public variable "CHAR". */

READCHAR : PROC BYTE PUBLIC;
12 1 CHAR = MON2(1,0);
13 2 RETURN CHAR;
14 2 END READCHAR;

/* DISPLAY outputs to the CRT a character string whose address is passed to it as a parameter. This string must be terminated by the ASCII code for a $. Note that if a "$" appears in the string to be outputted, DISPLAY will be terminated prematurely. A sample use of DISPLAY would be as follows:

CALL DISPLAY(('THIS STRING WILL BE PRINTED','$'));

*/

DISPLAY : PROC (A) PUBLIC;
16 1 DCL A ADDR;
17 2 CALL MON1(9,A);
18 2 END DISPLAY;

/* PRINT outputs a character string to the line printer. The format for PRINT is the same as for DISPLAY. */

PRINT : PROC (A) PUBLIC;
20 1 DCL A ADDR,
21 2 ITEM BASED A BYTE;
22 2 DO WHILE ITEM <> '$';
23 3 CALL MON1(5,ITEM);
A = A + 1;
END;

END PRINT;

/* CRLF CAUSES A CARRIAGE RETURN AND LINEFEED ON THE CRT. */

CRLF: PROC PUBLIC;
CALL DISPLAY(.('CDH,OAH,'$'));
END CRLF;

DISPLAY$ERROR: PROC (STRING$ADDR);
DCL STRING$ADDR ADDR;
CALL CRLF;
CALL DISPLAY(STRING$ADDR);
CALL EOT;
END DISPLAY$ERROR;

/* PAPER$ADVANCE CAUSES A CARRIAGE RETURN AND LINEFEED ON THE LINE PRINTER. */

PAPER$ADVANCE: PROC PUBLIC;
CALL PRINT(.('ODF,OAH,'$'));
END PAPER$ADVANCE;

/* DISPLAY$CHAR PRINTS A SINGLE CHARACTER ON THE CRT. IT IS PASSED THE ASCII CODE FOR THE CHARACTER TO BE DISPLAYED. */

DISPLAY$CHAR: PROC (CHARACTER) PUBLIC;
DCL CHARACTER BYTE;
CALL MON1(? ,CHARACTER);
END DISPLAY$CHAR;
PL/M-68 COMPILER    SYSTEM ROUTINES

/* PRINT$CHAR OUTPUTS A SINGLE CHARACTER TO THE LINE PRINTER. */

43 1 PRINT$CHAR: PROC (CHARACTER) PUBLIC;
44 2 LCI CHARACTER BYTE;
45 2 CALL MON1(5,CHARACTER);
46 2 END PRINT$CHAR;

/* OUTPUTSADDR PRINTS A DECIMAL NUMBER ON EITHER THE CRT OR
   THE LINE PRINTER DEPENDING ON THE 1ST PARAMETER IT IS PASSED
   (0 FOR CRT, 1 FOR LPT). THE SECOND PARAMETER IS THE SIGNED
   ADDRESS VARIABLE TO BE DISPLAYED. */

47 1 OUTPUTSADDR : PROC (DEVICE,VALUE) PUBLIC;
48 2 DCI DEVICE BYTE,
   VALUE ADDR,
   (I,J) BYTE,
   INTEGER$BUFF (6) BYTE,
   COUNT BYTE,
   FLAG BYTE;
49 2 IF DEVICE > 1 THEN DEVICE = 0;
51 2 FLAG = FALSE;
52 2 J = 0;
53 2 IF ROI(HIGH(VALUE),1) THEN DO;
55 3 INTEGER$BUFF(0)='-';
56 3 VALUE=-VALUE;
57 3 END;
58 2 ELSE INTEGER$BUFF(0)=SPACE;
59 2 DO I=0 TO 4;
60 3 COUNT = 30H;
61 3 DO WHILE VALUE >= DECIMAL$BUFF(I);
62 4 VALUE=VALUE-DECIMAL$BUFF(I);
63 4 COUNT=COUNT+1;
64 4 FLAG=TRUE;
END;
IF FLAG OR (I=4) THEN
INTEGER$BUFF(J:=J+1)=COUNT;
ELSE
INTEGER$BUFF(J:=J+1)=SPACE;
END;

DO CASE DEVICE;
DO;
DO I=0 TO 5;
IF INTEGER$BUFF(I) <> SPACE THEN
CALL DISPLAY$CHAR(INTEGER$BUFF(I));
END;
END;

DO I=0 TO 5;
IF INTEGER$BUFF(I) <> SPACE THEN
CALL PRINT$CHAR(INTEGER$BUFF(I));
END;

END; /* OF THE CASE STATEMENT */

END OUTPUT$ADDR;

/* OUTPUT$BYTE DISPLAYS A SIGNED BYTE VALUE AT EITHER THE CRT OR LINE PRINTER. */

OUTPUT$BYTE : PROC (DEVICE,VALUE) PUBLIC;
DCL DEVICE BYTE,
VALUE BYTE,
(I,J) BYTE,
INTEGER$BUFF (4) BYTE,
(COUNT,FLAG) BYTE;

IF DEVICE > 1 THEN DEVICE = 0;
FLAG = FALSE;
J = 0;
IF ROL(VALUE,1) THEN DO;
INTEGER$BUFF(\phi)='.';
VALUE = -VALUE;
END;
ELSE INTEGER$BUFF(\phi)=SPACE;

DO I = 2 TO 4;
COUNT = 3OH;
DO WHILE VALUE >= DECIMAL$BUFF(I);
VALUE = VALUE - DECIMAL$BUFF(I);
COUNT = COUNT + 1;
FLAG = TRUE;
END;
IF FLAG OR (I=4) THEN
INTEGER$BUFF(J:=J+1)=COUNT;
ELSE
INTEGER$BUFF(J:=J+1) = SPACE;
END;
DO CASE DEVICE;
DO; /* OUTPUT TO CRT */
DO I=0 TO 3;
IF INTEGER$BUFF(I) <> SPACE THEN
CALL DISPLAY$CHAR(INTEGER$BUFF(I));
END;
END;

DO; /* OUTPUT TO LPT */
DO I=0 TO 3;
IF INTEGER$BUFF(I) <> SPACE THEN
CALL PRINT$CHAR(INTEGER$BUFF(I));
END;
END;
END; /* OF THE CASE STATEMENT */
END OUTPUT$BYTE;

/* SET$FILENAME LOADS A FILE TO BE OPERATED ON IN THE CPM
FILE$CONTROL$BLOCK. THE NAME OF THE FILE IS DETERMINED BY THE
ADDRESS PASSED TO OPEN$FILE AS A PARAMETER. THE FILENAME MUST BE
OF THE FORM FILENAME$FILETYPE. THE FILENAME IS FROM ONE TO EIGHT
ALPHANUMERIC CHARACTERS WHILE THE FILETYPE IS FROM 0 TO THREE
ALPHANUMERIC CHARACTERS. A SAMPLE USE OF THIS ROUTINE WOULD BE:

CALL OPEN$FILE(.,(SAMPLE$ONE));

*/

SET$FILENAME : PROC (POINTER) PUBLIC;
LOC POINTER ADDR,
CHARACTER BASED POINTER BYTE,
(1,J) BYTE;

DO I=1 TO 11;
   FILE$CONT$BLK(I)=SPACE;
END;

I=0;
DO WHILE (CHARACTER <> '.' ) AND (I < 9);
FILE$CONT$BLK(I:=I+1) = CHARACTER;
POINTER = POINTER + 1;
END;
IF I > 9 THEN CALL DISPLAY$ERROR(,("IMPROPER FILENAME'','$'"))); ELSE
DO;
I=0;
POINTER=POINTER + 1;
DO WHILE (CHARACTER <> SPACE) AND (I < 12);
PI/M-80 COMPILER  SYSTEM Routines

139 4 FILE$CONT$BLK(I:=I+1) = CHARACTER;
140 4 POINTER = POINTER + 1;
141 4 END;
142 3 END;

143 2 END SET$FILE$NAME;

/* DISPLAY$FCB DISPLAYS THE NAME IN THE FILE CONTROL BLOCK IF THERE IS AN ERROR CONDITION IN OPEN OR CLOSE FILE. */

144 1 DISPLAY$FCB : PROC;
145 2 DCL NAME$BASE ADDR,
146 2 NAME$BASE (NAME$BASE (11) BYTE,
147 2 1 BYTE);
148 2 NAME$BASE = $DFH;
149 2 CALL CRIF;
150 2 CALL DISPLAY (.('THE FILE NAME IS : ', $''));
151 3 DO I = 0 TO 10;
152 2 CALL DISPLAY$CHAR (NAME(I));
153 3 END;
154 3 END DISPLAY$FCB;

/* OPENFILE OPENS THE FILE WHOSE NAME IS PASSED TO IT AS A FORMAL PARAMETER. THE FORMAT OF THE NAME IS DESCRIBED IN THE COMMENT FOR SET$FILE$NAME. */

155 2 OPEN$FILE : PROC (POINTER) PUBLIC;
156 2 LCL POINTER ADDR;
157 2 CALL SET$FILE$NAME$NAME(POINTER);
PL/M-80 COMPILER SYSTEM Routines

FILE$CONTRL Blk(32) = 0;
FILE$CONTRL Blk(0), FILE$CONTRL Blk(12), FILE$CONTRL Blk(15) = 0;

IF MON2(15, FILE$BLK$ADDR) = 255 THEN DO;

CALL DISPLAY$FCB;

CALL DISPLAY$ERROR( (COULD NOT OPEN FILE', 'r'));
END;

END OPEN$FILE;

/* CLOSEFILE CLOSES THE CURRENTLY OPENED FILE. */

CLOSE$FILE : PROC PUBLIC;

IF MON2(16, FILE$BLK$ADDR) = 255 THEN DO;

CALL DISPLAY$FCB;

CALL DISPLAY$ERROR( (COULD NOT CLOSE FILE', 'r'));
END;

END CLOSE$FILE;

/* READ$DISK READS A 128 BYTE BLOCK OF DATA FROM THE DISK AND LOADS IT INTO A BUFFER IN MEMORY WHOSE STARTING ADDRESS IS PASSED TO READ$DISK AS A FORMAL PARAMETER. NOTE THAT BEFORE ONE CAN READ FROM A FILE ON DISK YOU MUST FIRST OPEN THE FILE. READ$DISK RETURNS A TRUE IF THE FILE WAS SUCCESSFULLY READ AND A FALSE IF THE END OF THE FILE WAS REACHED. IT WILL TERMINATE PROGRAM EXECUTION IF AN ERROR IS DETECTED. */

READ$DISK : PROC (BUFFER$ADDR) BYTE PUBLIC;

DCL BUFFER$ADDR ADDR.
TEMP BYTE;

CALL MON1(26, BUFFER$ADDR);
PL/M-EE COMPILER SYSTEM Routines

174  2  TEMP = MFON2(20, FILE$BLK$ADDR);
175  2  DO CASE TEMP;
176  3  RETURN TRUE;  /* FILE SUCCESSFULLY READ */
177  3  RETURN FALSE;  /* READ PAST END OF FILE */
178  3  CALL DISPLAY$ERROR(.('FILE IMPROPERLY DEFINED', '$'));
179  3  END;  /* OF CASE */
180  2  END READ$DISK;

/* WRITE$DISK WRITES A 128 BYTE BLOCK OF DATA INTO A FILE. NOTE
THAT THE CURRENT FILE AS DETERMINED BY EITHER AN OPEN$FILE
OR CREATE$FILE MUST BE THE ONE YOU DESIRE TO WRITE TO.
WRITE$DISK WILL COMMENCE WRITING AT THE BEGINNING OF THE FILE
AND WILL DESTROY ANY EXISTING DATA AS IT WRITES. THE DATA
WRITE$DISK WILL OUTPUT IS DETERMINED BY THE ADDRESS OF THE
128 BYTE BUFFER PASSED TO WRITE$DISK AS A FORMAL PARAMETER.
WRITE$DISK WILL RETURN A TRUE IF THE WRITE WAS SUCCESSFUL
OTHERWISE IT WILL TERMINATE PROGRAM EXECUTION IF AN ERROR
OCCURS. */

181  1  WRITE$DISK : PROC (BUFFER$ADDR) BYTE PUBLIC;
182  2  DCI BUFFER$ADDR ADDR,
       TEMP BYTE;
183  2  CALL MFON1(26, BUFFER$ADDR);
184  2  TEMP = MFON2(21, FILE$BLK$ADDR) AND $3H;
185  2  DO CASE TEMP;
186  3  RETURN TRUE;  /* WRITE WAS SUCCESSFUL */
187  3  CALL DISPLAY$ERROR(.('ERROR IN EXTENDING FILE', '$'));
188  3  CALL DISPLAY$ERROR(.('DISK FULL', '$'));
189  3  CALL DISPLAY$ERROR(.('DIRECTORY FULL', '$'));
190  3  END;  /* OF CASE */
PL/M-80 COMPILER   SYSTEM Routines

191  2   END WRITE$DISK;

/* CREATE$FILE Initializes a new file as determined by the address
   of the filename passed to it as a formal parameter. */

192  1   CREATE$FILE : PROC (POINTER) PUBLIC;
193  2     DC1 POINTER ADDR;
194  2     CALL SET$FILENAME(POINTER);
195  2     IF MON2(22, FILE$BLK$ADDR) = 255 THEN
196  2       CALL DISPLAY$ERROR(.'DIRECTORY FULL', '$');
197  2     END CREATE$FILE;

/* DELETE$FILE deletes a file as determined by the address of the
   filename passed to it as a formal parameter. */

198  1   DELETE$FILE : PROC (POINTER) PUBLIC;
199  2     DC1 POINTER ADDR,
200     I BYTE;
201  2     CALL SET$FILENAME(POINTER);
202  2     I = MON2(19, FILE$BLK$ADDR);
203  2     END DELETE$FILE;
204  1     END COMMON;

MODULE INFORMATION:
PL/M-80 COMPILER SYSTEM Routines

CODE AREA SIZE = 05C5H 1477D
VARIABLE AREA SIZE = 0040H 64D
MAXIMUM STACK SIZE = 000AH 10D
374 LINES READ
0 PROGRAM ERROR(S)

END OF PL/M-80 COMPILATION
The System Routines invoke the CP/M operating system to perform their respective functions. This entails calling the subroutines monitor_1 (mon1) and monitor_2 (mon2). mon1 and mon2 very simply transfer control to the CP/M operating system via a jump vector located at 05F. The pseudocode for mon1 and mon2 is as follows:

```
mon1/mon2 : PROCEDURE (function_number, argument);

DECLARE function_number BYTE,
    argument ADDRESS,

    load the C register with function_number
    load the D & E register with argument
    jump to the CP/M entry point /* location 05F */

/* CP/M now performs the desired function as determined by the function_number and arguments */
    return byte value in the H & L reg /* mon2 only */
end mon1
```

The following is the assembly code for mon1 and mon2

```
ORG $100H
CSEG ;cseg tells the assembler to produce
    ; relocatable code
PUBLIC mon1, mon2
bdos equ $225E
mon1 : ; mon1 and mon2 are public labels
mon2 : 
    JMP bdos
END $100H
```
; DEMO displays a multiplication and addition table (in hex)
; of the numbers from 0 to 15

; PROCEDURE Demo,

DECLARE Demo ENTRY POINT,
Mult PROCEDURE EXTERNAL,
Header LATA EXTERNAL,
Display PROCEDURE EXTERNAL,

  title_pointer : POINTER,
title ARRAY of CHARACTERS BASED at title_pointer,

; /* end of declarations */

PROCEDURE Add (number),

  DECLARE number, i : BYTE,

  FOR i = 0 to 15,
     CALL Display.Hex_value (i + number),
  ENDFOR,

END Add,

PROCEDURE Build_table (routine),

  DECLARE routine : PROCEDURE,
    j : BYTE,

  FOR j = 0 to 15,
     CALL routine (i),
     CALL Display.Suffer (crlf),
  ENDFOR,
Demo.object_code

ENDFOR.

END Build_table,

/** begin demo **/
title_pointer = address of Header.title,
title = 'MULTIPLICATION',
CALL Display.Buffer (header),
CALL Build_table (Mult),
title_pointer = address of Header.title,
title = 'ADDICTION',
CALL Display.Buffer (header),
CALL Build_table (Add),
END Demo,
Demo.object_code

0103        POUTRE : DS 2
0105        TIPTR : TS 2
0107        NUMPER : DS 1
0108        I   : DS 1
0109        J   : TS 1
000D =      CR   : EQU 0DH
000A =      LF   : EQU 0AP
0E26 =      DELIM : EQU ','
012A 0D0A26  CRIF  : DB CR, LF, ','
012D 4D554CF449  MTRI : DS 'MULTIPLICATIONS'
011C 0044444954  ATITIE : DS 'ADDITION '

; Add displays the sum of number and 0 through 15

PAID :  

012F 210701  LXI H, NUMBRR  ; load the H & I reps w/ the address of number
012F 73      MOV M, E      ; move the parameter into number
012F 210501  LXI H, I      ; load the H & I reps w/ the address of 1
0132 760E    MVI M, 6      ; initialize i to 6
LOOP1 :  
0134 2F0F    MVI A, 15     ; load 15 into the accumulator
0176 210F01  LIX H, I      ; load the H & I reps w/ the address of i
0139 FE      CMP M    ; compare i and 15
013A DAF01   JC ENDFOR1  ; jump to endfor if i > 15
; demo.object_code

013D 1EFE1
014F 7F
0141 21FE1
0144 2C
0145 5F

LXI H, I
MOV A, M
LXI H, NUMBER
ADD M
MOV E, A

; dynamically link and call disp.ly.hex_value

0146 C5
0147 21FF1
014A E5
014B 2110F0
014E 09
014F E9

PUSH E
LXI H, RETAD1
PUSH H
LXI H, 19F
DAD E
PCFI

PETAD1:

PCP E

; restore the linkage pointer

0150 1E
0151 21BE1
0154 34
0155 C34F1

LXI H, I
INR M
JMP LOOP1

UND1:

; end of sum

; PROCEDURE build_table (routine)

0158 C9

BIND1:

; load the parameter into the H & I regs
Demo.object_code

015A 220301 SHID ROUTINE
015D 210901 LIY H, J
0160 3666 MVI M, F

LOOP2:
0162 3E0F MVI A, 15
0164 210961 LIY P, J
0167 1F CMP M
0168 DAF821 JC ENDPR2

;call routine ()

016F 212901 LIY E, J
016E 3E MOV E, M
016F C5 PUSH B
0170 2178F1 LIY H, PETAD2
0173 E5 PUSH H
0174 2AF301 LIYD POUTNE
0177 F9 PCHI

RETAD2:
0178 C1 POP B

;restore the linkage pointer

;dynamically link and call display.buffer (crlf)

0179 110A01 LIY D, CRIF
017C C5 PUSH E
017D 2166F1 LIY H, RETAD3
0180 E5 PUSH H
0181 211800 LIY H, IEH

;load the offset of the outgoing link

;load the P & I reps w/ the address of j
;initialize j to 0

;load 15 into the accumulator
;load the P & I reps w/ the address of j
;compare i and 15
;jump to endfor if j > 15

;load the P & I reps w/ the address of j
;move i into the E reg
;save the linkage pointer
;save the return address on the stack
;load the P & I reps w/ the address of routine
;jump to routine

;load the T & E reps w/ the address of crlf
;save the linkage pointer
;save the return address on the stack
;load the offset of the outgoing link
Demo object code

0184 09    DAT B
0185 09    PCHI

; compute Ip + outgoing link offset
; jump to the outgoing link

RETAD3 :    
0186 C1    POP E
0187 210001    LYO H, J
018A 34    INR M
018B C30001    JMP LOOP2

018E 09    RNDFR2 :    RET

; end of build_table

;...............

; /* begin demo */

START :

; dynamically link to header.title

01EF C5    PUSH B
0190 210001    LYO H, RETAD4
0193 E5    PUSP H
0194 210F001    LYO H, 0FH
0197 09    DAD B
0196 E9    PCLI

; save the linkage pointer
; save the return address on the stack
; load the offset of the outgoing link
; compute Ip + outgoing link offset
; jump to the outgoing link

RETAD4 :

0193 C1    POP B
019A FE    XCHC
019B 2200FF1    SHIFT TITPPR

; restore the linkage pointer
; move the address of header.title into F & I regs
; store header.title into title_pointer
Demo.object_code

019E 110D01 LXI D, MTITLE
C1A1 2A05C1 LNI TITPTR

LOOPS:
C1A4 1A LDAX D
C1A5 FE26 CPI DELIM
C1A7 CAB001 J7 FDNIPI
C1AA 77 MOV M, A
C1AF 23 INX H
C1AC 13 INY D
C1AD C3A401 JMF LOOP2

ENTLP1:

; dynamically link to header

C1F0 C5 PUSH E
C1F1 32EA01 ILY H, RETAD5
C1F4 F5 PUSH H
C1F5 2114F0 ILY H, 14F
C1FE 09 TAD B
C1FF F9 PCHI

RETLA:
C1EA C1 POP E

; dynamically link and call display.buffer (header), the address of
; header is in the D & E regs

C1FF C5 PUSH E
215C 2106F1 ILY F, RETAD6

; load the D & E regs w/ the address of mttitle
; load the E & L regs w/ title_pointer
; load the accumulator w/ a character from mttitle
; is that character the delimiter
; if so, jump to endloop1
; otherwise store the character in header.title
; increment title_pointer
; increment the address of mttitle
; and continue in loop

; save the linkage pointer
; save the return address on the stack
; load the offset of the outgoing link
; compute ly + offset of outgoing link
; jump to the outgoing link

; restore the linkage pointer

; save the linkage pointer
; save the return address on the stack
object_code

010F B5  FUSH H
010C 211F34  LVI H, 1EH  ;load the offset of the outgoing link
0103 09  DAD B  ;compute Ip + offset of outgoing link
0104 09  FCPL  ;jump to the outgoing link

RETDI:
0105 C1  POP B
0106 21CAFC  LXI H, CAE  ;load the offset of the outgoing link
0109 09  TAI E  ;compute Ip + offset for Multi
010A 0B  XCHG  ;store outgoing link address for Multi
              ;in the R & E reps
C10B CDF3F1  CAIL @DLTBL  ;and call build_table
              ;dynamically link to header.title

010C 0F  FUSH B  ;save the link& pointer
010C 21DFF1  LXI H, RETAD7  ;save the return address on the stack
0103 0F  FUSH F
0103 21F7FF  LXI H, FFF  ;load the offset of the outgoing link
0106 09  TAI E  ;compute Ip + outgoing link offset
0107 09  PICL  ;jump to the outgoing link

RETD7:
010F C1  POP B  ;restore the link& pointer
010F EF  XCHG  ;move the address of header.title into R & I reps
010A 223F31  SHID TITPTR  ;store header.title address into title_pointer
010D 111C61  LXI D, ATITLE  ;load the D & E reps w/ the address of atitle
010E D103F1  IDIF TIT PTR  ;load the R & I reps w/ title_pointer

LOOPS:

DEMO OBJECT CODE

OL3 1A
LDAX D ; load the accumulator w/ a character from atitle
OL4 FE26
CPI DELIMIT ; is that character the delimiter
OL6 C2FF1
J7 ENDF2 ; if so, jump to endloop2
OL8 2F
MOV M, A ; otherwise store the character in header.title
OL9 23
INX H ; increment title pointer
OLB 13
INX D ; increment the address of atitle
OLC C3EC1
 JMP LOOP4 ; and continue in loop

ENDF2 :

; dynamically link to header

OLF C5
PUSH P ; save the linkase pointer
OLF 21F9D1
LYI H, RETADD ; save the return address on the stack
OLF3 E5
PUSH H
OLF4 211400
LYI H, 16H ; load the offset of the outgoing link
OLF7 C9
TAP B ; compute Ip + outgoing link offset
OLF8 E9
FCPL ; jump to the outgoing link

RETADD :

OLF C1
POP P ; restore the linkase pointer

; dynamically link and call DISPLAY BUFFER (header), the address of ; header is in the F & E regs

OLFA C5
PUSH B ; save the linkase pointer
OLF 210422
LYI H, RETADD ; save the return address on the stack
OLF3 E5
PUSH H
OLF 211E77
LYI F, 1FE ; load the offset of the outgoing link
OFA 89
TAP B ; compute Ip + outgoing link offset
Demo.object_code

0263 89 PCPL ; jump to the outcome link
0264 C1 PETAL9 ; restore the linkage pointer
0265 212F11 IXI F, PADD ; load the H & L regs w/ the address of end
0268 PE XCHC ; move the address of end into the L & E regs
0269 CD59C1 CAIL BIDTPI ; and call build_table
026C C9 PEFT ; end of demo

; symbolic name table
; entry point into demo
027D 84 DESCC : DB 84
027E 4400 LINKC : DB 04, 00
027F FAA6 ENTCY : DB 5FH, 66
0282 44454D4F NAMPO : DB 'DEMO'

; entry for mult
0282 04 DESSC1 : DB 04H
0283 FAA6 LINK1 : DB 1AH, 00
0284 FAA6 ENTEY1 : DB 06, 66
0287 4D554C54 NAM1 : DB 'MULT'

; entry for header.title
Demo.object_code

C21F 8C  DESC2  : DL 0CF
C222 CF00  LINK2  : DL 0FE, 00
C222 0000  ENTRY2  : DL 00, 07
C224 4545414445  NAMP2  : DL 'HEADER:TITLE'

;entry for header

C230 86  DESC3  : DL 66H
C231 1400  LINK3  : DL 14H, 00
C233 0000  ENTRY3  : DL 00, 07
C235 4545414445  NAMP3  : DL 'HEADER'

;entry for displaynex_value

C238 17  DESC4  : DL 1CH
C23C 1900  LINK4  : DL 19H, 00
C23E 0000  ENTRY4  : DL 00, 00
C240 444353504C  NAMP4  : DL 'DISPLAY:NEX_VALUE'

;entry for display.buffer

C24A 6D  DESC5  : DL 06H
C251 1700  LINK5  : DL 17H, 00
C253 0000  ENTRY5  : DL 00, 07
C255 444353504C  NAMP5  : DL 'DISPLAY:BUFFER'

;end of symbolic name table

C262  ENT (100F)
; this is the template for demo

0100 ORG 0100H

0100 2300 SIZE : DF 35, 00
0102 0001 SNT : DB 6DH, 01H
BODY :

0104 00000000 DB 00, 00, 00, 00, 00, 00 ; incoming link into demo

010A D5 PUSH D
010B 110900 LXI D, 09
010E E7 RST 4 ; outgoing link for mult

010F D5 PUSH D
0110 111200 LXI D, 16
0113 E7 RST 4 ; outgoing link for header.title

0114 D5 PUSH D
0115 112300 LXI D, 35
0118 E7 RST 4 ; outgoing link for header

0119 D5 PUSH D
011A 112E00 LXI D, 46
011D E7 RST 4 ; outgoing link for display.hex_value

011E D5 PUSH D
011F 114300 LXI D, 67
0122 E7 RST 4 ; outgoing link for display.buffer

0123 END 0100H
; this is the relocation bits file for demo

<table>
<thead>
<tr>
<th>Offset</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>0100</td>
<td>ORG 0100H</td>
</tr>
<tr>
<td>0100</td>
<td>SIZE : DB 36, 06</td>
</tr>
<tr>
<td>0102</td>
<td>L0100 : DB 0100000B, 0000000B</td>
</tr>
<tr>
<td>0104</td>
<td>L0110 : DB 0000000B, 0000000B</td>
</tr>
<tr>
<td>0106</td>
<td>L0120 : DB 0000000B, 0000000B</td>
</tr>
<tr>
<td>0108</td>
<td>L0130 : DB 1000000B, 0000000B</td>
</tr>
<tr>
<td>010A</td>
<td>L0140 : DB 01010000B, 10000000B</td>
</tr>
<tr>
<td>010C</td>
<td>L0150 : DB 01000100B, 0100100B</td>
</tr>
<tr>
<td>010E</td>
<td>L0160 : DB 00001000B, 0101000B</td>
</tr>
<tr>
<td>0110</td>
<td>L0170 : DB 00000100B, 00100100B</td>
</tr>
<tr>
<td>0112</td>
<td>L0180 : DB 00000000B, 00100000B</td>
</tr>
<tr>
<td>0114</td>
<td>L0190 : DB 01000000B, 00010010B</td>
</tr>
<tr>
<td>0116</td>
<td>L01A0 : DB 01000000B, 00001010B</td>
</tr>
<tr>
<td>0118</td>
<td>L01B0 : DB 01000000B, 00001010B</td>
</tr>
<tr>
<td>011A</td>
<td>L01C0 : DB 00000000B, 00010000B</td>
</tr>
<tr>
<td>011C</td>
<td>L01D0 : DB 10000000B, 00100010B</td>
</tr>
<tr>
<td>011E</td>
<td>L01F0 : DB 01000000B, 00010010B</td>
</tr>
<tr>
<td>0120</td>
<td>L0200 : DB 01000000B, 00010000B</td>
</tr>
<tr>
<td>0122</td>
<td>L0220 : DB 00000000B, 01000000B</td>
</tr>
<tr>
<td>0124</td>
<td>ENI 0100H</td>
</tr>
</tbody>
</table>
; this is the header for the table generated by demo

0100 ORG $0100H

020D = CR : EQU $0D
020A = LF : EQU $0A
0226 = DElim : EQU '&'

HEADER :

0100 0D0A0DEA DB CR, LF, CR, LF,
0104 2020202020 DB '

TITLE1 :

0111 2020303134 DB ' 14 spaces '
011F 205441424C DB ' TABLES '
0127 0D0A DB CR, LF
0129 2020202020 DB '
0136 2D2D2D2D2D DB '----------------------'
014B 0D0A0DEA0DE DB CR, LF, CR, LF, CR, LF
0151 2030203031 DB ' 0 1 2 3 4 5 6 7 8 9 A B C D E F '
0161 0D0A0DEA DB CR, LF, CR, LF
0185 26 DB DElim

; end of header

0186 END $0100H
; this is the template for header

; entry point for header

; entry point for header.title

; end of symbolic name table

; find 0100H
; Mult displays the product of number and 2 through 15 on the
; CRT
;
PROCEDURE Mult (number),

DECLARE number, i : BYTE;

FUNCTION Product (x, y).

DECLARE x, y : BYTE;

sum, j : BYTE,

sum = 0,

FOR j = 1 to x,

sum = sum + y,

ENDFOR,

RETURN sum,

END Product.

; begin mult 
/

FOR i = 0 to 15,

CALL Display_hex_value (Product (i, number)),

ENDFOR,

END Mult.

; assembly language program for Mult

7107 OEC 7107F
Mult.object_code

; data declarations

I : IS 1
J : LS 1
SUM : DS 1
Y : IS 1
v : DS 1
NUMBER : DS 1
PARAMS : IS 

; product multiplies two number by repeated addition

PRODUCT:

; load the H & I regs w/ the address of parameters

210901 IXI H, PARAMS

; move the first parameter into the accumulator

3F PE MCV A, M

; store the first parameter into Y

206F1 STA Y

; increment the F & I regs to point to the second parameter

23 IXY H

; move the second parameter into the accumulator

3A4F1 MCV A, M

; store that parameter into y

20F2 STA Y

; move v into the accumulator

3F7F2 MVI A, V

; and initialize sum to v

326F1 STA SUM

; move 1 into the accumulator

3F61 MVI A, 1

; and initialize j to 1

324F1 STA J

LOOP1:
**mult.object_code**

0121 2A0601  
0124 210401  
0127 F4  
012E TA3C61  
012E TA5601  
012E 210701  
0131 46  
0132 320501  
0135 210401  
013F 34  
0139 C32141  

LDA X
LXI H, J
CMP M
LDA SUM
LXI H, Y
AIT M
STA SUM
LXI H, J
INR M
JMP LOOP1

; load the accumulator with x
; load the H & I regs w/ the address of j
; compare j to the value of x
; jump out of the loop if x < j
; otherwise move sum into the accumulator
; load the H & I regs w/ the address of y
; and add y to sum
; store the result in sum
; load the H & I regs w/ the address of j
; j = j + 1
; and jump to loop1

FNDFP1:

013C 210501  
013F 5E  
0140 C9  

LXI H, SUM
MOV E, M
RET

; load the H & I regs w/ the address of sum
; move sum into the E reg
; return sum to point of call, end of product

START:

; procedure mult

0141 210601  
0144 77  
0145 3E00  
0147 327301  

LXI H, NUMBER
MOV M, F
MVI A, 0
STA I

; load the H & I regs w/ the address of number
; move the parameter into number
; load 2 into the accumulator
; initialize i to 0

LOOP2:


Mult. object code

C14A 3E07 MVI A, 15 ; move 15 into the accumulator
C14C 21F31 IIX H, I ; load the H & I regs w/ the address of i
C14F EE CMP M ; compare i to 15
C150 DA7401 JC PNDPR2 ; jump to endfor if i > 15

; load the parameters i and number into params
C153 21F901 LVI H, PARAMS ; load the F & I regs w/ the address of params
C156 3AF301 LIA I ; load i into the accumulator
C159 77 MOV M, A ; move i into the first parameter
C15A 23 INX H ; increment the address of params
C15F 3AF601 LIA NUMBER ; load number into the accumulator
C15F 77 MOV M, A ; move number into the second parameter
C15F CDF401 CALL PRODCT

; dynamically link and call display.hex_value, the value
; product (i, number) is in the E reg
C162 C5 PUSF E ; save the linkage pointer
C163 21E001 LVI H, PNDAT1 ; save the return address on the stack
C166 E5 PUSF E
C167 21CA00 LVI H, OAH ; load the offset of the outgoing link
C168 G7 FAT E ; compute lp + outgoing link offset
C16F E9 PCFI ; jump to the outgoing link

PNDAT1:
C16C C1 FOE P ; restore the linkage pointer
C16D 21F31 IIX H, I ; load the H & I regs w/ the address of i
C170 74 INP M ; i = i + 1
C171 C34AV1 JMP LOOP2 ; and jump to loop2
MULT OBJECT CODE

0174 09          ENTP? : BFT ;end of mult

;........................................

;symbolic name table

;entry point into mult

0175 04          DESCP : DB 04
0176 0400        LINKP : DI 04, 00
0177 4100        ENTPY : DP 41H, 00
017A 4D554C54     NAM?P : DE 'MULT'

;entry for disp.ly.hex_value

017E 10          DESCP1 : DB 10H
017F C400        LINKP1 : DI C4H, 00
0181 0000        ENTPY1 : DI 00, 00
0183 444953504C   NAM?P1 : DI 'DISPLY:HEX_VALUE'

;end of symbolic name table

0193          END 01:0FH
; this is the template for mult

0100  ORG 0100H

0100  0F00  SIZE : DB 15, 00
0102  75E2  SNT : DE 75H, 02
BOLY :

0104  00000000  DB 00, 00, 00, 00, 00, 00 ; incoming link into mult

010A  D5  PUSH D ; outgoing link for display_hex_value
010B  110960  LXI D, 09H
010E  E7  RST 4

010F  END 0100H
; this is the relocation bits file for mult

org 0180h

0180 1100  size: db 17, 66
0180 4000  loc00 : db 01000000b, 00000000b
0180 9421  loc10 : db 10000100b, 00100010b
0180 2449  loc20 : db 00100100b, 01001001b
0180 1224  loc36 : db 01001001b, 01001001b
0180 2084  loc48 : db 01000000b, 10000100b
0180 4900  loc50 : db 01001001b, 00001001b
0180 6802  loc66 : db 10001001b, 00001001b
0180 20  loc70 : db 00100000b

218

0111  end 0180h
; Display outputs either a byte value in hexadecimal form
; (Hex_value) or an ASCII character string (Buffer)

PROCEDURE Disply,

DECLARE Hex_value ENTRY POINT,
   Buffer ENTRY POINT,

/* end of declarations */

/* Print displays an ASCII byte on the CRT */

PROCEDURE Print (ascii_byte),
   DECLARE ascii_byte : BYTE,
   OUTPUT (ascii_byte),
   END Print,

/* Hex_value prints the hexadecimal value of the parameter 'a_byte' on the CRT */

PROCEDURE Hex_value (a_byte),
   DECLARE a_byte, temp : BYTE,

/* Print_hex displays the hex value of a nibble on the CRT */

PROCEDURE Print_hex (nibble),

/* if nibble is less then 10, then print a digit,
   otherwise print the hex value A,B,C,D,E, or F */

IF nibble < 10 then CALL Print (nibble + 30H),
ELSE CALL Print (nibble + 37H);
Display.object_code

END Print_hex,

/* begin hex_value */

temp = SHIFT_RIGHT_4 (a_byte AND $FF),
CALL Print_hex (temp),
CALL Print_hex (a_byte AND $FF),
CALL Print (space).

END Hex_value,

/* Buffer displays the contents of an ASCII string on the CRT */

PROCEDURE Buffer (string_pointer),

DECLARE string_pointer : POINTER,
string_byte BYTE PASET at string_pointer.

DO WHILE string_byte <> delimiter,
CALL Print (string_byte),
string_pointer = string_pointer + 1,
ENDWHILE;

END Buffer,

END Display,
Display.object_code

; assembly language program for Display

0100 ORG 100H
0100 C31561 JMP START

; DATA DECLARATIONS

0143 NIBBLE : DS 1
0144 APYTE : DS 1
0145 TEMP : DS 1
0146 XTRIP : DS 2
0147 SBYTE : DS 1

0126 = DEIIM : EQU 'x'

; Print outputs the contents of the X register to the CRT

PRINT:

0169 E5 PUSH H ; save the registers
016A C5 PUSH B
016F FE PUSH PSW
01FC CEF2 MVI C, 02H ; tell the operating system to print
01F6 CD0500 CALL 05H ; call the opsys print routine
disply.object_code

0111 F1               POP PSW         ;restore the registers
0112 C1               PCP E
0113 E1               POP H
0114 C9               RFI

START:

; PROCEDURE Print_hex

PRTHEX:

0115 210301           LDI H, NIBBLE    ;load the H & I regs w/ the address of nibble
0116 73                MOV M, E        ;move the parameter into nibble
0119 7E                MOV A, M        ;move nibble into the accumulator
011A FE0A              CPI 10          ;compare nibble to 10
011C E22001            JNC LABEL1       ;if nibble >= 10 then jump to label1

011F 210301           LDI H, NIBBLE    ;load the H & I regs w/ the address of nibble
0122 3E3F              MVI A, 36H       ;move 36H into the accumulator
0124 8E                ADD M           ;add nibble to 36H
0125 5F                MOV E, A        ;move the result into the E reg
0126 CD0001            CALI PRINT      ;and call print
0129 C33601            JMP LABEL2      ;skip the ELSE clause

LABEL1:

012C 210301           LDI H, NIBBLE    ;load the H & I regs w/ the address of nibble
012F 3E3F              MVI A, 37H       ;move 37H into the accumulator
0131 FC                ADD M           ;add nibble to 37H
0132 5F                MOV E, A        ;move the result into the E reg
Display object_code

0133 CD09 01 CALL PRINT ; and call print
0136 C9 LABEL? : RET ; return to the point of call

; PROCEDURE Hex_value (a_byte),

LEXVAL :
0137 2145 01 LXI H, ABYTE ; load the H & L regs w/ the address of a_byte
0139 73 MOV M, E ; move the actual parameter into a_byte
013B 7E MGV A, M ; move a_byte into the accumulator
013C 66 0E ANI 012E ; AND a_byte with 1FB
013E 0F RRC ; shift the result right 4 bits
013F 0E RRC
0140 0F PPC
0141 0E RRC
0142 2145 01 LXI F, TEMP ; load the H & L regs w/ the address of temp
0144 77 MOV M, A ; move the result into temp
0146 5E MOV E, M ; move temp into the E reg
0147 CD15 01 CALL PRTHEX ; and call Print_hex
014A 2104 01 LXI H, ABYTE ; load the H & L regs w/ the address of a_byte
014D 7E MOV A, M ; load the accumulator with a_byte
014E 66 0E ANI 0F01 ; AND a_byte with 0FH
0150 5F MOV E, M ; move the result into the E reg
0151 CD15 01 CALL PRTHEX ; and call Print_hex
Display.object_code

0154 1E20 MVI E, 2EH ;move ASCII space into the E reg
0156 CD0901 CALL PRINT ;and call Print
0159 C9 RET ;end of Hex_value

;..............................................................

; PROCEDURE Buffer (string_pointer)

BUFFER:
015A FB ACHG ;move the parameter into the H & I regs
015B 220601 SHID STAPPTR ;and store it in string_pointer

WHILE:
0152 2A0601 LHI STAPPTR ;load string_pointer into the H & I regs
0161 7E MOV A, M ;move string_byte into the accumulator
0162 FE26 CFI DELIM ;compare string_byte with the delimiter
0164 CA7501 JZ ENDFWHL ;jump to ENDFWHL if string_byte = delimiter
0167 5E MOV E, M ;else move string_byte into the E reg
0165 CD0901 CALL PRINT ;and call Print
0166 2A0601 LHI STAPPTR ;load string_pointer into the H & I regs
016F 23 INX H ;increment string_pointer
016F 220601 SHIF STAPPTR ;and store the result
0172 C35E01 JMP WHILE ;continue in the WHILE loop

0175 C9 ENDFWHL : RET ;end of Buffer

;..............................................................

;symbolic name table
Dispaly.object_code

;entry point for Hex_value

0176 09                       DESC0  : DB 09
0177 440C                     LINK0 : DB 04, 0C
0179 3700                     ENTRY0 : DS 37H, 00
017B 4F455555F56              NAME0 : DB 'HEX_VALUE'

;entry point for Buffer

0184 06                       DESC1  : DB 06
0185 440C                     LINK1 : DB 04, 0C
0187 5A00                     ENTRY1 : DB 5AH, 00
0189 42555464645              NAME1 : DL 'BUFFER'

;end of symbolic name table

;end of Dispaly

018F                           END 016EH
; this is the template for display

ORG 0100H

SIZE : DB 16, 00
SRT : DB 7EH, 00

DE 00, 00, 00, 00, 00, 00 ; incoming link for hex_value
LB 00, 00, 00, 00, 00, 00 ; incoming link for buffer

END 0100F
; this is the relocation bits file for display

ORG $0100H

$0100 1100 SIZE : DB 17, 68

$0102 4000 LE16: DB $1000000B, $0000000B
$0104 0204 L0110: DB $0000001B, $0000010B
$0106 0124 L0120: DB $1000001B, $0001010B
$0108 0066 LF136: DB $0001000B, $0000010B
$010A 1090 L0140: DP $0010000B, $1001000B
$010C 2109 L0150: DP $0010001B, $0001001B
$010E 444E LF160: DP $0001000B, $1010000B
$0110 00 L0170: DP $0100000B

$0111 END $0100H
; SUM adds the bytes of the external data structure ARRAY
; and displays the result on the CPT

; PROCEDURE Sum,
DECLARE Sum ENTRY POINT,
   Array DATA EXTERNAL,
   Display PROCEDURE EXTERNAL.

   result : BYTE,
   array_pointer : POINTER,
   data_array EASIER at array_pointer STRUCTURE of
   number_of_bytes : BYTE,
   data : ARRAY of BYTES,
   END,

   i : BYTE,

   " end of declarations "/
array_pointer = address of array,
result = 0,

FOR i = 1 to data_array.number_of_bytes,
   result = result + data_array.data (i),
ENDFOR,

CALL display.buffer ('The sum of the data array is ', result),
CALL display.hex_buffer (result),
Sum.object_code

; /w generate a carriage return and line feed */
; 
; CAIL display.buffer (CR, IF, 'A'),
; CAIL display.buffer ("End of Sum", 'A');
; END Sum.

; assembly language program for Sum

0100 ORG $100H
0100 C33401 JMP START

DATA DECLARATIONS

0103 RESULT : DS 1
0104 I : DS 1
0105 POINTER : DS 2

00FD = CR : EQU 0Dh
00FA = LI : EQU 0Ah

0127 544652053 HEADING : DB 'The sum of the data array is &'
0125 4544264F ENDING : DB 'End of Sum &'
0131 0D0A26 CRIF : DB CR, IF, 'A'

START :

; dynamically link to array to get the value of array_pointer
sum.object_code

0134 C5  PUSH B ;save the linkage pointer
0135 21A501  LXI H, RETAD1 ;save the return address on the stack
0138 B5  PUSH P
0139 210400  LYI H, $00H
013C 09  DAD E
013D 89  PCHI

RETAD1:

013E C1  POP B ;restore the linkage pointer
013F 8E  XCHG ;move array_pointer into the H & I regs
0140 225501  SHIF POINTER ;store array_pointer
0143 3E00  MVI A, 0 ;set the accumulator to 0
0145 210301  LXI H, RESULT ;load the H & I regs w/ the address of result
0148 7F  MOV M, A ;initialize result to 0
0149 210401  LXI H, I ;load the H & I regs with the address of i
014C 3601  MVI M, 1 ;initialize 1 to 1

LOOP:

014E 2A501  LHLH POINTER ;load the H & I regs with array_pointer
0151 7E  MOV A, M ;move number_of_bytes into the accumulator
0152 210401  LXI H, I ;load the H & I regs w/ the address of i
0155 FE  CMP M ;compare i and number_of_bytes
0156 DA7301  JC FNDFOR ;jump to endfor if i < number_of_bytes
0159 210301  LXI H, RESULT ;load the H & I regs w/ the address of result
015C 7E  MOV A, M ;move result into the accumulator
015D 210401  LXI H, I ;load the H & I regs w/ the address of i
sum.object_code

0160 5E MOV E, M ;move 1 into the E reg
0161 160F MVI D, E ;clear the D reg
0163 2A05F1 LHLS POINT ;move array_pointer into the H & I regs
0166 19 DAD D ;compute the address of data_array.data (1)
0167 E6 ADD M ;add data_array.data (1) to result
0168 2103F1 LSIH H, RESULT ;load the H & I regs w/ the address of result
016B 77 MOVM, A ;store the accumulator in result
016C 2104F1 LSIV H, I ;load the H & I regs w/ the address of i
016F 34 INRM ;increment i
0170 C34F01 JMP LOOP ;jump to the start of the loop

PNDYCF : ;dynamically link and call display.buffer

0173 2107F1 LSIH H, HEADER ;load the H & I regs w/ the address of header
0176 EB VCPG ;move the address of header into the D & B regs
 ;to pass it as a actual parameter

0177 C5 PUSH R ;save the linkage pointer
017E 2101F1 LSIV H, FRTAUD ;save the return address on the stack
017F F5 PUSH F
017C 2113F0 IXY H, FFH ;load the offset of the outgoing link
017F 69 DAF F ;compute 1x + outgoing link offset
0180 E9 PCHI ;jump to the outgoing link

FRTAUD : ;restore the linkage pointer

0181 C1 POF F
sum.object_code

; dynamically link and call disp.ly.hex_value

O192 210301   LXI H, RESULT       ;load the H & I regs w/ the address of result
O185 5E         MVI E, M           ;move result into the E reg as a parameter
O186 1600       MVI D, O           ;clear the D reg
O188 C5         PUSH B             ;save the linkage pointer
O189 219201   LVI H, RETAD3       ;save the return address on the stack
O190 C5         PUSH H             ;load the offset of the outgoing link
O191 C9         DAD B              ;compute Lp + outgoing link offset
O190 E0         PCHI               ;jump to the outgoing link

RETAD3:
O192 C1         POP B              ;restore the linkage pointer

; dynamically link and call disp.ly.buffer

O193 213101   LXI H, CRIF         ;load the H & I regs w/ the address of crlf
O196 FB         XCHG               ;and pass it to disp.ly.buffer
O197 C5         PUSH B             ;save the linkage pointer
O198 21A101   LXI H, RETAD4       ;save the return address on the stack
O199 C5         PUSH H             ;load the offset of the outgoing link
O19C 21FF0C   LVI H, OPH          ;compute Lp + outgoing link offset
O19F C9         LAD B              ;jump to the outgoing link
O1AF E3         PCHI               ;restore the linkage pointer

RETAD4:
O1A1 C1         POP B              ;restore the linkage pointer
; dynamically link and call disp.ly.buffe r

01A2 212F01 LXI H, ENDING ; load the H & I regs w/ the address of endin g 
01A5 EF XCHG ; and pass it to disp.ly.buffe r

01A6 C5 PUSH B ; save the linka ge pointer
01A7 21FF01 LXI H, RETAD5 ; save the return address on the stack
01AA E5 PUSH H
01A4 210F00 LXI H, 0FH ; load the offset of the ou tgoing link
01AE 09 DAE B ; compute Lp + outgoing link offset
01AF E9 PCHI ; jump to the outgoing link

RPTAD5 :
01F0 C1 POP B ; restore the linka ge pointer
01B1 C9 RET ; end of sum

; symbolic name table
; entry point into sum

01B2 03 DEFC0 : DB 07
01F3 6400 LINS0 : DB 04, 00
01F5 0000 ENTF0 : DP 04, 00
01B7 555555 NAME0 : DB "SUM"

; entry for array
**Sum. object_code**

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<th>Description</th>
<th>Address</th>
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;entry for disp.ly.buffer

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;entry for disp.ly.hex_value

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;end of symbolic name table
; this is the template for sum

0100
ORG 0100H

0100 1900 SIZE : DB 019H, 00
0102 E200 SNT : DB FE2H, 00
BODY :

0104 00000000 DB 00, 00, 00, 00, 00, 00

; incoming link for sum

010A D5 PUSH D
010B 110600 IXI D, 0000H
010F FF RST 4

; outgoing link to array

010F D5 PUSH D
0110 111200 IXI I, 1E
0113 FF PST 4

; outgoing link to displ.buffer

0114 D5 PUSH D
0115 112402 IXI I, 36
0118 FF RST 4

0119 END 0100H
; this is the relocation bits file for sum

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<td>0100 1900</td>
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<td>LO100 : DB 01000000F, 0000000B</td>
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<td>0106 8000</td>
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<td>L01A0 : DB 00010000F, 10000000F</td>
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<td>0118 FF</td>
<td>L01F0 : DB 00000000F</td>
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<td>0119</td>
<td>END 0100H</td>
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</table>
;this is the external data structure array

0100  ORG 0100H

0100  0A01020304  ARRAY : DB 10, 01, 02, 03, 04, 05
0100  0A0706091B  DB 0A, 07, 06, 09, 1BH

010B  END 0100H
Array, template

;this is the template for array

0100     OPC 0100H
0100 00FF  SIZE : DB 14, 00
0102 0400  SNT : DB 04, 00

BODY :   ;array's symbolic name table

0104 85  DESC : DB 65H
0105 0000  LNF : DB 00, 00
0107 0000  FNTFY : DB 00, 00
0109 4152524159  NAME : DB 'ARRAY'

010E     END 0100H
## Multiplication Tables

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THE COMINEL LINKAGE TABLE

LINKAGE TABLE 1 (Ip = 7632) (DEMO)

| SIZE - 35 |
| SNT - 8968 |
| UNSNAPPED INCOMING LINK |
| JUMP TO 7117 | SNAPPED PROCEDURE LINK (ADDRESS - 7442) |
| LOAD PTR 9226 | SNAPPED DATA LINK (ADDRESS - 7445) |
| RETURN |
| JUMP TO 7096 | SNAPPED PROCEDURE LINK (ADDRESS - 7455) |
| JUMP TO 7142 | SNAPPED PROCEDURE LINK (ADDRESS - 7458) |

LINKAGE TABLE 2 (Ip = 7266) (HEADER)

| SIZE - 25 |
| SNT - 7676 |

DATA SYMBOLIC NAME TABLE (ADDRESS - 7676)

| DESCRIPTOR - 06H |
| LINK OFFSET - 2 |
| ENTRY POINT - 0 |
| NAME - HEADER |

| DESCRIPTOR - 05H |
| LINK OFFSET - 0 |
| ENTRY POINT - 17 |
| NAME - TITLE |

241
**LINKAGE TABLE 3** (Ip = 7102)  

<table>
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<tr>
<th>SIZE - 16</th>
<th>LOAD LP 7692</th>
<th>INCOMING LINK (ADDRESS - 7798)</th>
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<tbody>
<tr>
<td></td>
<td>JUMP TO 9650</td>
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<tr>
<td>SNT - 9713</td>
<td>LOAD LP 7692</td>
<td>INCOMING LINK (ADDRESS - 7162)</td>
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<td>JUMP TO 9650</td>
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**LINKAGE TABLE 4** (Ip = 7109)  

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<th>LOAD LP 7117</th>
<th>INCOMING LINK (ADDRESS - 7113)</th>
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<tr>
<td></td>
<td>JUMP TO 12844</td>
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<td>SNT - 16496</td>
<td>JUMP TO 7696</td>
<td>SNAPPED PROCEDURE LINK (ADDRESS - 7117)</td>
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242
DYNAMIC LINKER VERSION 1.0

THE SUM OF THE DATA ARRAY IS 4A
END OF SUM

THE PROCESS REFERENCE TABLE

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4 : NO ENTRY
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7 : NO ENTRY
8 : NO ENTRY
9 : NO ENTRY
10: NO ENTRY
11: NO ENTRY
12: NO ENTRY
13: NO ENTRY
14: NO ENTRY
15: NO ENTRY
16: NO ENTRY

243
THE COMBINED LINKAGE TABLE

**LINKAGE TABLE 1** \( (L_P = \text{7036}) \)  \( (\text{SUM}) \)

| SIZE - 25 |
| SAT - 0677 |

**UNSNAPPED INCOMING LINK**

**LOAD PTR 9683**  
**SNAPPED DATA LINK (ADDRESS - \text{7740})**

**RETURN**

**JUMP TO 7061**  
**SNAPPED PROCEDURE LINK (ADDRESS - \text{7045})**

**JUMP TO 7075**  
**SNAPPED PROCEDURE LINK (ADDRESS - \text{7052})**

**LINKAGE TABLE 2** \( (L_P = \text{7056}) \)  \( (\text{ARRAY}) \)

| SIZE - 14 |
| SAT - 0666 |

**DATA SYMBOLIC NAME TABLE (ADDRESS - \text{7062})**

- **DESCRIPTION** - \text{5H}
- **LINK OFFSET** - 2
- **ENTRY POINT** - 0
- **NAME** - ARRAY

244
<table>
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<tr>
<th>Si75 - 16</th>
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<th>( \text{INCOMING LINK (ADDRESS = 7CE75)} )</th>
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\( \text{DISPLAY} \)
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