The views, opinions and/or findings contained in this report are those of the author(s) and should not contrued as an official Department of the Army position, policy or decision, unless so designated by other documentation.

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Final Report: STIR: High Performance Thermoelectric Cryo-coolers based on II-VI Low Dimensional Structures

ABSTRACT

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Enter List of papers submitted or published that acknowledge ARO support from the start of the project to the date of this printing. List the papers, including journal references, in the following categories:

(a) Papers published in peer-reviewed journals (N/A for none)

Received Paper

05/26/2015 1.00 Charles W. Myles, Daryoosh Vashaee, Payam Norouzzadeh. Prediction of Giant Thermoelectric Power Factor in Type-VIII Clathrate Si46, Scientific Reports, (11 2014): 0. doi: 10.1038/srep07028


05/26/2015 3.00 Daryoosh Vashaee, Payam Norouzzadeh. The Effect of Multivalley Bandstructure on Thermoelectric Properties of Al x Ga1-x As, Journal of Electronic Materials, (12 2014): 0. doi: 10.1007/s11664-014-3535-1

TOTAL: 3

Number of Papers published in peer-reviewed journals:

(b) Papers published in non-peer-reviewed journals (N/A for none)

Received Paper

TOTAL:
Number of Papers published in non peer-reviewed journals:

(c) Presentations

Number of Presentations: 0.00

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Sub Contractors (DD882)
Inventions (DD882)

Scientific Progress

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Technology Transfer
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**19a. TELEPHONE NUMBER (include area code)**
(919)515-9599
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SECTION I – EXECUTIVE SUMMARY

This program performed material design for high-performance thermoelectric (TE) cooler with a figure-of-merit $ZT > 3$. The cooler is fabricated from HgCdTe using a low dimensional superlattice structure. The TE coolers can operate at temperatures ranging from 30K to 300K, thereby providing a fundamentally new and disruptive method for achieving cryogenic cooling.

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SECTION II – WORK CONDUCTED

In Phase I the program investigated the following primary tasks.

- Modeling of HgCdTe Low Dimensional Structures
- Thermoelectric Cooler Design
- Partnership with SCD.USA to integrate TEC into their new TEC FPA/Dewar

Thermoelectric Cooler Design

- Thermal conductivity of the HgCdTe SL was calculated versus materials composition and temperature. The model was benchmarked with the existing experimental data and was further extended to predict $ZT$ at cryogenic temperatures for the planned SL structure.

- The optimum parameters for SL well and barrier layer compositions versus doping concentration were calculated and the design rules were drawn. The data will be used for the growth of the SL structures in phase II.

- A three dimensional finite element model of heat transfer, Peltier cooling, and joule heating was developed. The model will be used for design optimization of the TEC modules in phase III.

Partnership with SCD.USA to integrate TEC into new FPA/Dewar assembly

- We worked closely with Amethyst Research Inc. and SCD.USA informing partnership. As part of the SCD.USA support to the Amethyst Research HgCdTe TEC development efforts, SCD.USA will design a new Integrated Dewar-Detector Cooler Assembly (IDDCA). The IDDCA will incorporate the prototype TEC into a typical Long Range thermal Imager dewar package design. After comprehensive performance/reliability analyses, a prototype IDDCA will be fabricated and evaluated. SCD.USA will use High Operating Temperature (HOT) MWIR Focal Plane Arrays (FPA) that have been developed by SCD.LTD (the parent
company of SCD.USA) for use in their low Size/Weight/Power packages. These FPAs will be mounted to ceramic carriers designed for easy integration into the IDDCA. SCD.USA will perform final IDDCA fabrication, integration and test while SCD.USA’s contractors will perform piece part fabrications. Amethyst Research will provide the TEC that will be integrated by SCD.USA to the FPA-feedthrough assembly. SCD.USA will then integrate the Cold Shield Assembly, weld the dewar assembly and complete package assembly through bake-out and seal.

**Hg$_{1-x}$Cd$_x$Te Superlattice Structures (SLS) Modeling**

In this section a modified SLS structure based on theoretical model structure calculations for HgCdTe/CdTe system on InSb substrates is presented for TEC applications. During our Phase I program there were concerns as to the thickness of the layers which would require extended growth times of 12 hours. While technically possible, this would drive up TEC production costs. Thus modeling was conducted to design TEC structures with thinner layers. The resulting HgCdTe SL structure thickness is about three times thinner than the initially proposed structure.

**Results of the Superlattice Design**

The superlattice barrier height and the optimum doping concentration were calculated for HgCdTe superlattice. The material properties of the ternary alloy Hg$_{1-x}$Cd$_x$Te are mostly available from the literature or can be approximated by virtual crystal approximation from those of the constituent binary alloys of HgTe and CdTe.

The calculation results for the n-type superlattice barrier height and the optimum doping concentrations are shown in Figure 1 and Figure 2. The SL is assumed to be uniformly doped.

![Figure 1: The required amount of Cd composition (x) is calculated for a given doping concentration to achieve the high ZT at 100K (blue) and 300K (red).](image)
Figure 2: SL barrier height is calculated versus Cd amount at 300K (red) and 100K (blue). This data will be used with the data of the previous figure to find the optimum doping concentration for the HgCdTe superlattice.

A large superlattice barrier height is required to improve the thermoelectric power factor. Since the optimum doping concentration increases with the superlattice barrier height, x is chosen so that the required doping concentration is practically achievable. A tentative superlattice is schematically shown in Figure 3.

Figure 3: The Schematic of the optimized Hg$_{1-x}$Cd$_x$Te superlattice structure with uniform doping. These new layer thickness (3/12 nm) will allow us to grow a TEC structure in 6 hours as opposed to 12 hours in previous Phase I design - significantly reducing production costs.

For x=0.8, which results in a barrier height of approximately 735meV at 300K and 830meV at 100K, the optimum doping concentration is approximately $3.3 \times 10^{19}$ cm$^{-3}$ at 300K and $5.7 \times 10^{19}$ cm$^{-3}$ at 100K.
The following tolerances of the superlattice parameters must be maintained:

- Composition: ±10%
- Well and barrier widths: ±10%

If the target doping concentration turns out to be too large to reach experimentally, we will choose a smaller x value based on the measured value of the highest practically achievable doping concentration. For example, for x=0.6, the optimum doping concentrations are 7.6×10^{18} cm^{-3} at 300K and 1.5×10^{19} cm^{-3} at 100K.

**Thermal Conductivity of the HgCdTe Superlattice**

The lattice thermal conductivity \( \kappa_{\text{lattice}} \) can be estimated from the relation:

\[
\kappa_{\text{lattice}} = \frac{1}{3} C_v \nu_s d
\]

where \( C_v \) is the specific heat at constant volume, \( \nu_s \) is the sound velocity, and \( d \) is the mean free path of the phonons. In superlattices, in addition to the phonon-phonon scattering which dominates bulk phonon relaxation time, phonons are also scattered by interfaces. Therefore, the calculation of thermal conductivity is complicated in SLs. To calculate phonon MFPs of an infinite superlattice at various temperatures, the lattice dynamics approach is employed. This first-principles approach relies on the use of harmonic and anharmonic interatomic force constants derived from density-functional perturbation theory (DFPT), which yields accurate interatomic force constants, and a solution of the Boltzmann transport equation to predict the lattice thermal conductivity.\(^1\) This approach leads to excellent agreement between experiments and theory for perfect crystals\(^2\) as well as alloys.\(^3\) Application of this approach to calculate the lattice thermal conductivity of ideal Si/Ge SuperLattices (SLs) with perfect interfaces showed that the thermal conductivity was estimated larger than the experimental values, which is a sign for the need of incorporation of the interfacial roughness effects.\(^4\) Using perturbation theory or the well-known Fermi Golden rule, one can derive the expression of the relaxation time as a function of the cubic force constants.\(^5,6,7\) The calculated phonon relaxation times multiplied by the sound velocity gives us the phonon mean free paths. The more detailed version of aforementioned formula can be expressed as:

\[
k_{\alpha} = \frac{\hbar^2}{NVk_B T^2} \sum_\lambda c_{\alpha\lambda}^2 \omega_\lambda^2 \bar{n}_\lambda (\bar{n}_\lambda + 1) \tau_\lambda
\]

where \( c, \omega, \bar{n}, \) and \( \tau \) are the phonon group velocity, frequencies, phonon equilibrium population, and relaxation times, respectively. \( \lambda \) represents the vibrational mode. \( T, V, \) and \( N \) are temperature, unit cell volume, and size of the k-point mesh used in calculations. The scattering rate, \( 1/\tau_\lambda \), of a phonon mode \( \lambda \) is taken to be the sum of two terms 1) due to the interfacial roughness and 2) due to anharmonic scattering. These two terms calculated using the following relations respectively:

\[
\frac{1}{\tau_{1\lambda}} = \pi \sum_{\lambda'} |V_3(-\lambda, \lambda', \lambda'')|^2 \times |2(\bar{n}_{\lambda'} - \bar{n}_{\lambda''}) \delta(\omega(\lambda) + \omega(\lambda') - \omega(\lambda'')) + (1 + \bar{n}_{\lambda'} + \bar{n}_{\lambda''}) \delta(\omega(\lambda) - \omega(\lambda') - \omega(\lambda''))|
\]
where $V_3(-\lambda, \lambda', \lambda'')$ is the weighted Fourier transforms of the cubic force constants; and

$$\frac{1}{\tau_{\lambda^2}} = \frac{\pi}{2N} \omega_0^2 \sum_{\lambda} \delta(\omega(\lambda) - \omega(\lambda')) \sum_{\sigma} g(\sigma) |\zeta(\sigma|\lambda')\zeta(\sigma|\lambda)|^2$$

in which $\sigma$ is the atomic site in the unit cell, g is the magnitude of the mass disorder and e is vibration eigenvector. According to the theory of atomic displacement parameter (ADP) and lattice thermal conductivity, the mean square displacement amplitude, $<u^2>$, of a quantized harmonic oscillator is given by:

$$U_{iso} = <u^2> = (h/8\pi^2 m\nu) \coth(h\nu/2k_BT)$$

where m is the reduced mass of the oscillator, h is the Planck’s constant, $\nu$ is the frequency of the vibration, $k_B$ is the Boltzmann constant, and $U_{iso}$ is the mean isotropic displacement parameter which measures the mean-squared displacement amplitude of the atom averaged over all directions. At high temperatures where $h\nu \leq k_BT$, the preceding equation reduces to $U_{iso} = k_BT/K$ and $K = m(2\pi \nu)^2$ where K is the force constant of the oscillator.

The Debye’s temperature, $\theta_D$, is estimated from the slope of the curve drawn between the $U_{iso}$ at various temperatures. Each slope of the curves can be described by the relation:

$$slope = \frac{3h^2}{(mk_B\theta_D^2 \cdot 4\pi^2) \cdot (mk_B\theta_D^2 \cdot 4\pi^2)}$$

The slope is estimated from the curve that is extrapolated to the origin. Static disorder that tends to shift these curves upward is ignored. The Debye’s temperature, $\theta_D$, is used to determine the average velocity of sound $v_s$ and is given by:

$$v_s = \frac{2\pi \theta_D k_B}{h(6\pi^2 n)^{\frac{1}{3}}}$$

where n is the number of atoms per unit volume.

The vibrational Helmholtz free energy ($F_{vib}$), vibrational entropy ($S_{vib}$) and specific heat at constant volume ($C_v$) can be calculated from vibrational density of states (VDOS). In the harmonic approximation, the vibrational Helmholtz free energy is given by:

$$F_{vib}(T) = k_B T \int_0^{\infty} \left[ \frac{1}{2} h\omega + k_B T \ln(1 - e^{-h\omega/k_BT}) \right] g(\omega) d\omega$$
where \( k_B \) is the Boltzmann constant. The VDOS is normalized such that \( \int g(\omega) d\omega = 3N \), where \( N \) is the number of atoms. The zero point vibrational energy is defined by \( F_{\text{vb}} \) at \( T = 0 \)K. The specific heat at constant volume is given by:

\[
C_v(T) = -T \left( \frac{\partial^2 F_{\text{vb}}}{\partial T^2} \right)_V.
\]

In this way, all ingredients necessary to compute the thermal conductivity, namely the phonon frequencies, group velocities, populations and lifetimes are derived from first-principles using density functional perturbation theory (DFPT).

**Thermal conductivity of HgCdTe superlattice**

The total thermal conductivity in TE materials consists of three parts: the transport of heat by (1) phonons (i.e. the lattice thermal conductivity) (2) free electrons and holes (i.e. the electrical thermal conductivity) and (3) electron-hole pair diffusion (i.e. the bipolar diffusion thermal conductivity). The relative strength of a given heat conduction mechanism in a semiconductor material depends on the temperature range, the band structure, and the doping level. At below room temperature, the bipolar part of the thermal conduction is often very small and the electronic and lattice parts become dominant.

The proposed HgCdTe SL cooler is a cross plane device and as such the heat conduction is perpendicular to the SL interfaces. Phonon reflection, confinement, and also diffuse scattering can significantly reduce the cross-plane thermal conductivity. Since the HgCdTe SL is lattice matched, the reflection coefficient dominantly depends on the difference in the unit cell masses in the well and barrier regions. The large deviation of unit cell masses in well and barrier regions of the tall barrier HgCdTe SL (due to large difference in \( x \) value) would satisfy the condition for enhanced phonon reflection at SL interfaces.

In Figure 4 we have calculated the thermal conductivity of the prototype \( \text{Hg}_{0.8}\text{Cd}_{0.2}\text{Te} / \text{Hg}_{0.2}\text{Cd}_{0.8}\text{Te} \) superlattice in the cross plane as compared with that of \( \text{Hg}_{0.8}\text{Cd}_{0.2}\text{Te} \) alloy. Our model calculation agrees with the experimental data of the alloy\(^{10}\).

To calculate the lattice part of thermal conductivity we used the relaxation time approximation in Boltzmann’s equation for phonons based on the Debye model.\(^{11}\) The model calculation takes into account three-phonon scattering, including normal (N) and umklapp (U) processes, electron-phonon scattering, point defect (alloy) scattering, and SL boundary phonon scattering. Defects at interfaces can introduce different types of scattering of phonons depending on the type and size of the defect. MBE grown superlattices usually have better interface morphology than the other types of interfaces. However, even for the best material systems such as MBE grown GaAs/AlAs, the interfaces are not perfect. Interface mixing and regions with monolayer thickness variations exist at the interface. Such imperfections cause diffuse scattering at SL interfaces resulting in further reduction of the thermal conductivity.\(^{12}\) We may group interface scattering into three different types: (1) regular reflection and refraction scattering when the phonon wavelength (\( \lambda \)) is small compared with the linear dimensions of the SL interface,\(^{13}\) (2) diffusive scattering if \( \lambda \) is comparable with defects dimension \( \ell \)\(^{14}\) or (3) Rayleigh scattering if \( \lambda \gg \ell \).\(^{15}\) We expect regular reflection and refraction scattering at SL interface be the dominant scattering mechanism. Our model calculations as depicted in Figure 10 predicts a 2-3 fold reduction in thermal conductivity
of the SL structure over the entire range of 100K to 300K, which agrees with the trend of extensive recent experimental data on the thermal conductivity of various superlattices in recent years.\textsuperscript{16,17}

Figure 4: Thermal conductivity of (a) single crystalline Hg\textsubscript{0.8}Cd\textsubscript{0.2}Te alloy and (b) Hg\textsubscript{0.8}Cd\textsubscript{0.2}Te/ Hg\textsubscript{0.2}Cd\textsubscript{0.8}Te SL. Lines are our model calculations and symbols are experimental data \cite{10}. The SL thermal conductivity is significantly smaller than that of the bulk material especially at low temperature. This would enhance the ZT even further than our previous prediction which was based only on the enhancement of the TE power factor.

\textbf{Multi Stage Cryo Cooling}

The thermoelectric properties of the MCT SL are a function of temperature and doping concentration. For a given working temperature, the SL must be doped to an optimum value to reach the maximum ZT of the material.

Figure 5 shows the calculated ZT of the MCT SL versus temperature for several values of doping concentration. It can be seen that a multi-stage cooler (here five stages) is required to make a solid state cooler that can cool from room temperature down to 30K. With five stages of cooling the ZT remains above one for the entire range of temperature. A doping variation from 3.4×10\textsuperscript{19} cm\textsuperscript{-3} to 1.82×10\textsuperscript{19} cm\textsuperscript{-3} changes the peak ZT from 300K to 30K. Therefore, a precise control of the doping concentration is required to tune the location of the peak ZT.

It is possible to reduce the cooling even to lower temperature by further increasing the doping concentration. For example, doping concentration of 3.6×10\textsuperscript{19} cm\textsuperscript{-3} will result in ZT peak at approximately 10K. For cryogenic application, a five stage cooler can cool from 300K down to under 50K.

It is also seen that as the temperature reduces, the ZT peak becomes narrow indicating that the cooler is efficient over a narrower range of temperature. However, the peak ZT increases as the temperature reduces. ZT>4 is predicted at T<30K.
Figure 5: Thermoelectric figure of merit of HgCdTe SL versus temperature for different values of doping concentration. A five stage cooler is required to cool from room temperature down to 30K. A two stage cooler can cool from 90K to 30K (blue and green curves). A five stage cooler can cool from 300K to below 50K.

This value of ZT is obviously a ground-breaking value especially at such a low temperature. The ZT increase at low temperature is the result of narrowing of the carrier distribution that would allow more effective filtering of the high energy carriers by optimizing the location of the Fermi energy with respect to the top of the SL energy barrier [18,19]. At higher temperature, the carriers are broadened around the Fermi energy and the SL potential barrier would reflect a large number of the carriers that would reduce the number of carrier, hence, the electrical conductivity.

The required thickness of the SL structure will be determined by model calculations and comparison with experimental data. In order to determine the feasibility of the required thickness, a three dimensional single leg HgCdTe cryo-cooler was implemented in COMSOL Multiphysics described by coupled heat and Poisson’s equations. These equations were extended by the thermoelectric effects and were solved simultaneously to get the solution for independent variables of temperature and voltage. The cooler consisted of one n-type HgCdTe element with cross section are of 50×50 μm² in size. The SL is contacted by two copper electrodes 1μm in thickness. The contact resistance was included at the interface of the SL and the metal contact: therefore a 5-stacked layer of different materials with temperature dependent thermoelectric properties were modeled. Adiabatic boundary conditions were taken on the surrounding surfaces.

Figure 6-(a) shows the calculated temperature distribution along the TE leg. Figure 6-(b) shows the temperature difference at 30K for two SL thickness of 15 μm and 50 μm. A heat load of 1200 W/m² was applied at the cold side. The plot shows the optimum working current for both SLs is approximately 0.12 A. It can be seen that while the 15 μm SL cools by ~50C, the 50 μm SL can cool down by ~100C. This difference is mainly associated with the effect of joule heating at the metal-SL interface. This effect is demonstrated in Figure 6-(c) which shows the maximum temperature difference versus the SL thickness for three different values of ohmic contact.
resistances ($\rho_{oc}$). It can be seen that only in the limit of $\rho_{oc}=10^{-8}$ cm$^2$ the thickness has small effect on the temperature difference. This value of ohmic contact is often practically too small to reach; however, with a well-designed metal contact, values in the range of $\rho_{oc}=10^{-7}$ cm$^2$ are achievable. Although, even with a typical $\rho_{oc}=10^{-6}$ cm$^2$ a 15µm thick SL can cool for 50 degrees. Our model calculations show similar trend at higher temperature up to the room temperature. Therefore, with 50 degrees cooling across the SL, a three-stage cooler can cool from 300K to 150K.

![Diagram of temperature distribution](image)

**Figure 6:** (a) The resultant temperature distribution of an n-type HgCdTe superlattice thermoelectric leg which is contacted by copper electrodes. (b) The temperature difference versus applied current. The optimum current is 0.12A per leg. (c) Maximum temperature difference versus the thickness of the HgCdTe SL for three different values of ohmic contact resistances. A low ohmic contact resistance in the range of $\sim10^{-7}$ Ωcm$^2$ is required to achieve a large $\Delta T$ across a 15 µm thick HgCdTe SL.
**TEC Design**

The fabrication of the TEC involves several steps that do not usually exist in fabrication of conventional microelectronic devices. The detailed fabrication steps will be presented in the following phases of the project. However, the new proposed structure is shown in Figure 7, which is a drawing of the final cryo-cooler structure that will be fabricated in Phase II+ of the program. The TEC will consist of multiple stages. A five-stage cooler can cool from 300K to below 50K for the cryogenic application. To cool from 300K to 150K, which is for the FPA applications, a three stage cooler is also required as the ZT peak is broadened at high temperature, which means that each stage can cool over a wider range of temperature. We will collaborate closely with Marlow Industries in the integration of the multi-stage cooler. Marlow is producing multi-stage coolers for above room temperature application. The concept and techniques for multi-staging is similar and their technology can be adapted for our purpose. Marlow with 40 years of business is one of the largest manufacturers of thermoelectric modules in the world. We have signed the NDA with Marlowe and have an ongoing research project on thermoelectric materials with them that can be integrated with the proposed research.

Figure 7: Schematic of the milestones required and final thermoelectric cooler (TEC) planned for the Phase II and Phase II+ program. (a) n and p type doping HgCdTe, (b) TEC fabrication steps and design concept from n and p-type low dimensional HgCdTe superlattice structure. (c) Single (left) and multi-stage (right) TEC device. In order to cool from 90K to operation at 10K a three stage TEC will be required.

**Strategic Partnership with SCD.USA**

Amethyst has recently entered into a development agreement with SCD.USA to develop these ultra high performance HgCdTe TEC coolers for their IR focal plane arrays. This will generate considerable market pull and a commercial outlet for the proposed structure. This cooler will also be available to other military users.
SCD.USA Activities

Development of TEC Integrated HOT MWIR detector for Tactical applications

Due to strong demand for improved MWIR detector performance, development of cooled technology will bring new capabilities for infrared imaging. Proposed TEC technology is four fold, specifically TEC development, dewer development, ROIC and FPA development, and package development. Ongoing work on development of TEC technologies to raise the efficiency of coolers by raising the ZT factor from 1 to 3 using SL structure is the first step of the proposed work. SCD will design new TEC compatible dewer to minimize the size without compromise to the image quality. SCD also works on existing ROIC/FPA to minimize existing ROIC/FPA power dissipation to reduce active heat load and optimize uncorrected uniformity at highest feasible operating temperature. For further reductions in heat load by replacing traditional wire bonds with low Tc interconnect technology, we need new package development. The improvements in TEC technology will add new features and capabilities to develop highly innovative MWIR products.

Figure 8 shows the schematics of the dewar design concept for the two-stage cooler. In future work with SCD we are proposing two stage cooler integrated into the MWIR detector. These two-stage coolers will provide significant improvement in the high temperature differentials hence allows operating the device at lower temperatures than conventional single-stage modules.

Figure 8: Schematic of the SCD.USA dewar design for the proposed two-stage cooler for a new generation of TEC FPA’s.

These are preferable option where operating temperature is around 210-250K and where the requirement of noise reduction and improving the signal resolution is crucial, such as in case of infrared detectors.

As part of the SCD.USA support, they will design a new Integrated Dewar-Detector Cooler Assembly (IDDCA). The IDDCA will incorporate the prototype TEC into a typical Long Range
thermal Imager dewar package design. After comprehensive performance/reliability analyses, a prototype IDDCA will be fabricated and evaluated. SCD.USA will use High Operating Temperature (HOT) MWIR Focal Plane Arrays (FPA) that have been developed by SCD.LTD (the parent company of SCD.USA) for use in their low Size/Weight/Power packages. These FPAs will be mounted to ceramic carriers designed for easy integration into the IDDCA. SCD.USA will perform final IDDCA fabrication, integration and test while SCD.USA’s contractors will perform piece part fabrications. Amethyst Research will provide the TEC that will be integrated by SCD.USA to the FPA-Feedthrough assembly. SCD.USA will then integrate the Cold Shield Assembly, weld the dewar assembly and complete package assembly through bake-out and seal. After Proxy electronics integration, SCD.USA will then perform Radiometric Characterization of the FPAs at the temperatures achieved with the integrated prototype TEC. Finally we will integrate the prototype IDDCA with appropriate optics and electronics to provide a camera system that can be used to demonstrate the capabilities of the technology. The camera system will include an opto-mechanical mounting platform (i.e. optical bench), lens mount, lens, interface electronics, computer based video data storage and image quality evaluation software. SCD.USA has provided similar detector evaluation & test systems to customers over the past few years. Figure 19 shows the expected steps in the detector development and test. SCD.USA can also utilize its in-house environmental test capability to evaluate IDDCA reliability.

SECTION III – DISSEMINATION OF RESULTS

Four journal articles were published based on the multi-scale computational tools developed through the course of this program. Publications are listed below:


